

Fabrication Notes:

Unspecified Components:

D1, D2, D3 and D4 were not able to be specified by the automatic BOM uploader. Please use the following components:

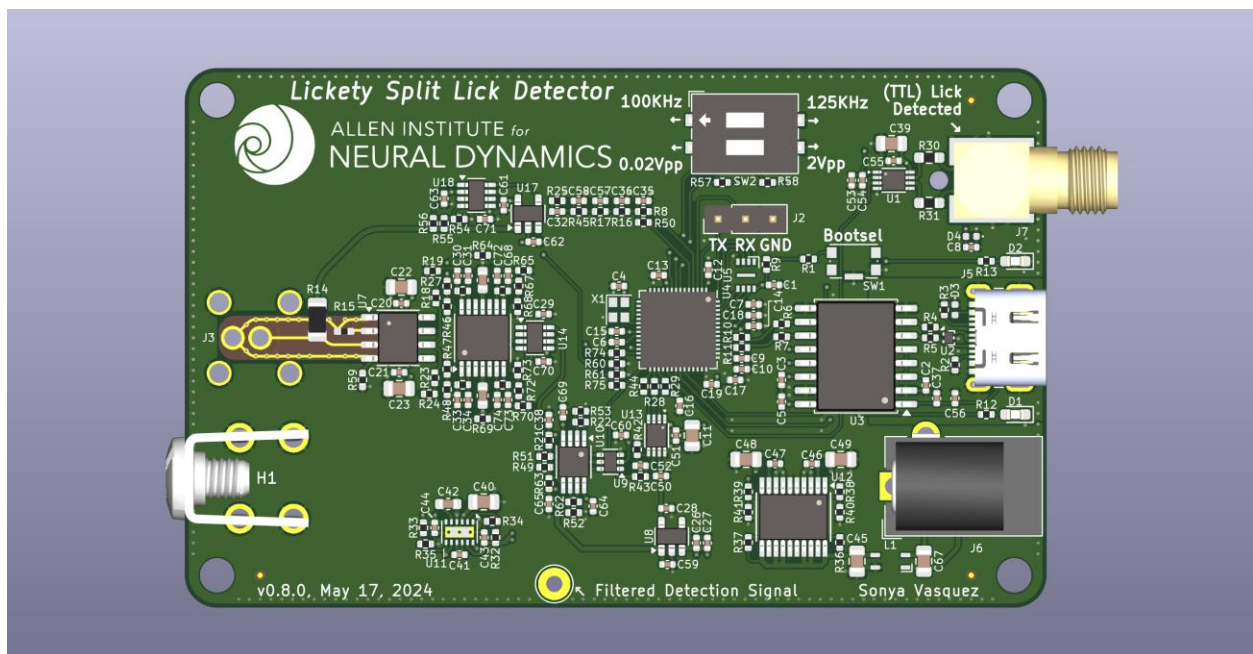
- D3, D4 should be replaced with ESD7571N2T5G.
- D1, D2 should be replaced with APT1608LZGCK.

Silkscreen Feature Size Limits:

Some silkscreen features are smaller than the suggested silkscreen feature size. Proceed as-is with these features.

Connector Orientations:

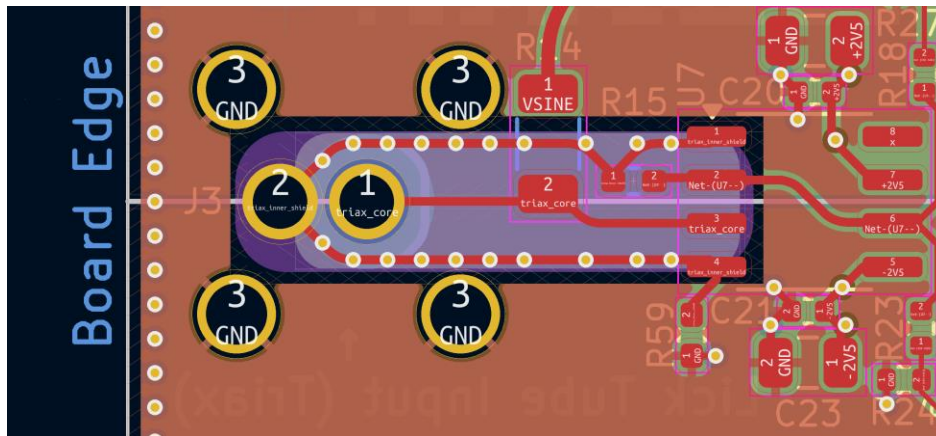
H1 and **J7** should be placed facing outwards as show below:



Soldermask Cutouts:

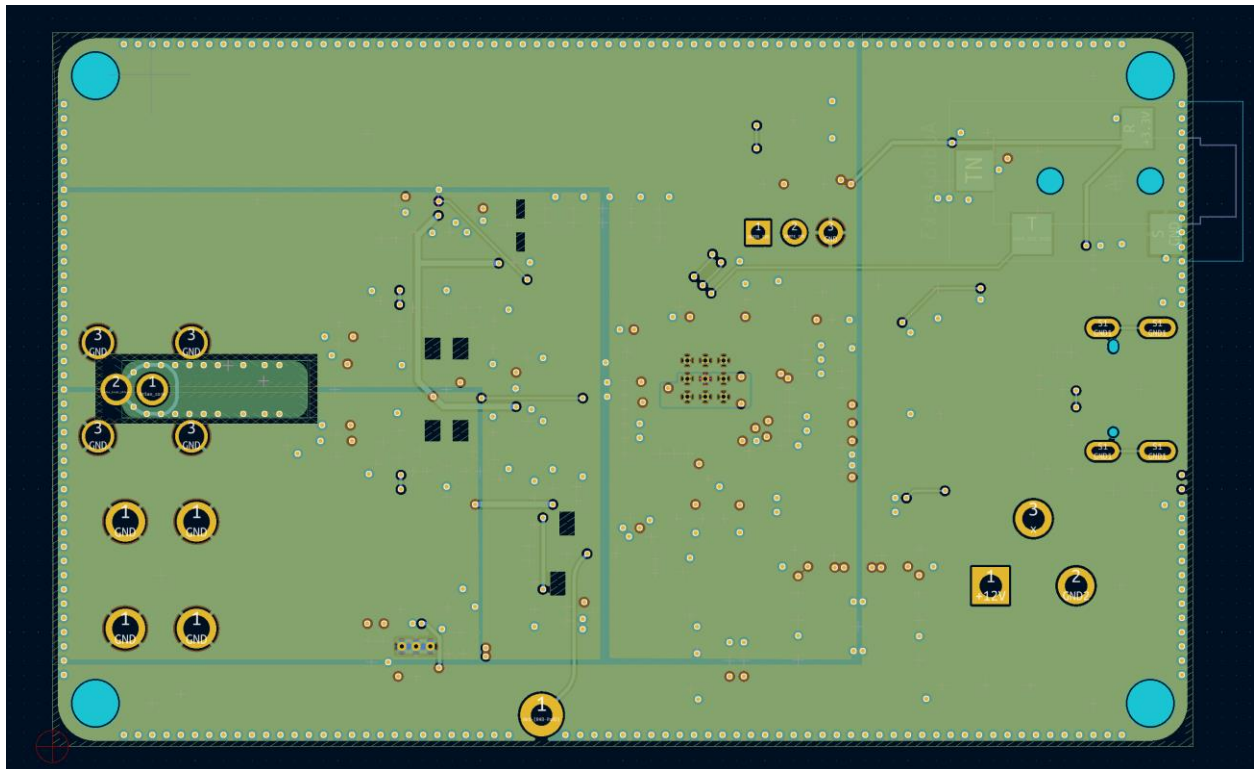
The following soldermask omissions are intentional.

This section has an intentional soldermask removal on top and bottom soldermask layers.



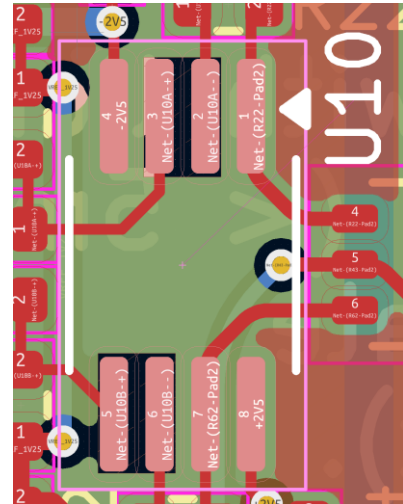
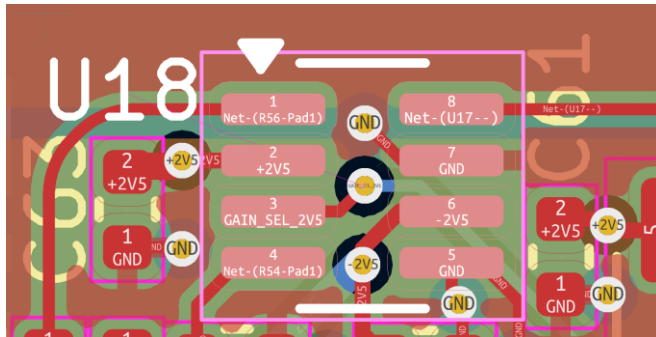
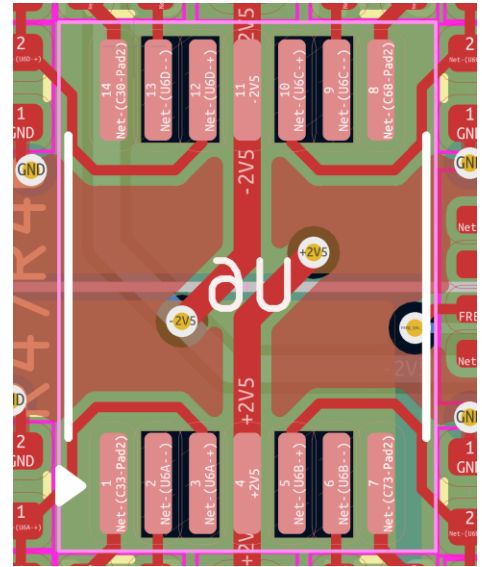
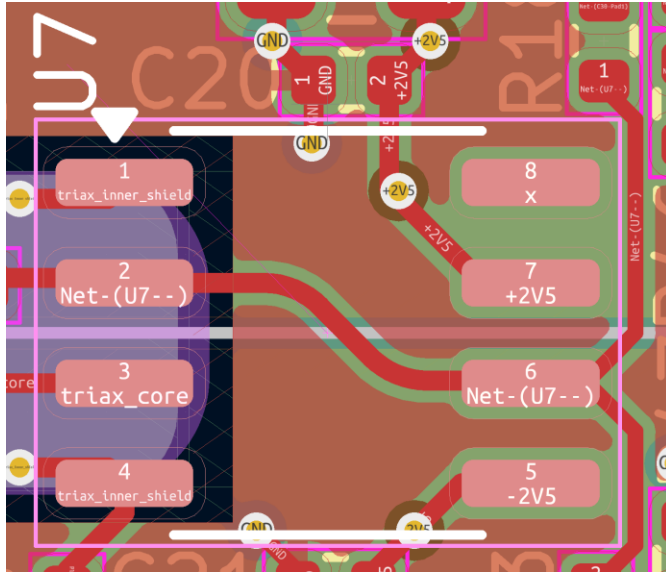
Copper Pour Cavities:

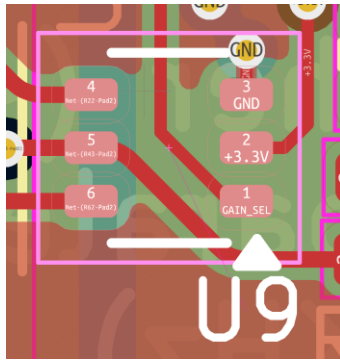
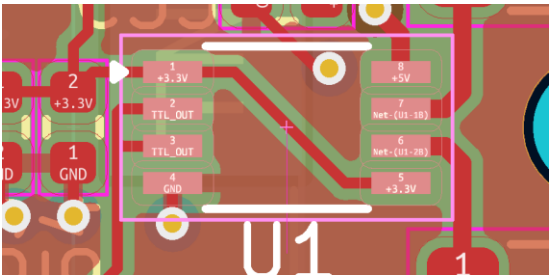
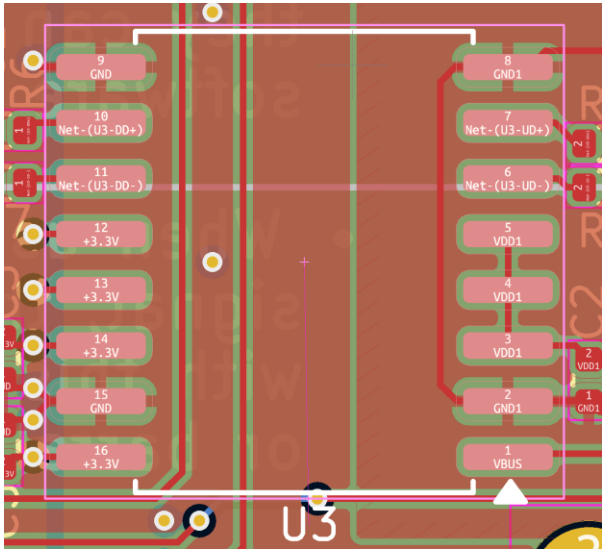
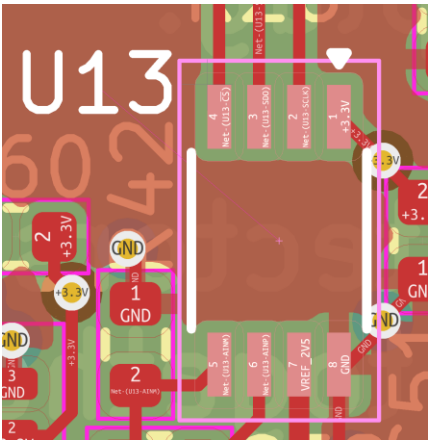
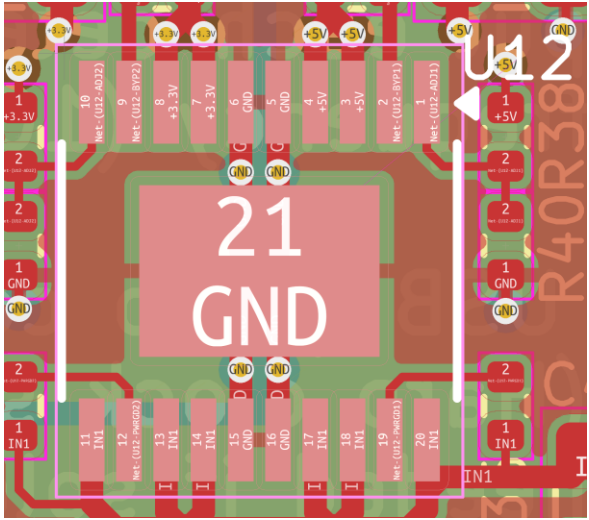
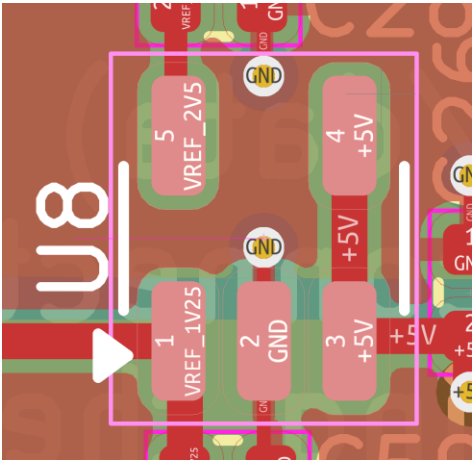
These 8 rectangular cavities in the copper pour on all four layers under **U6**, **U10**, and **U17** are intentional.

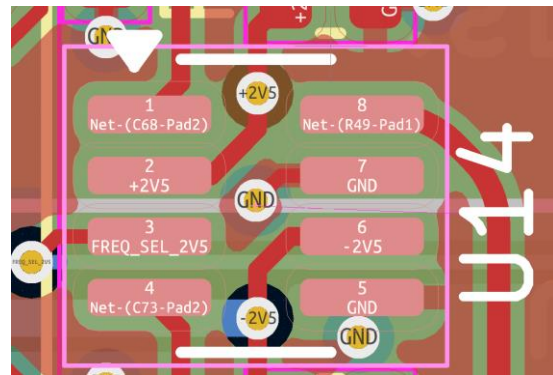
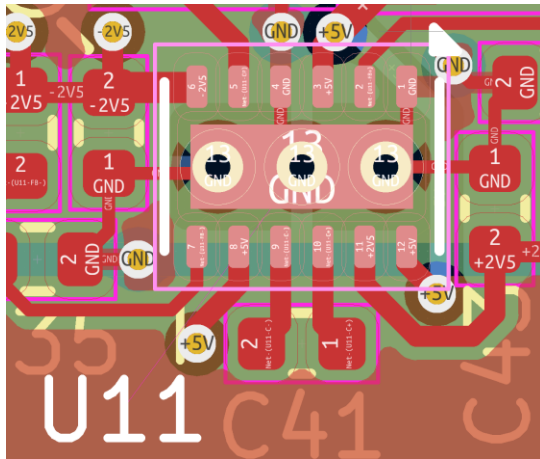


Pin 1 Markers:

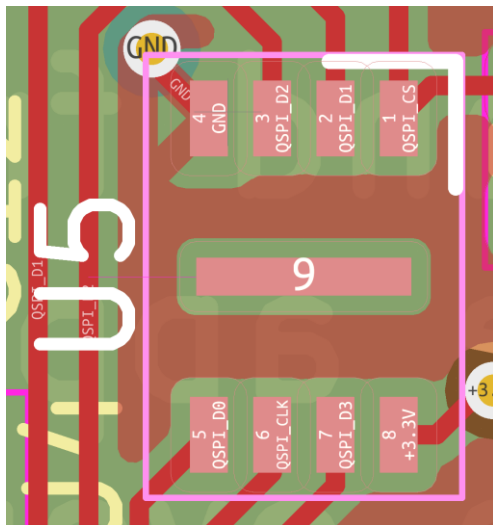
Arrows in the silkscreen indicate Pin 1 on **U7**, **U6**, **U18**, **U10**, **U8**, **U12**, **U13**, **U3**, **U1**, **U9**, **U11**, and **U14**.



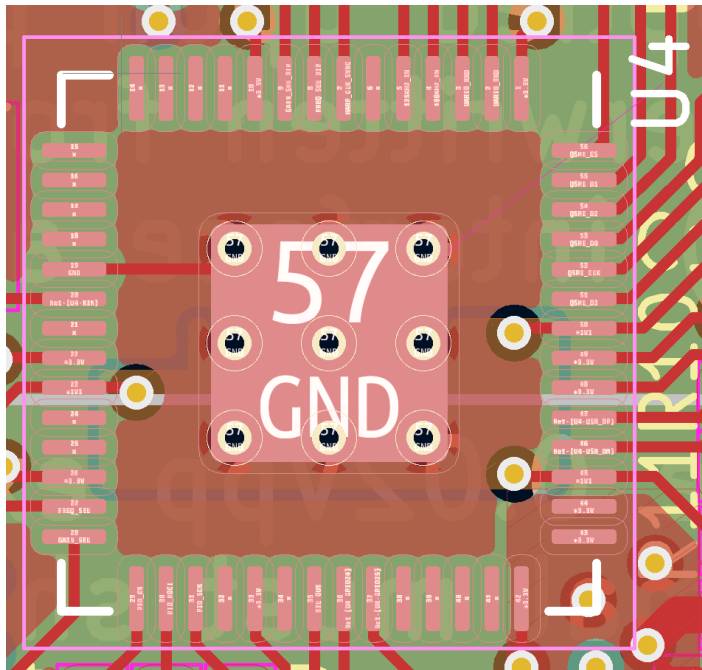




U5 pin-1 is indicated by the “L” in the silkscreen (upper right).



U4 pin-1 is indicated by the band (upper right).



Diode Cathode Markers:

D1, and **D2** cathodes are indicated by the band.

