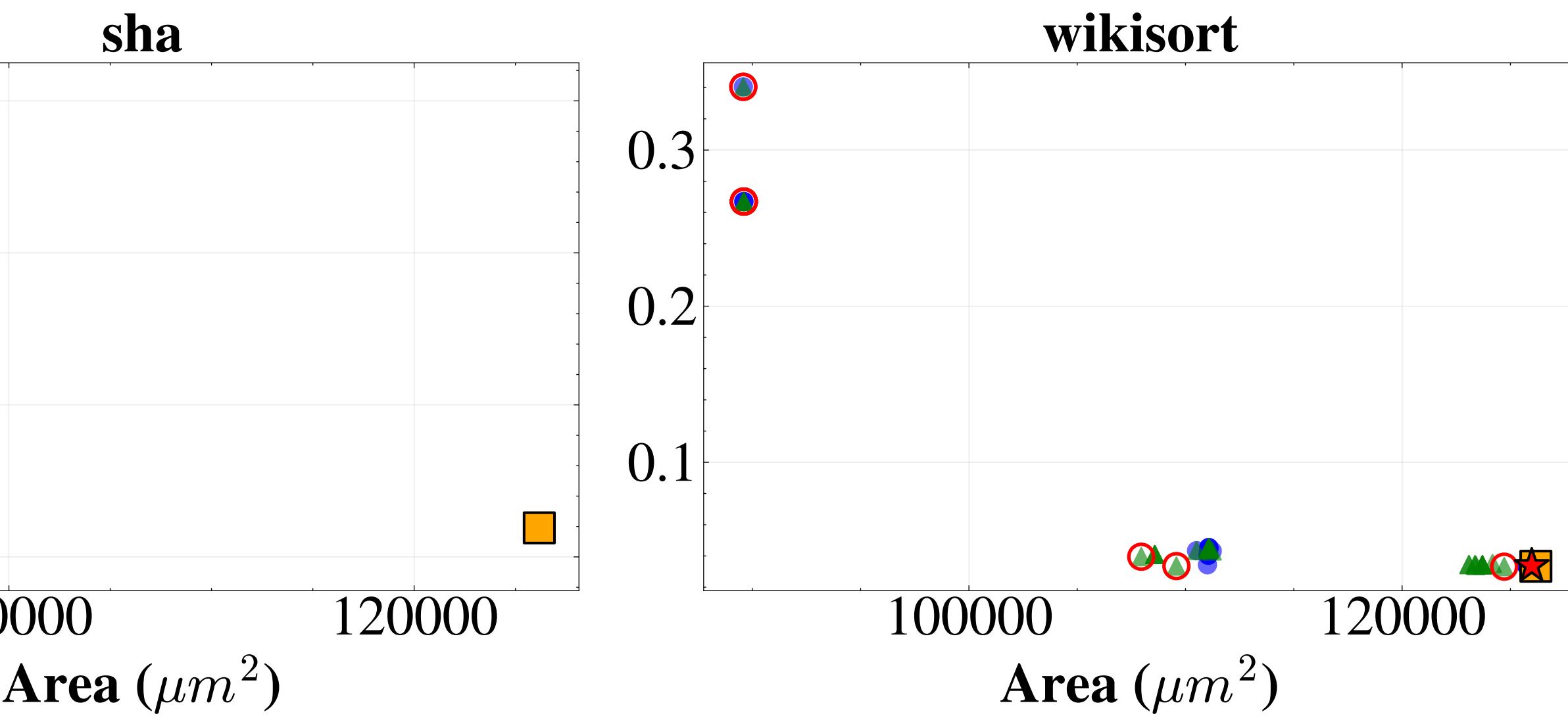
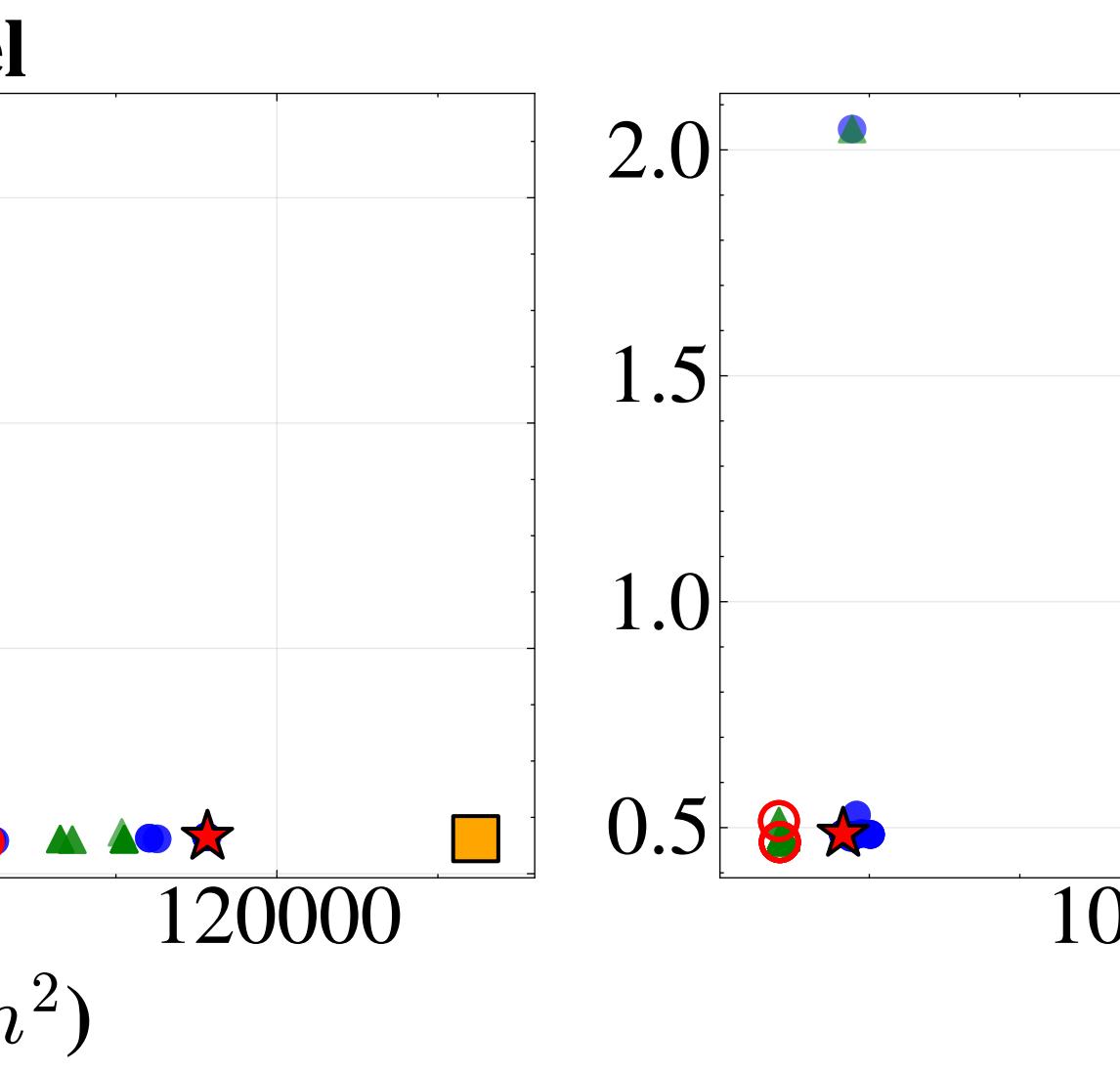
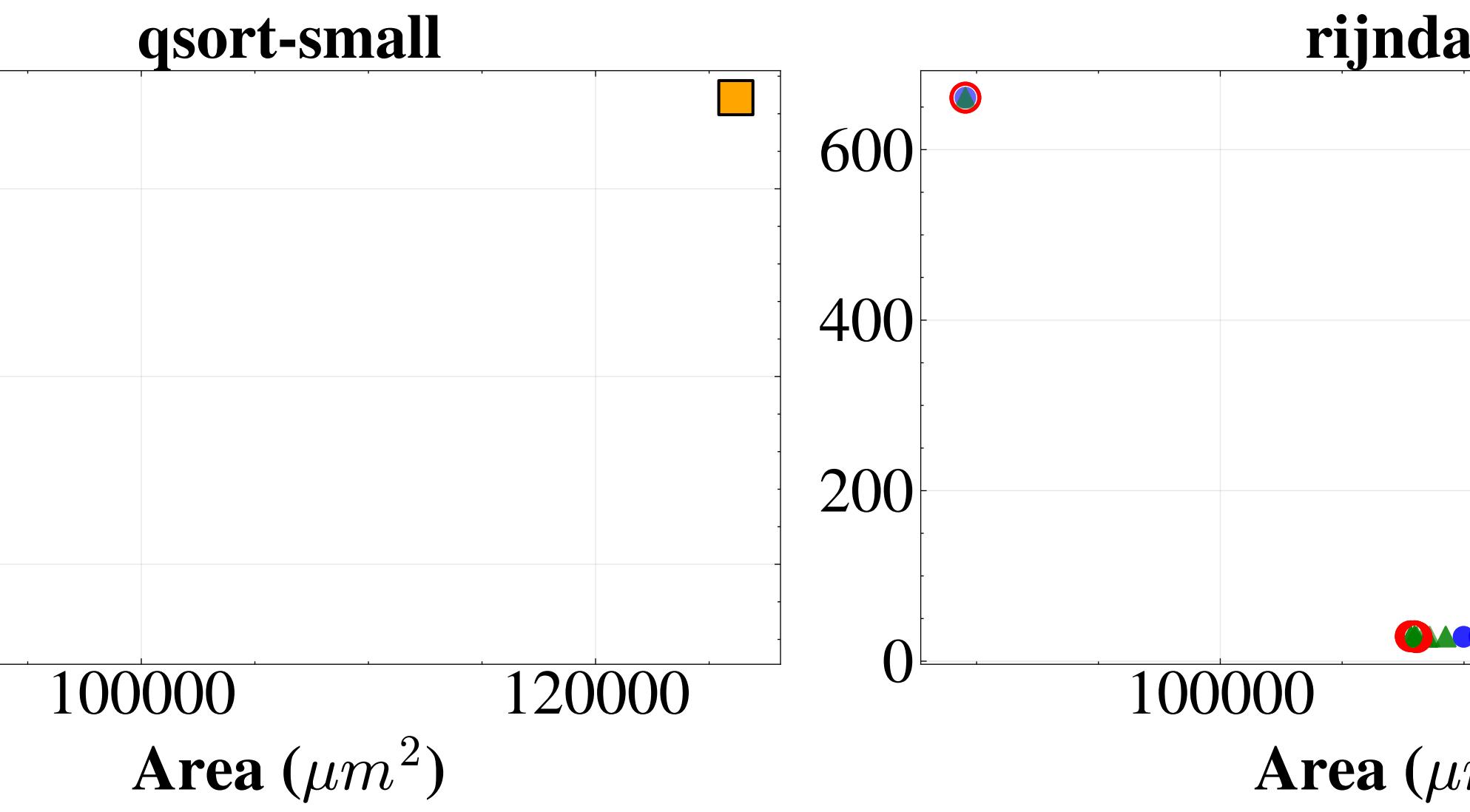
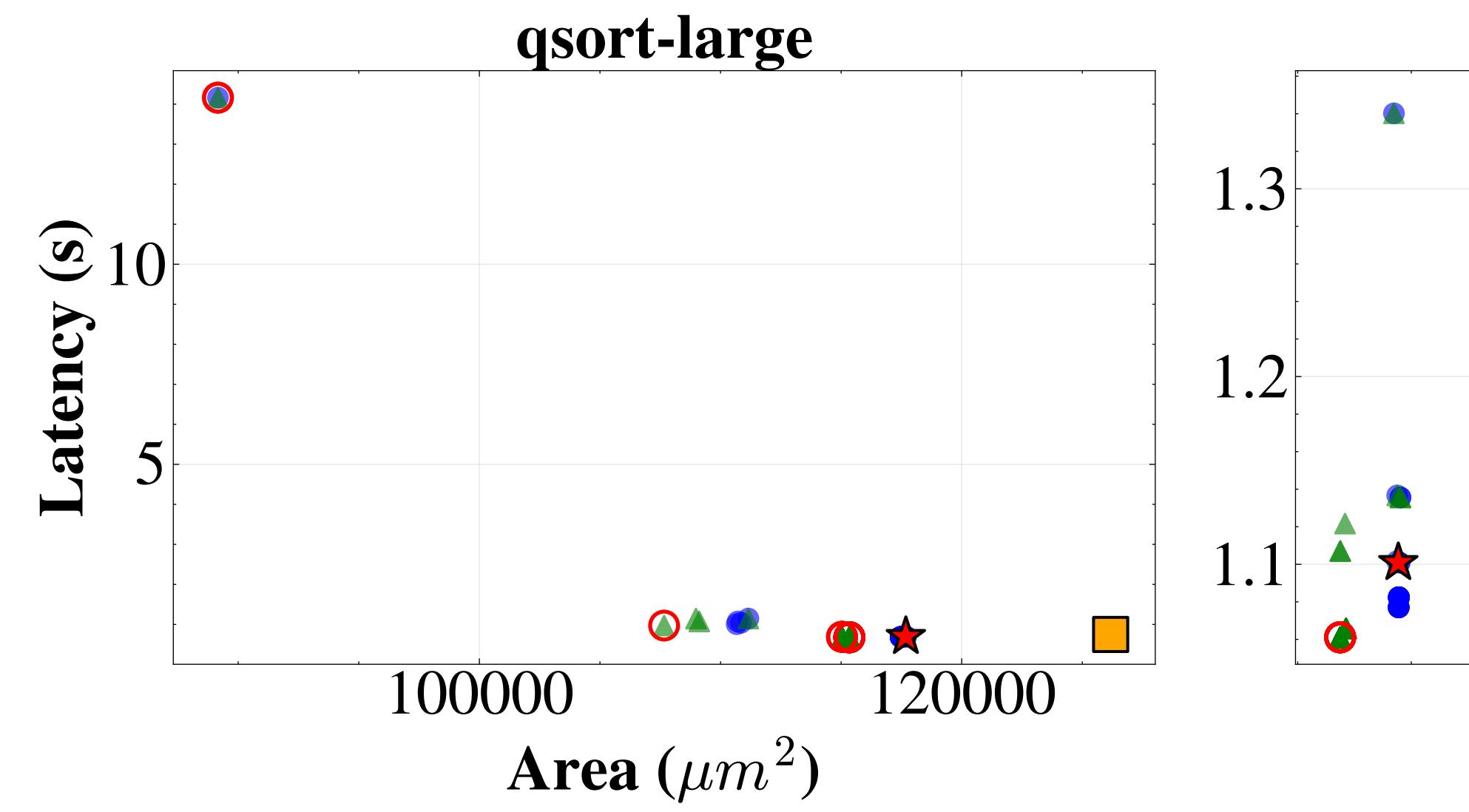
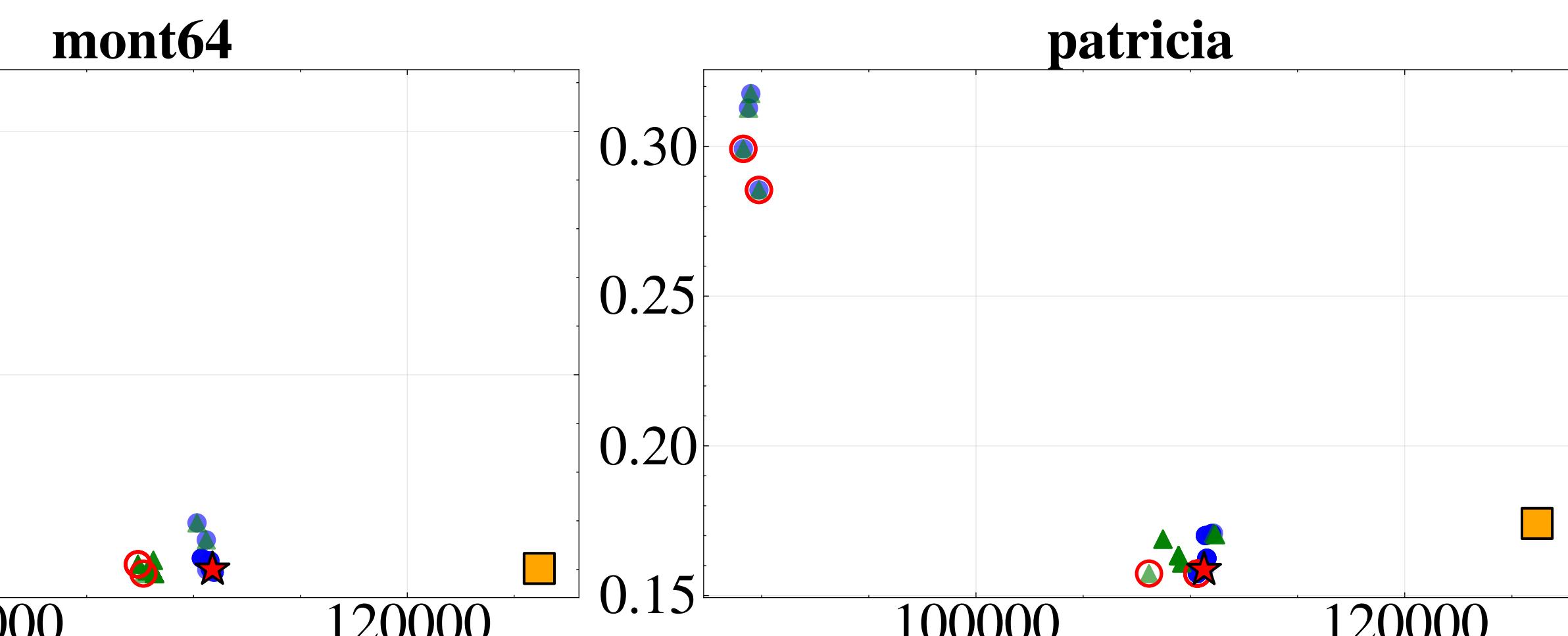
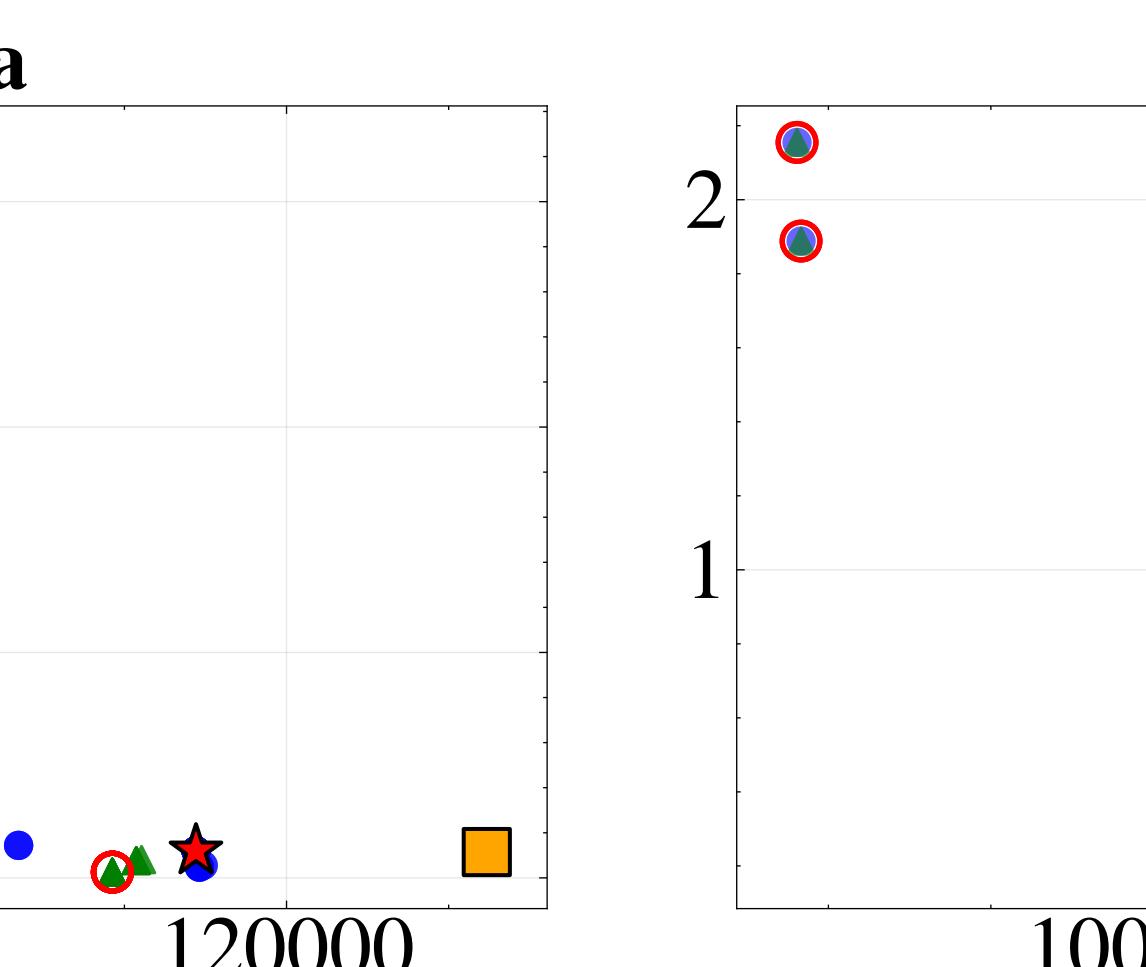
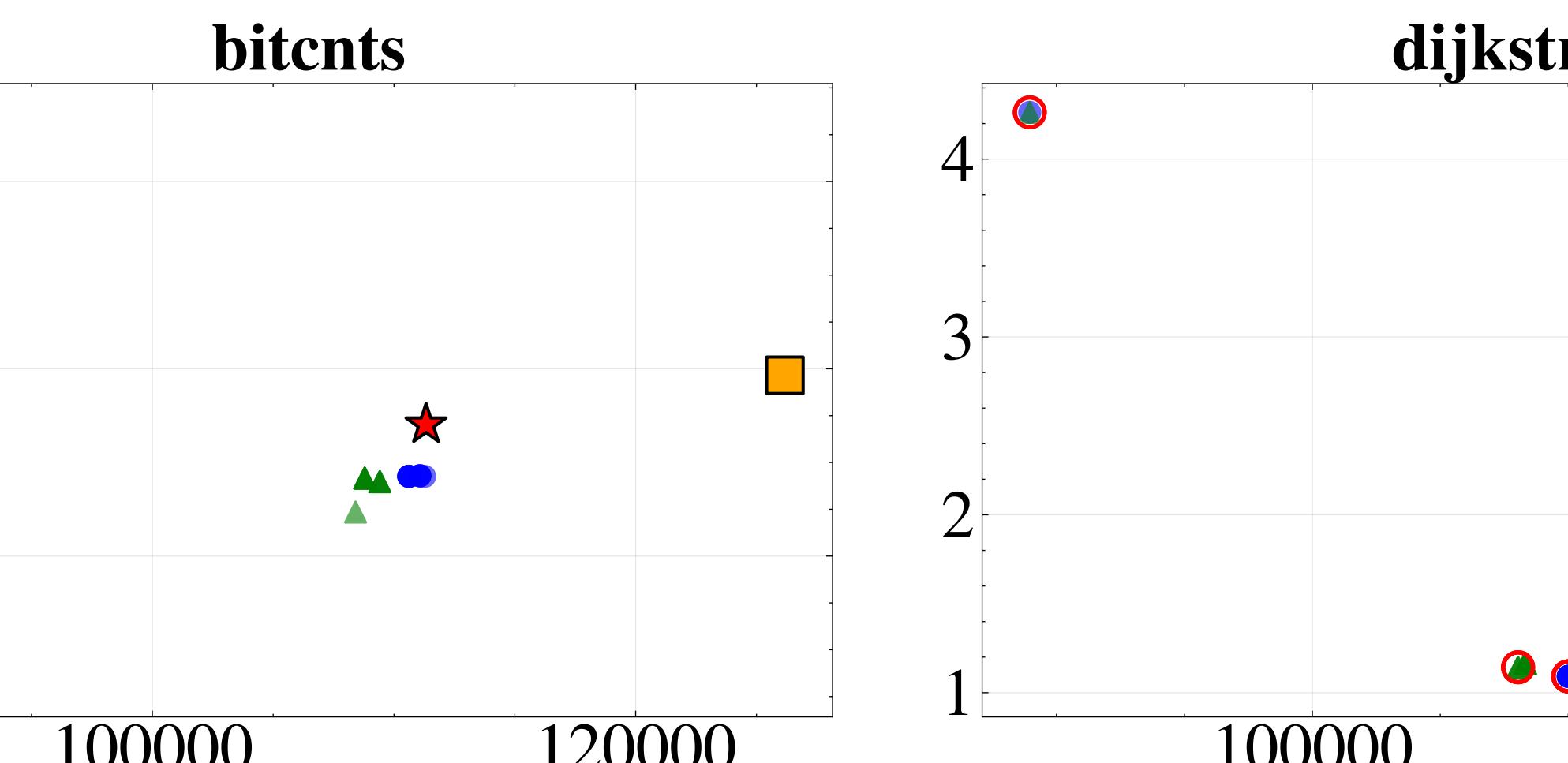
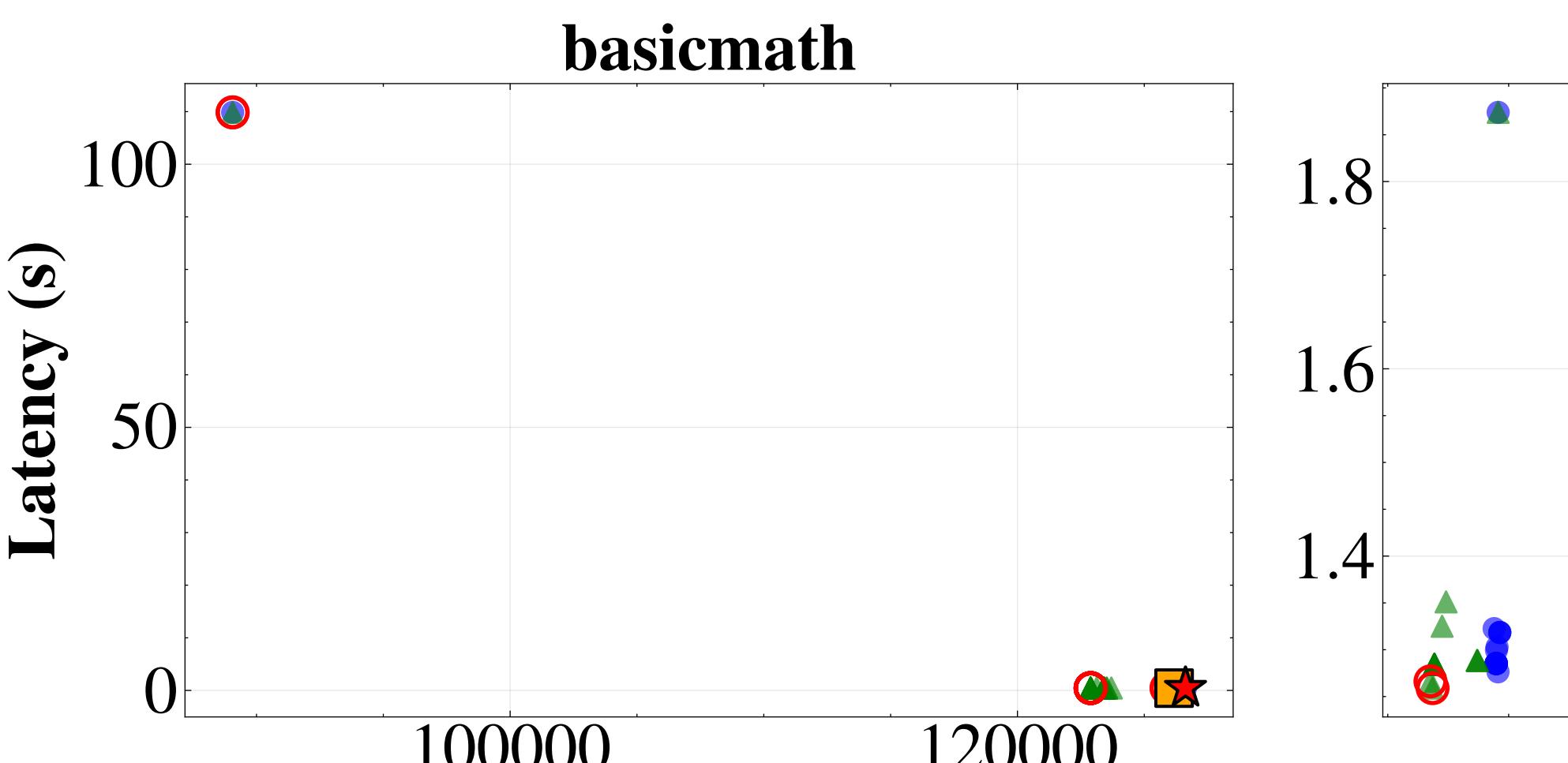


• aeSIP: ISA level constraint ▲ aeSIP: uarch aware constraint ■ Baseline Ibex (RV32IM) ★ PDAG ○ Pareto frontier



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