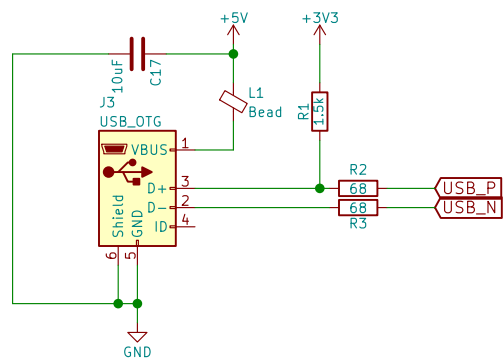
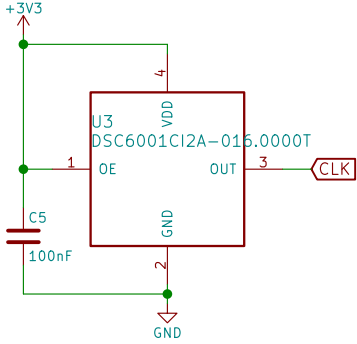


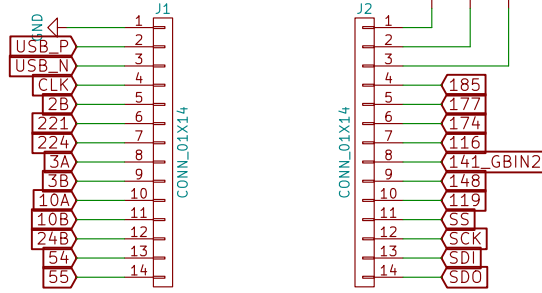
USB connection and power filtering



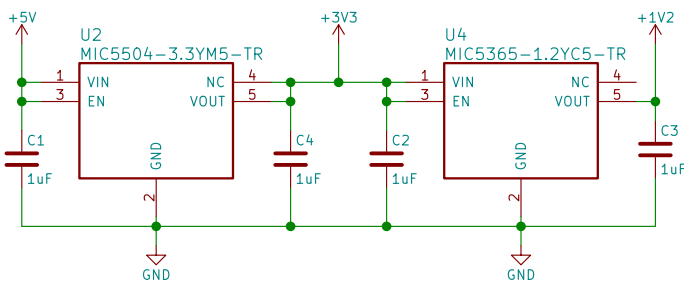
16MHz Clock



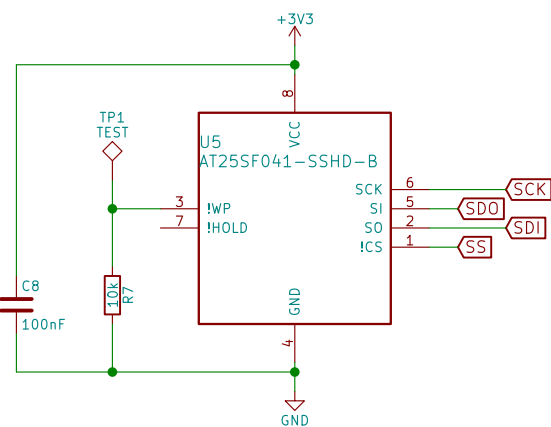
TinyFPGA Bx Pin Headers



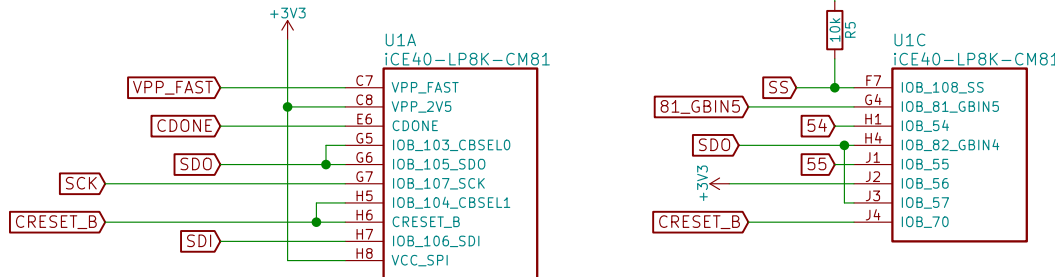
Voltage Regulation



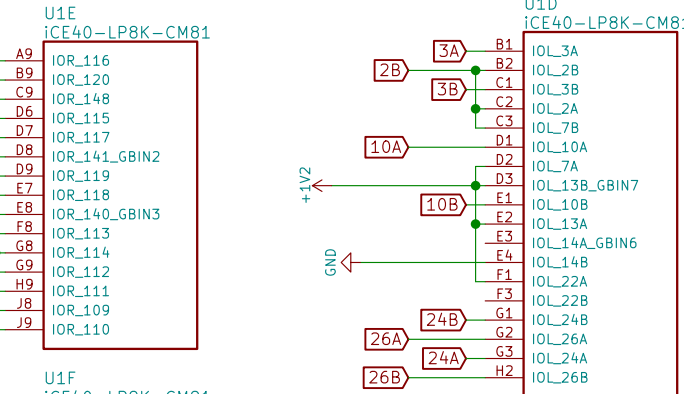
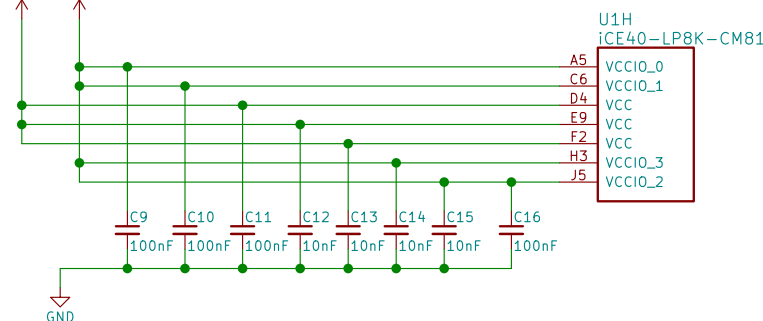
SPI flash memory for FPGA configuration



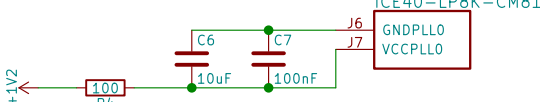
FPGA connections



FPGA core and IO power connections and decoupling capacitors



FPGA PLL decoupling capacitors



FPGA reset button and testpoint

