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++gSIGUSR1-{si_signo=SIGUSR1,si_code=SI_KERNEL}051-b.h:260:#include <sys/pcvt_ioctl.h>
ioctl(13, DRM_IOCTL_MODE_CURSOR, 0x7ffc704555d0) = 0
ioctl(14, DRM_IOCTL_AMDGPU_GEM_CREATE or DRM_IOCTL_VIA_ALLOCMEM, 0x7ffc70454da0) = 0
ioctl(14, _IOC(_IOC_READ|_IOC_WRITE, 0x64, 0x48, 0x28), 0x7ffc70454db0) = 0
ioctl(14, DRM_IOCTL_AMDGPU_GEM_METADATA, 0x7ffc70454fb0) = 0
ioctl(14, DRM_IOCTL_PRIME_HANDLE_TO_FD, 0x7ffc7045530c) = 0
ioctl(17, DRM_IOCTL_PRIME_FD_TO_HANDLE, 0x7ffc7045534c) = 0
ioctl(14, DRM_IOCTL_PRIME_HANDLE_TO_FD, 0x7ffc7045530c) = 0
ioctl(17, DRM_IOCTL_PRIME_FD_TO_HANDLE, 0x7ffc7045534c) = 0
ioctl(14, DRM_IOCTL_PRIME_HANDLE_TO_FD, 0x7ffc7045538c) = 0
ioctl(17, DRM_IOCTL_PRIME_FD_TO_HANDLE, 0x7ffc704553cc) = 0
ioctl(14, DRM_IOCTL_PRIME_HANDLE_TO_FD, 0x7ffc704552bc) = 0
ioctl(17, DRM_IOCTL_PRIME_FD_TO_HANDLE, 0x7ffc704552fc) = 0
ioctl(14, DRM_IOCTL_PRIME_HANDLE_TO_FD, 0x7ffc704552bc) = 0
ioctl(17, DRM_IOCTL_PRIME_FD_TO_HANDLE, 0x7ffc704552fc) = 0
ioctl(14, DRM_IOCTL_PRIME_HANDLE_TO_FD, 0x7ffc704552ec) = 0
ioctl(14, DRM_IOCTL_PRIME_HANDLE_TO_FD, 0x7ffc704552dc) = 0
ioctl(17, DRM_IOCTL_PRIME_FD_TO_HANDLE, 0x7ffc7045531c) = 0
ioctl(13, DRM_IOCTL_PRIME_FD_TO_HANDLE, 0x7ffc7045555c) = 0
ioctl(13, DRM_IOCTL_AMDGPU_GEM_METADATA, 0x7ffc70455440) = 0
ioctl(13, DRM_IOCTL_MODE_ADDFB, 0x7ffc70455590) = 0
ioctl(14, DRM_IOCTL_AMDGPU_CS, 0x7ffc704552b0) = 0
ioctl(14, DRM_IOCTL_AMDGPU_CS, 0x7ffc70455320) = 0
ioctl(14, DRM_IOCTL_AMDGPU_WAIT_CS, 0x7ffc70455550) = 0
ioctl(13, DRM_IOCTL_MODE_SETCRTC, 0x7ffc70455510) = 0
ioctl(14, _IOC(_IOC_READ|_IOC_WRITE, 0x64, 0x48, 0x28), 0x7ffc704554f0) = 0
ioctl(14, DRM_IOCTL_GEM_CLOSE, 0x7ffc70455540) = 0
ioctl(13, DRM_IOCTL_MODE_RMFB, 0x7ffc7045556c) = 0
ioctl(14, _IOC(_IOC_READ|_IOC_WRITE, 0x64, 0x48, 0x28), 0x7ffc704554f0) = 0
ioctl(14, DRM_IOCTL_GEM_CLOSE, 0x7ffc70455540) = 0
ioctl(13, DRM_IOCTL_MODE_RMFB, 0x7ffc7045558c) = 0
ioctl(13, DRM_IOCTL_DROP_MASTER, 0) = 0
ioctl(12, VT_RELDISP, 0x1) = 0
ioctl(13, DRM_IOCTL_MODE_RMFB, 0x7ffc7045575c) = 0
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