Lecture 22 — GPU Programming Continued

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The Host Code

We've learned about how a kernel works and a bit about how to write one.

The next part is the host code.

Now, fortunately, we don't have to write the whole program in C++ or C, even though the kernel has to be written in the CUDA variant.

We're going to use the Rustacuda library!

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Rust Host Code Example

```
#[macro use]
extern crate rustacuda:
use rustacuda::prelude::*;
use std::error::Error;
use std::ffi::CString;
fn main() -> Result <(), Box<dyn Error>> {
    // Set up the context, load the module, and create a stream to run kernels
         in.
    rustacuda::init(CudaFlags::empty())?;
    let device = Device::get device(0)?;
    let ctx = Context::create and push(ContextFlags::MAP HOST | ContextFlags
         :: SCHED AUTO. device)?:
    let ptx = CString::new(include_str!("../resources/add.ptx"))?;
    let module = Module::load from string(&ptx)?:
    let stream = Stream::new(StreamFlags::DEFAULT, None)?;
    // Create buffers for data
    let mut in x = DeviceBuffer::from slice(&[1.0f32; 10])?;
    let mut in v = DeviceBuffer::from slice(&[2.0f32: 10])?:
    let mut out 1 = DeviceBuffer::from slice(&[0.0f32; 10])?;
```

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Rust Host Code Example

```
// This kernel adds each element in 'in_x' and 'in_y' and writes the
     result into 'out'.
unsafe {
    // Launch the kernel with one block of one thread, no dynamic shared
         memory on 'stream'.
    let result = launch!(module.sum<<<1, 1, 0, stream>>>(
        in x.as device ptr(),
        in_y.as_device_ptr(),
        out_1.as_device_ptr(),
        out 1.len()
    ));
    result?;
// Kernel launches are asynchronous, so we wait for the kernels to finish
     executing.
stream.svnchronize()?:
```



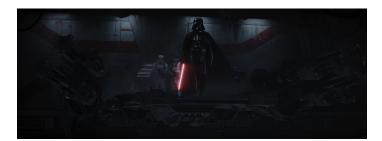
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Rust Host Code Example

```
// Copy the results back to host memory
let mut out_host = [0.0 f32; 20];
out_1.copy_to(&mut out_host[0..10])?;

for x in out_host.iter() {
    assert_eq!(3.0 as u32, *x as u32);
}

println!("Launched kernel successfully.");
Ok(())
```



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Corresponding Kernel

```
extern "C" __constant__ int my_constant = 314;

extern "C" __global__ void sum(const float* x, const float* y, float* out, int count) {
   for (int i = blockldx.x * blockDim.x + threadIdx.x; i < count; i += blockDim.x * gridDim.x) {
      out[i] = x[i] + y[i];
   }
}</pre>
```



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N-Body Problem

Let's take a look at the N-Body Problem code in the repo.

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That's great, but how much does it speed up?

I ran this on ecetesla1.

With 100 000 points:

- nbody (sequential, no approximations): 40.3 seconds
- nbody-parallel (parallel, no approximations): 5.3 seconds
- nbody cuda (parallel, no approximations): 9.5 seconds

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Wait a Minute



That's worse than the CPU version.

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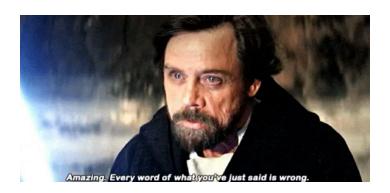
Kylo Ren is very Meme-Able

Theory: 100 000 points is not enough to overcome the overhead of setup and transferring data to and from the device.



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Sequel Memes!

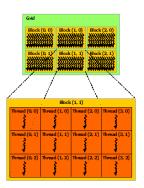


This turned out to be incorrect.

Most of the time was going to the kernel execution of the calculate_forces – as expected?

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This Was Important



The documentation isn't super great about how grids and blocks work.

A lot of the guidance on the internet says is "experiment and try".

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Grid Guidance

The initial approach that I wrote had each work-item be its own grid.

That's inefficient, because we're not taking advantage of all the hardware that's available (the warp hardware).

The advice that I can find says the number of threads per block should always be a multiple of 32 with a maximum of 512 (or perhaps 1024).

The second guidance I can find is that numbers like 256 and 128 are good ones to start with, and you can tweak it as you need.

Then you have to adjust the grid size: divide the number of points by the threads per block to give the number of blocks.

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Change the Launch

I did have to add a +1 to the number of blocks, because 100 000 does not divide evenly by 256.

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Kernel Crash

But just running it as-is didn't work (and led to the kernel crashing. Why?

Because the indexing strategy that I used contained only the reference to the block index blockIdx.x.

That's fine in the scenario where every work-item gets its own block, but that's no longer the case now: 256 work-items (points) now share each block.

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Updated Kernel

```
extern "C" __global__ void calculate_forces(const float4* positions, float3*
    accelerations, int num_points) {
    int idx = threadIdx.x + blockIdx.x * blockDim.x;
    if (idx >= num_points) {
        return;
    }
    float4 current = positions[idx];
    float3 acc = accelerations[idx];
    for (int i = 0; i < num_points; i++) {
        body_body_interaction(current, positions[i], &acc);
    }
    accelerations[idx] = acc;
}</pre>
```

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With 100 000 points:



- nbody (sequential, no approximations): 40.3 seconds
- nbody-parallel (parallel, no approximations): 5.3 seconds
- nbody cuda (parallel, no approximations): 9.5 seconds
- nbody-cuda-grid (parallel, no approx., grid): 1.65 seconds

Now that's a lot better!

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