### Lecture 16 — Dependencies and Speculation

Patrick Lam & Jeff Zarnett patrick.lam@uwaterloo.ca

Department of Electrical and Computer Engineering University of Waterloo

March 1, 2019

ECE 459 Winter 2019 1/30

### **Next topic: Dependencies**

Dependencies are the main limitation to parallelization.

Example: computation must be evaulated as XY and not YX.

ECE 459 Winter 2019 2/30

### Not synchronization

Assume (for now) no synchronization problems.

Only trying to identify code that is safe to run in parallel.

ECE 459 Winter 2019 3/30

### **Dependencies: Analogies**

Must extract bicycle from garage before closing garage door.

Must close washing machine door before starting the cycle.

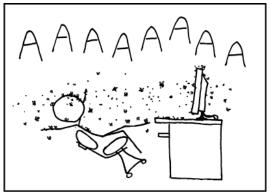
Must be called on before answering questions? (sort of)

Students must submit assignment before course staff can mark the assignment.

ECE 459 Winter 2019 4/30

### **Dependencies: Analogies**

Must install package X before running package Y.



MY PACKAGE MADE IT INTO DEBIAN-MAIN BECAUSE IT LOOKED INNOCUOUS ENOUGH; NO ONE NOTICED "LOCUSTS" IN THE DEPENDENCY LIST.

xkcd 797

### **Memory-carried Dependencies**

Dependencies limit the amount of parallelization.

Can we execute these 2 lines in parallel?

```
x = 42x = x + 1
```

ECE 459 Winter 2019 6 / 30

### **Memory-carried Dependencies**

Dependencies limit the amount of parallelization.

Can we execute these 2 lines in parallel?

```
x = 42x = x + 1
```

#### No.

■ Assume x initially 1. What are possible outcomes?

ECE 459 Winter 2019 6/30

### **Memory-carried Dependencies**

Dependencies limit the amount of parallelization.

Can we execute these 2 lines in parallel?

```
x = 42x = x + 1
```

#### No.

Assume x initially 1. What are possible outcomes? x = 43 or x = 42 or x = 2

Next, we'll classify dependencies.

ECE 459 Winter 2019 6/

# **Summary of Memory-carried Dependencies**

Well, turns out our memory-carried dependencies are the hazards:

		Second Access	
		Read	Write
First Access	Read	No Dependency	Anti-dependency
		Read After Read	Write After Read
		(RAR)	(WAR)
	Write		Output Dependency
		Read After Write	Write After Write
		(RAW)	(WAW)

ECE 459 Winter 2019 7/30

# **Loop-carried Dependencies (1)**

# Can we run these lines in parallel? (initially a[0] and a[1] are 1)

```
a[4] = a[0] + 1
a[5] = a[1] + 2
```

ECE 459 Winter 2019 8 / 30

## **Loop-carried Dependencies (1)**

# Can we run these lines in parallel? (initially a[0] and a[1] are 1)

```
a[4] = a[0] + 1
a[5] = a[1] + 2
```

#### Yes.

- There are no dependencies between these lines.
- However, this is not how we normally use arrays...

ECE 459 Winter 2019 8/

### Loop-carried Dependencies (2)

### What about this? (all elements initially 1)

```
for (int i = 1; i < 12; ++i)
a[i] = a[i-1] + 1
```

ECE 459 Winter 2019 9/30

### Loop-carried Dependencies (2)

#### What about this? (all elements initially 1)

```
for (int i = 1; i < 12; ++i)
a[i] = a[i-1] + 1
```

No, 
$$a[2] = 3$$
 or  $a[2] = 2$ .

- Statements depend on previous loop iterations.
- An example of a loop-carried dependency.

ECE 459 Winter 2019 9 / 30

# Loop-carried Dependencies (3)

Can we parallelize this? (again, all elements initially 1)

```
for (int i = 4; i < 12; ++i)
a[i] = a[i-4] + 1
```

ECE 459 Winter 2019 10/30

### Loop-carried Dependencies (3)

Can we parallelize this? (again, all elements initially 1)

```
for (int i = 4; i < 12; ++i)
a[i] = a[i-4] + 1
```

Yes, to a degree.

- We can execute 4 statements in parallel:
  - a[4] = a[0] + 1, a[8] = a[4] + 1
  - a[5] = a[1] + 1, a[9] = a[5] + 1
  - $\blacksquare$  a[6] = a[2] + 1, a[10] = a[6] + 1
  - $\blacksquare$  a[7] = a[3] + 1, a[11] = a[7] + 1

ECE 459 Winter 2019 10/30

### Loop-carried Dependencies (3)

Can we parallelize this? (again, all elements initially 1)

```
for (int i = 4; i < 12; ++i)
a[i] = a[i-4] + 1
```

Yes, to a degree.

- We can execute 4 statements in parallel:
  - a[4] = a[0] + 1, a[8] = a[4] + 1
  - a[5] = a[1] + 1, a[9] = a[5] + 1
  - a[6] = a[2] + 1, a[10] = a[6] + 1
  - a[7] = a[3] + 1, a[11] = a[7] + 1

Always consider dependencies between iterations.

ECE 459 Winter 2019 10/3

### Larger example: Loop-carried Dependencies

```
// Repeatedly square input, return number of iterations before
// absolute value exceeds 4, or 1000, whichever is smaller.
int inMandelbrot(double x0, double y0) {
  int iterations = 0;
  double x = x0, y = y0, x2 = x*x, y2 = y*y;
  while ((x2+y2 < 4) && (iterations < 1000)) {
    y = 2*x*y + y0;
    x = x2 - y2 + x0;
    x2 = x*x; y2 = y*y;
    iterations++;
  }
  return iterations;
}</pre>
```

How can we parallelize this?

ECE 459 Winter 2019 11/30

### Larger example: Loop-carried Dependencies

```
// Repeatedly square input, return number of iterations before
// absolute value exceeds 4, or 1000, whichever is smaller.
int inMandelbrot(double x0, double y0) {
  int iterations = 0;
  double x = x0, y = y0, x2 = x*x, y2 = y*y;
  while ((x2+y2 < 4) && (iterations < 1000)) {
    y = 2*x*y + y0;
    x = x2 - y2 + x0;
    x2 = x*x; y2 = y*y;
    iterations++;
  }
  return iterations;
}</pre>
```

#### How can we parallelize this?

■ Run inMandelbrot sequentially for each point, but parallelize different point computations.

ECE 459 Winter 2019 11/3

### **Breaking Dependencies**

Speculation: architects use it to predict branch targets.



Image Credit: Diacritica

Roll the dice and see how we do!

ECE 459 Winter 2019 12 / 30

### **Breaking Dependencies**

We need not wait for the branch to be evaluated.

We'll use speculation at a coarser-grained level: speculatively parallelize code.

Two ways: speculative execution and value speculation.

ECE 459 Winter 2019 13/30

### Speculative Execution: Example

#### Consider the following code:

```
void doWork(int x, int y) {
   int value = longCalculation(x, y);
   if (value > threshold) {
     return value + secondLongCalculation(x, y);
   }
   else {
     return value;
   }
}
```

Will we need to run secondLongCalculation?

ECE 459 Winter 2019 14/30

### Speculative Execution: Example

### Consider the following code:

```
void doWork(int x, int y) {
   int value = longCalculation(x, y);
   if (value > threshold) {
      return value + secondLongCalculation(x, y);
   }
   else {
      return value;
   }
}
```

Will we need to run secondLongCalculation?

OK, so: could we execute longCalculation and secondLongCalculation in parallel if we didn't have the conditional?

ECE 459 Winter 2019 14/30

### Speculative Execution: Assume No Conditional

Yes, we could parallelize them. Consider this code:

```
void doWork(int x, int y) {
    thread_t t1, t2;
    point p(x,y);
    int v1, v2;
    thread_create(&t1, NULL, &longCalculation, &p);
    thread_create(&t2, NULL, &secondLongCalculation, &p);
    thread_join(t1, &v1);
    thread_join(t2, &v2);
    if (v1 > threshold) {
        return v1 + v2;
    } else {
        return v1;
    }
}
```

We do both the calculations in parallel and return the same result as before.

What are we assuming about longCalculation and secondLongCalculation?

ECE 459 Winter 2019 15/

### **Estimating Impact of Speculative Execution**

 $T_1$ : time to run longCalculation.

 $T_2$ : time to run secondLongCalculation.

p: probability that secondLongCalculation executes.

In the normal case we have:

$$T_{\text{normal}} = T_1 + pT_2$$
.

S: synchronization overhead. Our speculative code takes:

$$T_{\text{speculative}} = \max(T_1, T_2) + S.$$

Exercise. When is speculative code faster? Slower? How could you improve it?

ECE 459 Winter 2019 16/

### **Shortcomings of Speculative Execution**

#### Consider the following code:

```
void doWork(int x, int y) {
   int value = longCalculation(x, y);
   return secondLongCalculation(value);
}
```

Now we have a true dependency; can't use speculative execution.

But: if the value is predictable, we can execute secondLongCalculation using the predicted value.

This is value speculation.

ECE 459 Winter 2019 17/3

### Value Speculation Implementation

#### This Pthread code does value speculation:

Note: this is like memoization (plus parallelization).

ECE 459 Winter 2019 18 / 30

### **Estimating Impact of Value Speculation**

 $T_1$ : time to run longCalculatuion.

 $T_2$ : time to run secondLongCalculation.

p: probability that secondLongCalculation executes again.

S: synchronization overhead.

In the normal case, we have:

$$T = T_1 + T_2$$
.

This speculative code takes:

$$T = \max(T_1, T_2) + S + pT_2.$$

Exercise. Again, when is speculative code faster? Slower? How could you improve it?

ECE 459 Winter 2019 19/

### When Can We Speculate?

#### Required conditions for safety:

- longCalculation and secondLongCalculation must not call each other.
- secondLongCalculation must not depend on any values set or modified by longCalculation.
- The return value of longCalculation must be deterministic.

General warning: Consider side effects of function calls.

ECE 459 Winter 2019 20 / 30

### **Side Effects**



"Oh yes. It's mentioned here, under side-effects."

Image Credit: Kes, Cartoonstock
ECE 459 Winter 2019

As a general warning: Consider the side effects of function calls.

They have a big impact on parallelism. Side effects are problematic, but why?

For one thing they're kind of unpredictable.

Side effects are changes in state that do not depend on the function input.

Calling a function or expression has a side effect if it has some visible effect on the outside world.

Some things necessarily have side effects, like printing to the console.

Others are side effects which may be avoidable if we can help it, like modifying a global variable.

ECE 459 Winter 2019 22 /

### STM: Introduction

Instead of programming with locks, we have transactions on memory.

■ Analogous to database transactions

An old idea; recently saw some renewed interest.

A series of memory operations either all succeed; or all fail (and get rolled back), and are later retried.

ECE 459 Winter 2019 23/30

### STM: Benefits

Simple programming model: need not worry about lock granularity or deadlocks.

Just group lines of code that should logically be one operation in an atomic block!

It is the responsibility of the implementer to ensure the code operates as an atomic transaction.

ECE 459 Winter 2019 24/30

### STM: Implementing a Motivating Example

[Note: bank transfers aren't actually atomic!]

With locks we have two main options:

- Lock everything to do with modifying accounts (slow; may forget to use lock).
- Have a lock for every account (deadlocks; may forget to use lock).

With STM, we do not have to worry about remembering to acquire locks, or about deadlocks.

ECE 459 Winter 2019 25/

### STM: Drawbacks

#### Rollback is key to STM.

But, some things cannot be rolled back. (write to the screen, send packet over network)

#### Nested transactions.

What if an inner transaction succeeds, yet the transaction aborts?

#### Limited transaction size:

Most implementations (especially all-hardware) have a limited transaction size.

ECE 459 Winter 2019 26 / 30

## Basic STM Implementation (Software)

In all atomic blocks, record all reads/writes to a log.

At the end of the block, running thread verifies that no other threads have modified any values read.

If validation is successful, changes are **committed**. Otherwise, the block is **aborted** and re-executed.

Note: Hardware implementations exist too.

ECE 459 Winter 2019 27/30

### **Basic STM Implementation Issues**

Since you don't protect against dataraces (just rollback), a datarace may trigger a fatal error in your program.

```
atomic {
    x++;
    y++;
}
```

```
atomic {
    if (x != y)
        while (true) { }
}
```

In this silly example, assume initially x = y. You may think the code will not go into an infinite loop, but it can.

ECE 459 Winter 2019 28 / 30

## **STM Implementations**

Note: Typically STM performance is no worse than twice as slow as fine-grained locks.

- Toward.Boost.STM (C++)
- SXM (Microsoft, C#)
- Built-in to the language (Clojure, Haskell)
- AtomJava (Java)
- Durus (Python)

ECE 459 Winter 2019 29 / 30

### STM Summary

Software Transactional Memory provides a more natural approach to parallel programming:

no need to deal with locks and their associated problems.

Currently slow, but a lot of research is going into improving it. (futile?)

Operates by either completing an atomic block, or retrying (by rolling back) until it successfully completes.

ECE 459 Winter 2019 30/30