## Lecture 12 — Autoparallelization

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## **Automatic Parallelization**

We'll now talk about automatic parallelization. The vision is that the compiler will take your standard sequential C program and convert it into a parallel C program which leverages multiple cores, CPUs, machines, etc. This was an active area of research in the 1990s, then tapered off in the 2000s (because it's a hard problem!); it is enjoying renewed interest now (but it's still hard!)

What can we parallelize? The easiest kind of program to parallelize is the classic Fortran program which performs a computation over a huge array. C code—if it's the right kind—is a bit worse, but still tractable, given enough hints to the compiler. For us, the right kind of code is going to be array codes. Some production compilers, like the non-free Intel C compiler icc, the free-as-in-beer Solaris Studio compiler [?] and the free GNU C compiler gcc, include support for parallelization, with different maturity levels.

Following Gove, we'll parallelize the following code:

```
#include <stdlib.h>

void setup(double *vector, int length) {
    int i;
    for (i = 0; i < length; i++) {
        vector[i] += 1.0;
    }
}

int main() {
    double *vector;
    vector = (double*) malloc (sizeof (double) * 1024 * 1024);
    for (int i = 0; i < 1000; i++) {
        setup (vector, 1024*1024);
    }
}</pre>
```

**Automatic Parallelization.** Let's first see what compilers can do automatically. The Solaris Studio compiler yields the following output:

```
$ cc -03 -xloopinfo -xautopar omp_vector.c
"omp_vector.c", line 5: PARALLELIZED, and serial version generated
"omp_vector.c", line 15: not parallelized, call may be unsafe
```

**Note:** The Solaris compiler generates two versions of the code, and decides, at runtime, if the parallel code would be faster, depending on whether the loop bounds, at runtime, are large enough to justify spawning threads.

Under the hood, most parallelization frameworks use OpenMP, which we'll see next time. For now, you can control the number of threads with the OMP\_NUM\_THREADS environment variable.

**Autoparallelization in** gcc. gcc 4.3+ can also parallelize loops, but there are a couple of problems: 1) the loop parallelization doesn't seem very stable yet; 2) I can't figure out how to make gcc tell you what it did in a comprehensible way (you can try -fdump-tree-parloops-details); and, perhaps most importantly for performance, 3) gcc doesn't have many heuristics yet for guessing which loops are profitable (since 4.8, it can use profiling data and tries to infer the number of loop iterations happen) [?].

The BSD and Mac OS X default C compiler clang also has the polly parallelization framework, but we'll leave that aside for now. If you have significant experience with it, make a pull request for this lecture and it will be added!

One way to inspect gcc's output is by giving it the -S option and looking at the resulting assembly code yourself. This is obviously not practical for production software.

```
$ gcc -std=c99 omp_vector.c -02 -floop-parallelize-all -ftree-parallelize-loops=2 -S
```

The resulting .s file contains the following code:

```
call GOMP_parallel_start
movl %edi, (%esp)
call setup._loopfn.0
call GOMP_parallel_end
```

gcc code appears to ignore OMP\_NUM\_THREADS. Here's some potential output from a parallelized program:

(When you use multiple (virtual) CPUs, CPU usage can increase beyond 100% in top, and real time can be less than user time in the time output, since user time counts the time used by all CPUs.)

Let's look at some gcc examples from [?].

Loops That gcc's Automatic Parallelization Can Handle.

Single loop:

```
for (i = 0; i < 1000; i++)
x[i] = i + 3;
```

Nested loops with simple dependency:

```
for (i = 0; i < 100; i++)
  for (j = 0; j < 100; j++)
     X[i][j] = X[i][j] + Y[i-1][j];</pre>
```

Single loop with not-very-simple dependency:

```
for (i = 0; i < 10; i++)
 X[2*i+1] = X[2*i];
```

## Loops That gcc's Automatic Parallelization Can't Handle.

Single loop with if statement:

```
for (j = 0; j \le 10; j++)
if (j > 5) X[i] = i + 3;
```

Triangle loop:

```
for (i = 0; i < 100; i++)
  for (j = i; j < 100; j++)
     X[i][j] = 5;</pre>
```

**Manual Parallelization.** Let's first think about how we could manually parallelize this code.

- **Option 1:** horizontal,  $\equiv \equiv \equiv \equiv$  Create 4 threads; each thread does 1000 iterations on its own sub-array.
- **Option 2:** bad horizontal,  $\equiv \equiv \equiv \equiv$  1000 times, create 4 threads which each operate once on the sub-array.
- **Option 3:** vertical |||| |||| |||| |||| Create 4 threads; for each element, the owning thread does 1000 iterations on that element.

We can try these and empirically see which works better. As you might expect, bad horizontal does the worst. Horizontal does best. Let's take a minute to look at the results from [?]

## Case study: Multiplying a Matrix by a Vector.

Next, we'll see how automatic parallelization does on a more complicated program. We will progressively remove barriers to parallelization for this program:

The Solaris C compiler refuses to parallelize this code:

```
$ cc -03 -xloopinfo -xautopar fploop.c
"fploop.c", line 5: not parallelized, not a recognized for loop
"fploop.c", line 8: not parallelized, not a recognized for loop
```

For definitive documentation about Sun's automatic parallelization, see Chapter 10 of their *Fortran Programming Guide* and do the analogy to C:

http://download.oracle.com/docs/cd/E19205-01/819-5262/index.html

In this case, the loop bounds are not constant, and the write to out might overwrite either row or col. So, let's modify the code and make the loop bounds into rather than int \*s.

This changes the error message:

```
$ cc -03 -xloopinfo -xautopar fploop1.c
"fploop1.c", line 5: not parallelized, unsafe dependence
"fploop1.c", line 8: not parallelized, unsafe dependence
```

Now the problem is that out might alias mat or vec; as I've mentioned previously, parallelizing in the presence of aliases could change the run-time behaviour.

restrict qualifier. Recall that the restrict qualifier on pointer p tells the compiler that it may assume that, in the scope of p, the program will not use any other pointer q to access the data at p [?].

Now Solaris cc is happy to parallelize the outer loop:

```
$ cc -03 -xloopinfo -xautopar fploop2.c
"fploop2.c", line 5: PARALLELIZED, and serial version generated
"fploop2.c", line 8: not parallelized, unsafe dependence
```

There's still a dependence in the inner loop. This dependence is because all inner loop iterations write to the same location, out [i]. We'll discuss that problem below.

In any case, the outer loop is the one that can actually improve performance, since parallelizing it imposes much less barrier synchronization cost waiting for all threads to finish. So, even if we tell the compiler to ignore the reduction issue, it will generally refuse to parallelize inner loops:

```
$ cc -g -03 -xloopinfo -xautopar -xreduction fploop2.c
"fploop2.c", line 5: PARALLELIZED, and serial version generated
"fploop2.c", line 8: not parallelized, not profitable
```

**Summary of conditions for automatic parallelization.** Here's what I can figure out; you may also refer to Chapter 3 of the Solaris Studio *C User's Guide*, but it doesn't spell out the exact conditions either. To parallelize a loop, it must:

- have a recognized loop style, e.g. for loops with bounds that don't vary per iteration;
- have no dependencies between data accessed in loop bodies for each iteration;
- not conditionally change scalar variables read after the loop terminates, or change any scalar variable across iterations;
- have enough work in the loop body to make parallelization profitable.

**Reductions.** The concept behind a reduction (as made "famous" in MapReduce, which we'll talk about later) is reducing a set of data to a smaller set which somehow summarizes the data. For us, reductions are going to reduce arrays to a single value. Consider, for instance, this function, which calculates the sum of an array of numbers:

```
double sum (double *array, int length)
{
  double total = 0;
  for (int i = 0; i < length; i++)
    total += array[i];
  return total;
}</pre>
```

There are two barriers: 1) the value of total depends on what gets computed in previous iterations; and 2) addition is actually non-associative for floating-point values. (Why? When is it appropriate to parallelize non-associative operations?)

Nevertheless, the Solaris C compiler will explicitly recognize some reductions and can parallelize them for you:

```
$ cc -03 -xautopar -xreduction -xloopinfo sum.c "sum.c", line 5: PARALLELIZED, reduction, and serial version generated
```

**Note:** If we try to do the reduction on fploop.c with restricts added, we'll get the following:

```
$ cc -03 -xautopar -xloopinfo -xreduction -c fploop.c
"fploop.c", line 5: PARALLELIZED, and serial version generated
"fploop.c", line 8: not parallelized, not profitable
```

**Dealing with function calls.** Generally, function calls can have arbitrary side effects. Production compilers will usually avoid parallelizing loops with function calls; research compilers try to ensure that functions are pure and then parallelize them. (This is why functional languages are nice for parallel programming: impurity is visible in type signatures.)

For builtin functions, like sin(), you can promise to the compiler that you didn't replace them with your own implementations (-xbuiltin), and then the compiler will parallelize the loop.

Another option is to crank up the optimization level (-x04), or to explicitly tell the compiler to inline certain functions (-xinline=), thereby enabling parallelization. This doesn't work as well as one might hope; using macros will always work, but is less maintainable.

Helping the compiler parallelize. Let's summarize what we've seen. To help the compiler, we can use the restrict qualifier on pointers (possibly copying a pointer to a restrict-qualified pointer: int \* restrict p = s->p;); and, we can make sure that loop bounds don't change in the loop (e.g. by using temporary variables). Some compilers can automatically create different versions for the alias-free case and the (parallelized) aliased case; at runtime, the program runs the aliased case if the inputs permit.

**What happened last time?** There was some confusion about manual parallelization. Recall that we manually parallelized three ways:

```
horizontal good:
create 4 threads to do 1000 iterations on sub-arrays.
horizontal bad:
1000 times, create 4 threads to iterate on sub-array.
vertical:
create 4 threads, handle 1 element at a time.
```

Timings were inconclusive. I tried harder and got these timings (in seconds) with perf stat -r 5:

	H good	H bad	V	auto
gcc, no opt	2.794	2.953	2.799	
gcc, -O3	0.588	1.490	0.980	
solaris, no opt	3.175	3.291	2.966	
solaris, -xO4	0.494	1.453	2.739	0.688

perf also told me other fun facts about the executions:

- fast executions had 3 to 7 cpu-migrations, slow ones had 4000 cpu-migrations.
- branch misses varied from 8k (gcc -O3, H good) to 208k (gcc -O3, bad).
- # cycles varied from 2B (gcc -O3, H good) to 9.7B (unopt).

Turns out that these stats don't perfectly predict the runtime. Frontend cycles was really high for solaris autoparallelization (which was quite fast).