Lecture 22 — GPU Programming / OpenCL

Patrick Lam patrick.lam@uwaterloo.ca

Department of Electrical and Computer Engineering University of Waterloo

April 4, 2019

ECE 459 Winter 2019 1/46

Part I

OpenCL concepts

ECE 459 Winter 2019 2/46

Introduction

OpenCL: coding on a heterogeneous architecture.

 No longer just programming the CPU; will also leverage the GPU.

OpenCL = Open Computing Language. Usable on both NVIDIA and AMD GPUs.

ECE 459 Winter 2019 3/46

Another term you may see vendors using:

- Single Instruction, Multiple Threads.
- Runs on a vector of data.
- Similar to SIMD instructions (e.g. SSE).
 However, the vector is spread out over the GPU.

ECE 459 Winter 2019 4 / 46

Other Heterogeneous Programming Examples

- PlayStation 3 Cell
- CUDA

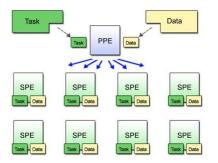
[PS4: back to a regular CPU/GPU system, albeit on one chip.]

ECE 459 Winter 2019 5/46

(PS3) Cell Overview

Cell consists of:

- a PowerPC core; and
- 8 SIMD co-processors.



(from the Linux Cell documentation)

ECE 459 Winter 2019 6 / 46

CUDA Overview

Compute Unified Device Architecture: NVIDIA's architecture for processing on GPUs.

"C for CUDA" predates OpenCL, NVIDIA supports it first and foremost.

- May be faster than OpenCL on NVIDIA hardware.
- API allows you to use (most) C++ features in CUDA; OpenCL has more restrictions.

ECE 459 Winter 2019 7/46

GPU Programming Model

The abstract model is simple:

- Write the code for the parallel computation (*kernel*) separately from main code.
- Transfer the data to the GPU co-processor (or execute it on the CPU).
- Wait...
- Transfer the results back.

ECE 459 Winter 2019 8 / 46

Data Parallelism

■ Key idea: evaluate a function (or *kernel*) over a set of points (data).

Another example of data parallelism.

- Another name for the set of points: *index space*.
- Each point corresponds to a work-item.

Note: OpenCL also supports *task parallelism* (using different kernels), but documentation is sparse.

ECE 459 Winter 2019 9 / 46

Work-Items

Work-item: the fundamental unit of work in OpenCL. Stored in an *n*-dimensional grid (ND-Range); 2D above.

OpenCL spawns a bunch of threads to handle work-items. When executing, the range is divided into **work-groups**, which execute on the same compute unit.

The set of compute units (or cores) is called something different depending on the manufacturer.

- NVIDIA warp
- AMD/ATI wavefront

ECE 459 Winter 2019 10 / 46

Work-Items: Three more details

One thread per work item, each with a different thread ID.

You can say how to divide the ND-Range into work-groups, or the system can do it for you.

Scheduler assigns work-items to warps/wavefronts until no more left.

ECE 459 Winter 2019 11/46

Shared Memory

There are many different types of memory available to you:

- private memory: available to a single work-item;
- local memory (aka "shared memory"): shared between work-items belonging to the same work-group; like a user-managed cache;
- global memory: shared between all work-items as well as the host;
- constant memory: resides on the GPU and cached. Does not change.

There is also host memory (normal memory); usually contains app data.

ECE 459 Winter 2019 12 / 46

Here's some traditional code to evaluate $C_i = A_i B_i$:

And as a kernel:

ECE 459 Winter 2019 13/46

Restrictions when writing kernels in OpenCL

It's mostly C, but:

- No function pointers.
- No bit-fields.
- No variable-length arrays.
- No recursion.
- No standard headers.

ECE 459 Winter 2019 14/46

OpenCL's Additions to C in Kernels

In kernels, you can also use:

- Work-items.
- Work-groups.
- Vectors.
- Synchronization.
- Declarations of memory type.
- Kernel-specific library.

ECE 459 Winter 2019 15/46

The hardware will execute *all* branches that any thread in a warp executes—can be slow!

In other words: an if statement will cause each thread to execute both branches; we keep only the result of the taken branch.

ECE 459 Winter 2019 16/46

A loop will cause the workgroup to wait for the maximum number of iterations of the loop in any work-item.

Note: when you set up work-groups, best to arrange for all work-items in a workgroup to execute the same branches & loops.

ECE 459 Winter 2019 17 / 46

Synchronization

Different workgroups execute independently.

You can only put barriers and memory fences between work-items in the same workgroup.

OpenCL supports:

- Memory fences (load and store).
- Barriers.
- volatile (beware!)

ECE 459 Winter 2019 18 / 46

Part II

Programming with OpenCL

ECE 459 Winter 2019 19 / 46

Introduction

Today, we'll see how to program with OpenCL.

- We're using OpenCL 1.1.
- There is a lot of initialization and querying.
- When you compile your program, include 10penCL.

You can find the official documentation here:

http://www.khronos.org/opencl/

More specifically:

http://www.khronos.org/registry/cl/sdk/1.1/docs/man/xhtml/

Let's just dive into an example.

ECE 459 Winter 2019 20 / 46

First, reminders

- All data belongs to an **NDRange**.
- The range can be divided into **work-groups**. (in software)
- The work-groups run on wavefronts/warps. (in hardware)
- Each wavefront/warp executes work-items.

All branches in a wavefront/warp should execute the same path.

If an iteration of a loop takes t: when one work-item executes 100 iterations, the total time to complete the wavefront/warp is 100t.

ECE 459 Winter 2019 21 / 46

Part III

Simple Example

ECE 459 Winter 2019 22/46

```
// Note by PL: don't use this example as a template;
  it uses the C bindings! Instead, use the C++ bindings.
  source: pages 1-9 through 1-11,
   http://developer.amd.com/wordpress/media/2013/07/
//
//
       AMD Accelerated Parallel Processing OpenCL
//
       Programming Guide-rev - 2.7. pdf
#include <CL/cl.h>
#include <stdio.h>
#define NWITEMS 512
// A simple memset kernel
const char *source =
 __kernel void memset( __global uint *dst )
                                                          \n"
    dst[get_global_id(0)] = get_global_id(0);
" }
                                                          \n";
int main(int argc, char ** argv)
   // 1. Get a platform.
   cl platform id platform;
   clGetPlatformIDs (1, &platform, NULL);
```

ECE 459 Winter 2019 23 / 46

Explanation (1)

Include the OpenCL header.

Request a platform (also known as a host).

A platform contains compute devices:

■ GPUs or CPUs.

ECE 459 Winter 2019 24 / 46

Simple Example (2)

ECE 459 Winter 2019 25 / 46

Explanation (2)

Request a GPU device.

Request a OpenCL context (representing all of OpenCL's state).

Create a command-queue:

get OpenCL to do work by telling it to run a kernel in a queue.

ECE 459 Winter 2019 26 / 46

Simple Example (3)

ECE 459 Winter 2019 27 / 46

Explanation (3)

We create an OpenCL "program" (runs on the compute unit):

- kernels;
- functions; and
- declarations.

In this case, we create a kernel called memset from source. OpenCL may also create programs from binaries (may be in intermediate representation).

Next, we need a data buffer (enables inter-device communication).

This program does not have any input, so we don't put anything into the buffer (just declare its size).

ECE 459 Winter 2019 28 / 46

Simple Example (4)

```
// 6. Launch the kernel. Let OpenCL pick the local work
      size.
size t global work size = NWITEMS;
clSetKernelArg(kernel,0, sizeof(buffer), (void*)&buffer);
clEnqueueNDRangeKernel(queue,
                       kernel.
                       1. // dimensions
                       NULL, // initial offsets
                       &global work size, // number of
                                           // work-items
                       NULL, // work-items per work-group
                       O. NULL. NULL): // events
clFinish (queue):
// 7. Look at the results via synchronous buffer map.
cl uint *ptr;
ptr = (cl uint *)clEnqueueMapBuffer(queue, buffer,
                                    CL_TRUE, CL_MAP_READ,
                                    0. NWITEMS *
                                     sizeof(cl uint).
                                    O. NULL. NULL. NULL):
```

ECE 459 Winter 2019 29 / 46

Explanation (4)

Set kernel arguments to buffer.

We launch the kernel, enqueuing the 1-dimensional index space starting at 0.

We specify that the index space has NWITEMS elements; and not to subdivide the program into work-groups.

There is also an event interface, which we do not use.

We copy the results back; call is blocking (CL_TRUE); hence we don't need an explicit clFinish() call.

We specify that we want to read the results back into buffer.

ECE 459 Winter 2019 30 / 46

Simple Example (5)

```
int i;
for(i=0; i < NWITEMS; i++)
    printf("%d %d\n", i, ptr[i]);
return 0;
}</pre>
```

- The program simply prints 0 0, 1 1, ..., 511 511.
- Note: I didn't clean up or include error handling for any of the OpenCL functions.

ECE 459 Winter 2019 31/46

Part IV

Another Example

ECE 459 Winter 2019 32 / 46

C++ Bindings

If we use the C++ bindings, we'll get automatic resource release and exceptions.

■ C++ likes to use the RAII style (resource allocation is initialization).

Change the header to CL/cl.hpp and define $__\text{CL_ENABLE_EXCEPTIONS}.$

We'd also like to store our kernel in a file instead of a string.

The C API is not so nice to work with.

ECE 459 Winter 2019 33/46

Vector Addition Kernel

Let's write a kernel that adds two vectors and stores the result. This kernel will go in the file vector_add_kernel.cl.

Other possible qualifiers: local, constant and private.

ECE 459 Winter 2019 34/46

```
// Vector add example, C++ bindings (use these!)
   source:
//
   http://www.thebigblob.com/getting-started-
//
          with-opencl-and-gpu-computing/
#define __CL_ENABLE_EXCEPTIONS
#include <CL/cl.hpp>
#include <iostream>
#include <fstream>
#include <string>
#include <utility >
#include <vector>
int main() {
    // Create the two input vectors
    const int LIST SIZE = 1000;
    int *A = new int[LIST_SIZE];
    int *B = new int[LIST SIZE];
    for (int i = 0; i < LIST SIZE; i++) {
        A[i] = i:
        B[i] = LIST SIZE - i;
```

```
try {
    // Get available platforms
    std::vector < cl::Platform > platforms;
    cl::Platform::get(&platforms):
    // Select the default platform and create a context
    // using this platform and the GPU
    cl context properties cps[3] = {
        CL CONTEXT PLATFORM,
        (cl context_properties)(platforms[0])(),
    };
    cl::Context context(CL DEVICE TYPE GPU, cps):
    // Get a list of devices on this platform
    std::vector<cl::Device> devices =
        context.getInfo < CL CONTEXT DEVICES > ();
    // Create a command queue and use the first device
    cl::CommandOueue queue = cl::CommandOueue(context.
        devices [0]):
```

ECE 459 Winter 2019 36/46

Explanation (2)

You can define __NO_STD_VECTOR and use cl::vector (same with strings).

You can enable profiling by adding CL_QUEUE_PROFILING_ENABLE as 3rd argument to queue.

ECE 459 Winter 2019 37/46

```
// Read source file
std::ifstream sourceFile("vector_add_kernel.cl");
std::string sourceCode(
    std::istreambuf iterator < char > (sourceFile),
    (std::istreambuf iterator < char > ())
cl::Program::Sources source(
    std::make pair(sourceCode.c str(),
                   sourceCode.length()+1)
);
// Make program of the source code in the context
cl::Program program = cl::Program(context, source);
// Build program for these specific devices
program.build(devices);
// Make kernel
cl::Kernel kernel(program, "vector_add");
```

ECE 459 Winter 2019 38 / 46

Vector Addition (4)

```
// Create memory buffers
cl::Buffer bufferA = cl::Buffer(
    context,
    CL_MEM_READ_ONLY,
    LIST_SIZE * sizeof(int)
);
cl::Buffer bufferB = cl::Buffer(
    context,
    CL_MEM_READ_ONLY,
    LIST_SIZE * sizeof(int)
);
cl::Buffer bufferC = cl::Buffer(
    context,
    CL_MEM_URITE_ONLY,
    LIST_SIZE * sizeof(int)
);
```

ECE 459 Winter 2019 39 / 46

Vector Addition (5)

```
// Copy lists A and B to the memory buffers
queue.enqueueWriteBuffer(
    bufferA,
    CL TRUE,
    0,
    LIST SIZE * sizeof(int),
);
queue.enqueueWriteBuffer(
    bufferB.
    CL_TRUE,
    0,
    LIST_SIZE * sizeof(int),
    В
);
// Set arguments to kernel
kernel.setArg(0, bufferA);
kernel.setArg(1, bufferB);
kernel.setArg(2, bufferC);
```

ECE 459 Winter 2019 40 / 46

Explanation (5)

enqueue*Buffer arguments:

- buffer
- cl_ bool *blocking_write*
- ::size_t offset
- ::size_t size
- const void * ptr

ECE 459 Winter 2019 41 / 46

Vector Addition (6)

```
// Run the kernel on specific ND range
cl::NDRange global(LIST_SIZE);
cl::NDRange local(1);
queue.enqueueNDRangeKernel(
    kernel,
    cl:: NullRange,
    global,
    local
);
// Read buffer C into a local list
int* C = new int[LIST SIZE];
queue.enqueueReadBuffer(
     bufferC.
     CL TRUE,
     0,
     LIST_SIZE * sizeof(int),
     C
);
```

ECE 459 Winter 2019 42 / 46

Vector Addition (7)

This program just prints all the additions (equalling 1000).

ECE 459 Winter 2019 43/46

Other Improvements

The host memory is still unreleased.

■ With the same number of lines, we could use the C++11 unique_ptr, which would free the memory for us.

You can use a vector instead of an array, and use &v[0] instead of <type>*.

■ Valid as long as the vector is not resized.

ECE 459 Winter 2019 44 / 46

OpenCL Programming Summary

Went through real OpenCL examples. Have the reference card for the API.

Saw a C++ template for setting up OpenCL.

Aside: if you're serious about programming in C++, check out **Effective C++** by Scott Meyers (slightly dated with C++11, but it still has some good stuff)

ECE 459 Winter 2019 45 / 46

Overall summary

First Half: Brief overview of OpenCL and its programming model.

Many concepts are similar to plain parallel programming (more structure).

Second Half: Looked at an OpenCL implementation and how to organize it.

Need to write lots of boilerplate!

ECE 459 Winter 2019 46/46