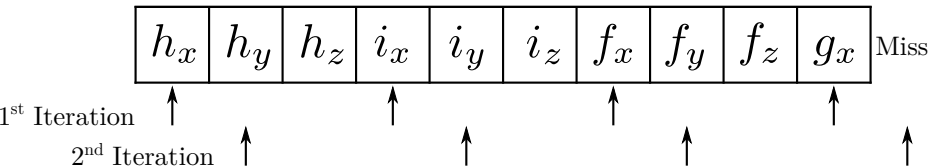


Strided Memory Access



Coalesced Memory Access

