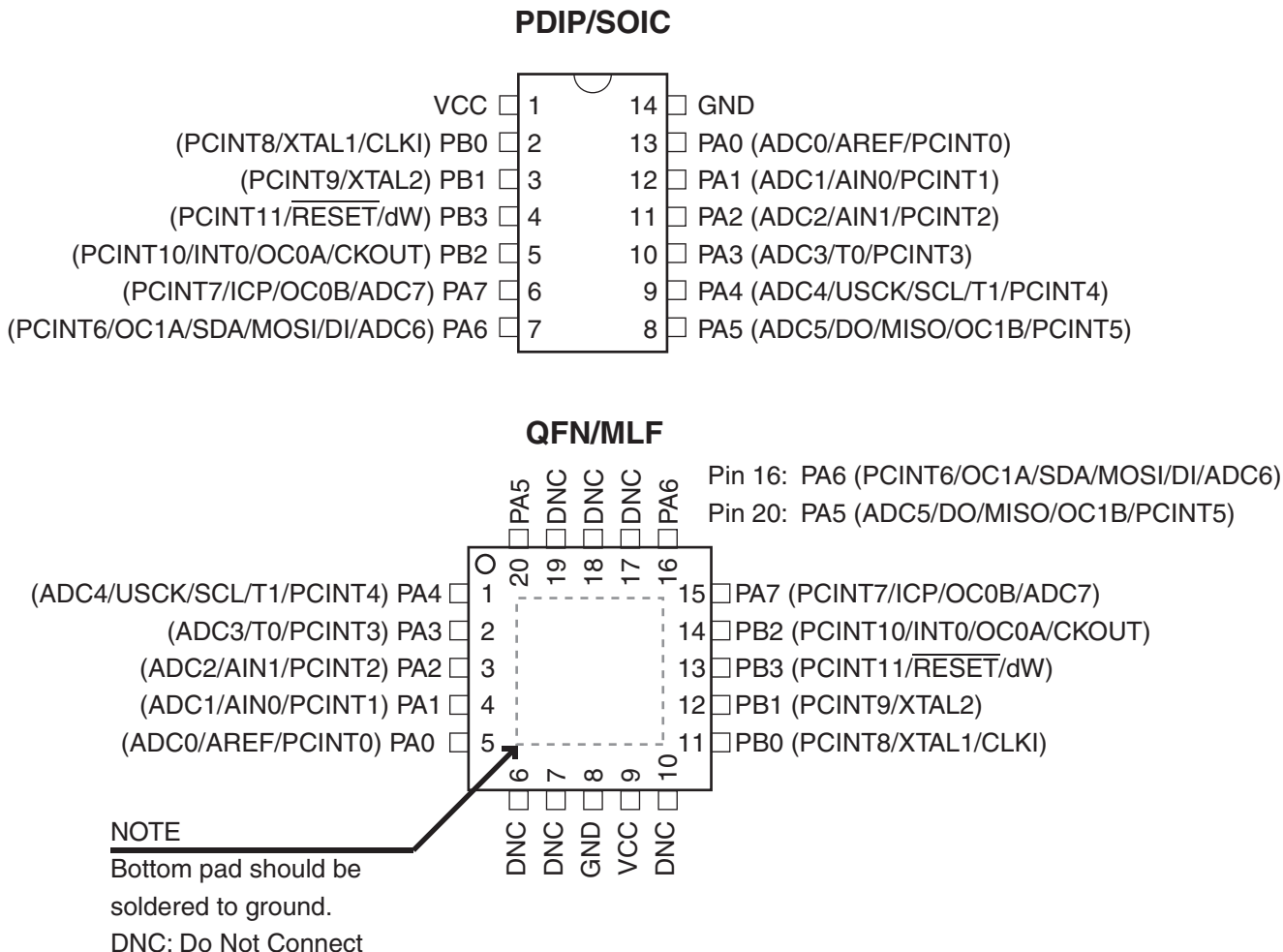


## 1. Pin Configurations

Figure 1-1. Pinout ATtiny24/44/84



### 1.1 Pin Descriptions

#### 1.1.1 VCC

Supply voltage.

#### 1.1.2 GND

Ground.

#### 1.1.3 Port B (PB3:PB0)

Port B is a 4-bit bi-directional I/O port with internal pull-up resistors (selected for each bit). The Port B output buffers have symmetrical drive characteristics with both high sink and source capability except PB3 which has the  $\overline{\text{RESET}}$  capability. To use pin PB3 as an I/O pin, instead of RESET pin, program ('0') RSTDISBL fuse. As inputs, Port B pins that are externally pulled low will source current if the pull-up resistors are activated. The Port B pins are tri-stated when a reset condition becomes active, even if the clock is not running.