A1) N=100. Direct mapped row by row addition

| Block size(words) Cache size(bytes)	2	4	8	16	32
128	Miss rate: 49% # of misses: 10080	Miss rate: 25% # of misses: 5042	Miss rate: 22% # of misses: 2522	Miss rate: 6% # of misses: 1264	Miss rate: 3% # of misses: 636
256	Miss rate: 49% # of misses: 10080	Miss rate: 25% # of misses:5042	Miss rate: 22% # of misses: 2522	Miss rate: 6% # of misses: 1264	Miss rate: 3% # of misses: 636
512	Miss rate: 49% # of misses: 10080	Miss rate: 25% # of misses: 5042	Miss rate: 22% # of misses: 2522	Miss rate: 6% # of misses: 1264	Miss rate: 3% # of misses: 636
1024	Miss rate: 49% # of misses: 10080	Miss rate: 25% # of misses: 5042	Miss rate: 22% # of misses: 2522	Miss rate: 6% # of misses: 1264	Miss rate: 3% # of misses: 636
2048	Miss rate: 49% # of misses: 10080	Miss rate: 25% # of misses: 5042	Miss rate: 22% # of misses: 2522	Miss rate: 6% # of misses: 1264	Miss rate: 3% # of misses: 636

N=100. Direct mapped col by col addition

Block size(words) Cache size(bytes)	2	4	8	16	32
128	Miss rate: 73% # of misses: 15080	Miss rate: 61% # of misses: 12542	Miss rate: 55% # of misses: 11272	Miss rate: 52% # of misses: 10639	Miss rate: 50% # of misses: 10323
256	Miss rate: 73% # of misses: 15080	Miss rate: 61% # of misses: 12542	Miss rate: 55% # of misses: 11272	Miss rate: 52% # of misses: 10639	Miss rate: 50% # of misses: 10323
512	Miss rate: 73% # of misses: 15080	Miss rate: 61% # of misses: 12542	Miss rate: 55% # of misses: 11272	Miss rate: 52% # of misses: 10638	Miss rate: 50% # of misses: 10322
1024	Miss rate: 67% # of misses: 13680	Miss rate: 51% # of misses: 10442	Miss rate: 55% # of misses: 11272	Miss rate: 52% # of misses: 10638	Miss rate: 50% # of misses: 10322
2048	Miss rate: 49% # of misses: 10070	Miss rate: 24% # of misses: 5037	Miss rate: 45% # of misses: 9172	Miss rate: 52% # of misses: 10638	Miss rate: 50% # of misses: 10322

Matrix1 Graph n=100(col by col)
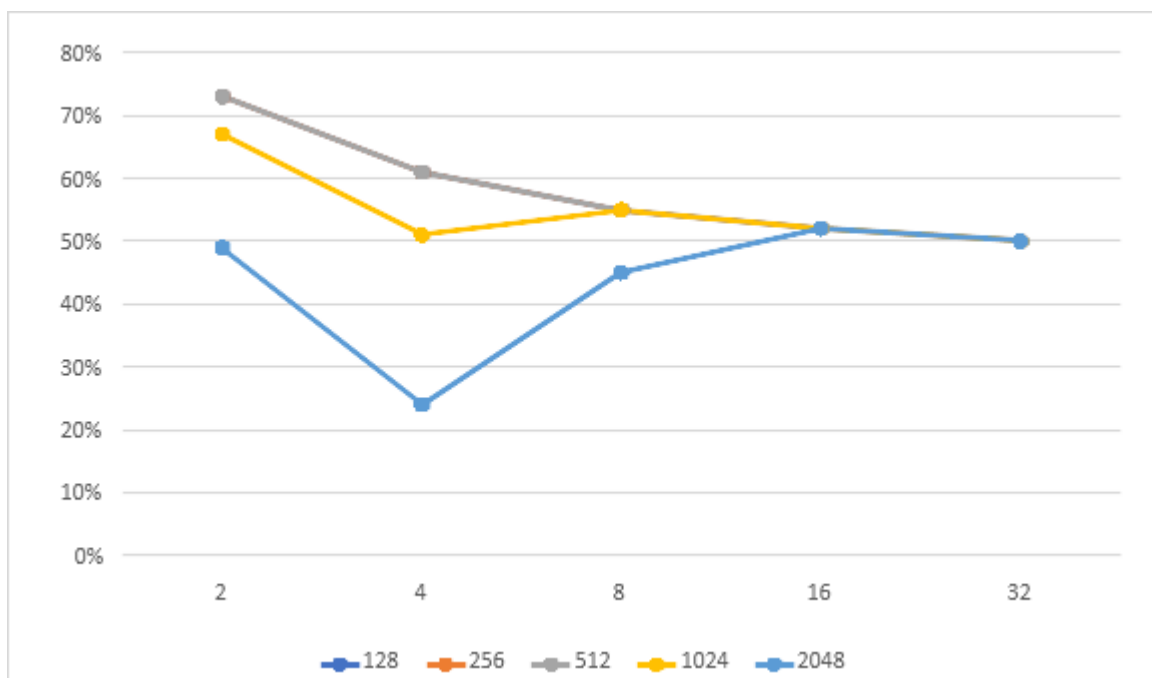
Matrix1 Graph n=100(row by row)
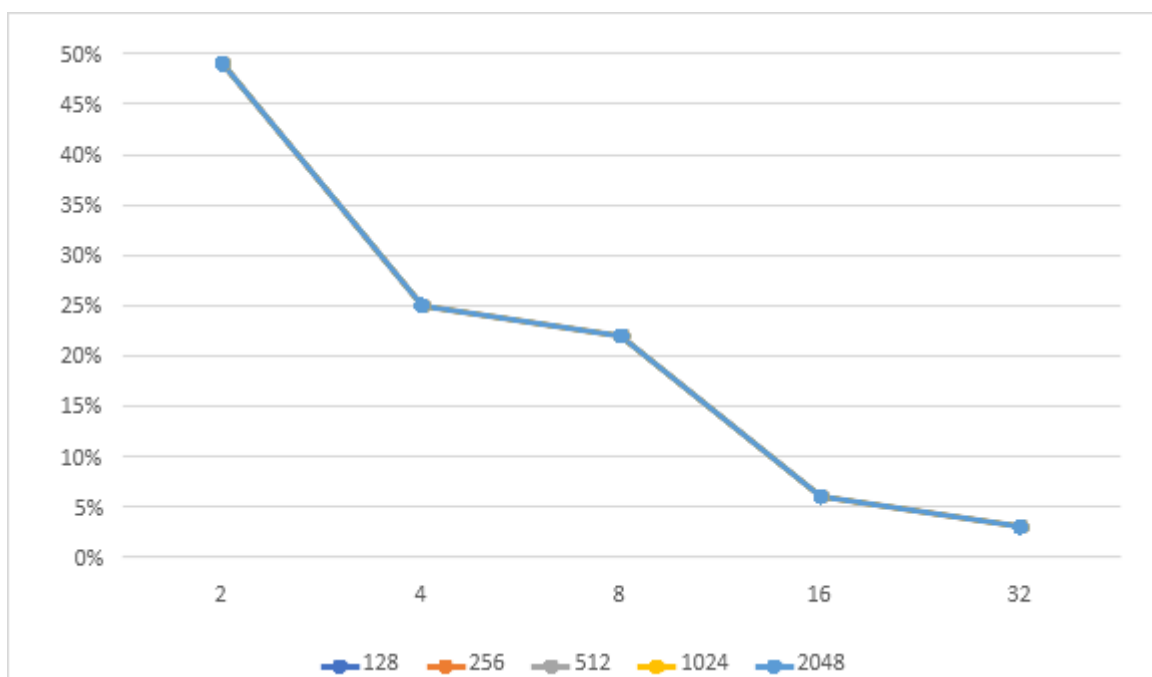
B1)

	Good Block size = 4 Cache size = 2048	Med Block size = 16 Cache size =512	Poor Block size = 2 Cache size = 128
Direct mapped	Miss rate: 24% # of misses: 5037	Miss rate: 52% # of misses: 10639	Miss rate: 73% # of misses: 15080
LRU	Miss rate: 25% # of misses: 5042	Miss rate: 52% # of misses: 10638	Miss rate: 73% # of misses: 15080
random	Miss rate: 34% # of misses: 6908	Miss rate: 52% # of misses: 10638	Miss rate: 73% # of misses: 15073

C1)

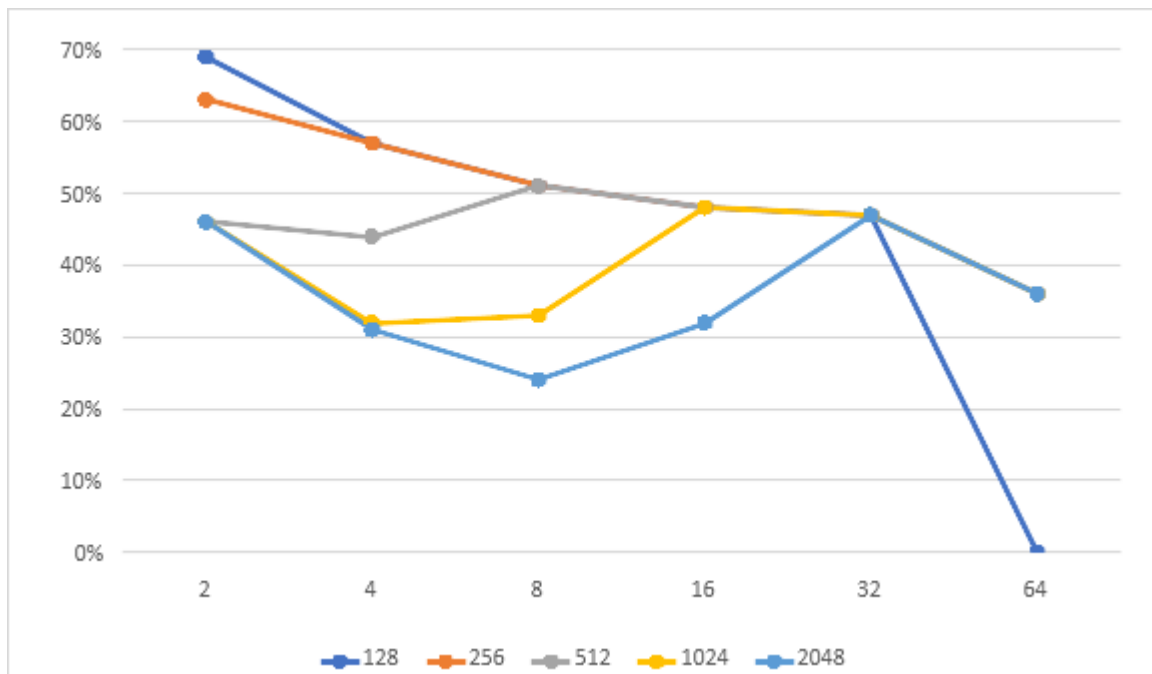| Set size	Med Block size = 16 Cache size =512	Good Block size = 4 Cache size = 2048	Poor Block size = 2 Cache size = 128
1	Miss rate: 52% # of misses: 10638	Miss rate: 24% # of misses: 5037	Miss rate: 73% # of misses: 15080
2	Miss rate: 52% # of misses: 10638	Miss rate: 25% # of misses: 5040	Miss rate: 73% # of misses: 15080
4	Miss rate: 52% # of misses: 10638	Miss rate: 25% # of misses: 5042	Miss rate: 73% # of misses: 15080
8	Miss rate: 52% # of misses: 10639	Miss rate: 25% # of misses: 5042	Miss rate: 73% # of misses: 15080

A2) N=50. Direct mapped row by row addition

| Block size(words) Cache size(bytes)	2	4	8	16	32	64
128	Miss rate: 46% # of misses: 2580	Miss rate: 23% # of misses: 1292	Miss rate: 12% # of misses: 648	Miss rate: 6% # of misses: 328	Miss rate: 3% # of misses: 168	
256	Miss rate: 46% # of misses: 2580	Miss rate: 23% # of misses: 1292	Miss rate: 12% # of misses: 648	Miss rate: 6% # of misses: 328	Miss rate: 3% # of misses: 168	Miss rate: 2% # of misses: 88
512	Miss rate: 46% # of misses: 2580	Miss rate: 23% # of misses: 1292	Miss rate: 12% # of misses: 648	Miss rate: 6% # of misses: 328	Miss rate: 3% # of misses: 168	Miss rate: 2% # of misses: 88
1024	Miss rate: 46% # of misses: 2580	Miss rate: 23% # of misses: 1292	Miss rate: 12% # of misses: 648	Miss rate: 6% # of misses: 328	Miss rate: 3% # of misses: 168	Miss rate: 2% # of misses: 88
2048	Miss rate: 46% # of misses: 2580	Miss rate: 23% # of misses: 1292	Miss rate: 12% # of misses: 648	Miss rate: 6% # of misses: 328	Miss rate: 3% # of misses: 168	Miss rate: 2% # of misses: 88

N=50. Direct mapped col by col addition

Block size(words)	2	4	8	16	32	64
Cache size(bytes)						
128	Miss rate: 69% # of misses: 3830	Miss rate: 57% # of misses: 3167	Miss rate: 51% # of misses: 2835	Miss rate: 48% # of misses: 2671	Miss rate: 47% # of misses: 2589	
256	Miss rate: 63% # of misses: 3480	Miss rate: 57% # of misses: 3167	Miss rate: 51% # of misses: 2835	Miss rate: 48% # of misses: 2671	Miss rate: 47% # of misses: 2589	Miss rate: 36% # of misses: 2002
512	Miss rate: 46% # of misses: 2579	Miss rate: 44% # of misses: 2433	Miss rate: 51% # of misses: 2835	Miss rate: 48% # of misses: 2670	Miss rate: 47% # of misses: 2588	Miss rate: 36% # of misses: 2002
1024	Miss rate: 46% # of misses: 2573	Miss rate: 32% # of misses: 1757	Miss rate: 33% # of misses: 1859	Miss rate: 48% # of misses: 2670	Miss rate: 47% # of misses: 2589	Miss rate: 36% # of misses: 2002
2048	Miss rate: 46% # of misses: 2532	Miss rate: 31% # of misses: 1739	Miss rate: 24% # of misses: 1341	Miss rate: 32% # of misses: 1783	Miss rate: 47% # of misses: 2588	Miss rate: 36% # of misses: 2002

Matrix2 Graph n=50(col by col)

Matrix2 Graph n=50(row by row)
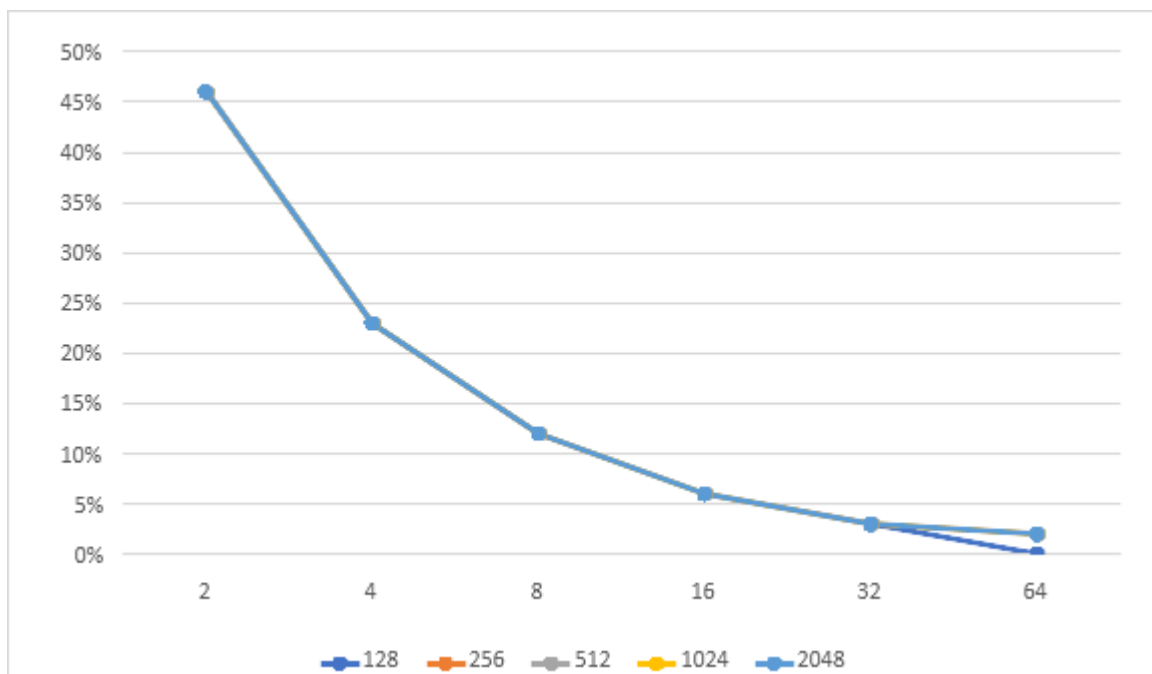
B2)

	Good Block size = 8 Cache size = 2048	Med Block size = 16 Cache size =1024	Poor Block size = 2 Cache size = 128
Direct mapped	Miss rate: 24% # of misses: 1341	Miss rate: 48% # of misses: 2670	Miss rate: 69% # of misses: 3830
LRU	Miss rate: 12% # of misses: 685	Miss rate: 48% # of misses: 2670	Miss rate: 48% # of misses: 2671
random	Miss rate: 21% # of misses: 1186	Miss rate: 46% # of misses: 2548	Miss rate: 48% # of misses: 2671

C2)

Set size	Med Block size = 16 Cache size =1024	Good Block size = 8 Cache size = 2048	Poor Block size = 2 Cache size = 128
1	Miss rate: 48% # of misses: 2670	Miss rate: 24% # of misses: 1341	Miss rate: 69% # of misses: 3830
2	Miss rate: 48% # of misses: 2670	Miss rate: 16% # of misses: 882	Miss rate: 69% # of misses: 3830
4	Miss rate: 48% # of misses: 2670	Miss rate: 13% # of misses: 707	Miss rate: 69% # of misses: 3830
8	Miss rate: 48% # of misses: 2670	Miss rate: 12% # of misses: 684	Miss rate: 69% # of misses: 3830