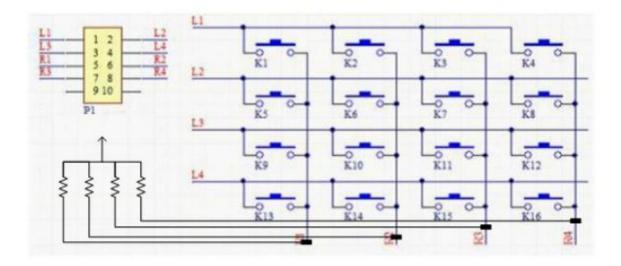
C:\Users\alper\Desktop\EE\EE4-1\EE 447\Lab2\Q3\q1.s

```
AREA
                                 main, READONLY, CODE
                    THUMB
                                 OutChar
                   EXTERN
                   EXPORT
                                 DELAY150
     DELAY150
                   PROC;
                                 R8,#6000000
                                                   ;Since each loop takes 4 cycles. # of iteration should be
                   MOV32
     2.400.000/4 = 600000.
     ;Since its clock is 16MHz. Each cycle will take 0.06us (1/16M sec) to operate
     ;To take 150ms (0.15 sec = 1.5*10^5 us),
;It should take (0.15sec)/((1/16M)sec) =2.4M cycle
10
                                                   ;Taking 1 cycle
;Taking 1 cycle
11
     delaying
                   NOP
                   SUBS
                                 R8,#1
12
13
                   BNE
                                 delaying
                                                    ;Taking 2 cycle
14
                   BX
                   AT.TGN
15
                   ENDP
16
                   END
```

2)

```
C:\Users\alper\Desktop\EE\EE4-1\EE 447\Lab2\Q2\q2.s
                                AREA
                                             main, READONLY, CODE
                                THUMB
    3
                                EXTERN
                                             DELAY150
                                EXTERN
                                             PB INIT
                                             __main
    5
                                EXPORT
    6
        PB INP
                                EQU 0x4000503C
         PB OUT
                                EQU 0x400053C0
   10
        GPIO_PORTB_DATA
                                EQU 0x400053FC; data a d d r e s s t o a l l pi n s
                                EQU 0x40005400
        GPIO PORTB DIR
GPIO PORTB AFSEL
   11
   12
                                EQU 0x40005420
   13
        GPIO_PORTB_DEN
                                EQU 0x40005510
        GPIO PORTB PDR
GPIO PORTB PUR
                                EQU 0x40005514
                                EQU 0x40005510
   15
        SYSCTL RCGCGPIO
                               EQU 0x400FE608; these are written in Week-6 Lecture Notes page 58
   16
        __main
                                PROC;
   19
                               BL
LDR
                                             PB_INIT
                                             R1,=GPIO_PORTB_DATA
R0,[R1]
   20
        nanInp
                                LDR
                                LSR
                                             R5,R0,#4
   23
24
25
                                LDR
                                             R0,[R1]
                                CMP
                                             R5,#0xE
                                BEO
                                             LED1
   26
                                             R5,#0xD
                                CMP
   27
                                BEQ
                                             LED2
   28
29
                                CMP
                                             R5,#0xB
                                BEO
                                             LED3
   30
                                CMP
                                             R5,#0x7
   31
                                BEQ
                                              LED4
                                             DELAY150
   32
                                _{\mathrm{BL}}
   33
                                В
                                             nanInp
        LED1
                                MOV
   34
                                             R2,R5
   35
                                             R1,=GPIO PORTB DATA
                                LDR
   36
                                STR
                                             R2, [R1]
   37
                                В
                                             nanInp
   38
   39
                                MOV
        LED2
                                             R2,R5 ;D
   40
                                LDR
                                             R1,=GPIO PORTB DATA
   41
                                STR
                                             R2,[R1]
   42
43
                                В
                                             nanInp
        LED3
                                MOV
                                             R2,R5
                                             R1,=GPIO PORTB DATA
R2,[R1]
   45
                                LDR
   46
                                STR
   47
                                             nanInp
   48
        LED4
                                MOV
                                             R1,=GPIO PORTB DATA
R2,[R1]
   50
                                LDR
   51
                                STR
   52
                                В
                                             nanInp
   53
                                ALIGN
   55
                                ENDP
```

a) When there is a push in any keys, the output data will also change. We used PB 0-3 as output and PB 4-7 as inputs to be able to do that we connected R 1-4 to PB 0-3 and L 1-4 to PB 4-7. We checked if the input pins are changed, and if they don't, it means there are no pushed buttons.



- b) If there is a button pressed, it will go to COLM1, COLM2, COLM3, and COLM4. After that input will be read again. If the input is changed that means the key is released.
- c) We shifted right the input and determined which COLM it belongs to. After that by compering the values of output we determined the ROW of the key.
- d) If we don't eliminate the bouncing effect, the results will be not acceptable. Results will be undetermined if we don't add any delay. To overcome this situation, we add a delay and after reading it will read again and checks if there is a change in data and continue to do the rest of the code.

```
:\Users\alper\Desktop\EE\EE4-1\EE 447\Lab2\Q3\Q3.s
                                AREA
                                              main, READONLY, CODE
                                THUMB
   2
                                EXTERN
                                              DELAY150
                                              PB INIT
                                EXTERN
                                              OutChar
                               EXPORT __main EQU 0x20000400
   6
        WRITE
        GPIO_PORTB_DATA
                                EQU 0x400053FC; data a d d r e s s t o a l l pi n s
                               EQU 0x40005400
EQU 0x40005420
       GPIO PORTB DIR
GPIO PORTB AFSEL
  10
  11
        GPIO PORTB DEN
                                EQU 0x40005510
       GPIO PORTB PUR
                               EQU 0x40005514
EQU 0x40005510
  12
  13
  14
       SYSCTL RCGCGPIO
                                EQU 0x400FE608 ; these are written in Week-6
  15
                                PROC
  16
         main
  17
18
                               BL
LDR
                                              PB INIT
                                                                     ; Port B init
                                              R2,=GPIO PORTB DATA
       start
  19
                                MOV
                                              R3, #0xF0
  20
21
                               MOV
                                              R0,#48
R7,#0
                                                                     ;Define R0 as 0
  22
                                STR
                                              R3, [R2]
  23
                                LDR
                                              R3. [R2]
                                                                     ;Debouncing Effect
  24
                                BL
                                              DELAY150
  25
                                LDR
                                              R4, [R2]
                                CMP
  26
                                              R2.R4
                                                                     ; If there is no debouncing , continue
                                              start
  28
                                LSR
                                             R5, R3, #4
                                                                     ; in this part of the code we search row and
       colum in only one loop
  29
                                CMP
                                              R5, #0xD
                                                                     ; to determine the number of button we use column
        number
  30
                                ADDEQ
                                              RO, #1
                                                                     ; if pressed button is in R1 we add to R2 1
                               CMP
ADDEQ
  31
                                              R5, #0xB
                                                                     ;if pressed button is in R1 we add to R3 2
  32
                                              R0,#2
R5,#0x7
  33
                                CMP
                                                                     ;if pressed button is in Rl we add to R4 3
                                ADDEQ
CMP
                                              R0,#3
R5,#0xF
  34
  35
                                                                     ;Determine button is being pressed ;If there is button pressed continue
  36
                                BNE
                                              ROW_finder
  37
                                В
                                              start
  38
  39
  40
       ROW finder
                               MOV
                                             R6,#0x7
                                                                     ;ROW1 Which means L4
                                STR
  41
                                             R6, [R2]
  42
                                NOP
  43
                                NOP
                                NOP
  44
  45
                                LDR
                                              R7,[R2]
                                LSR
                                             R7,R7,#4
R7,R5
  46
                                                                      ;Output is taken
  47
                                CMP
                                                                      ; If output is same with R5 we can assure it is
                                ADDEQ
  48
                                             R0,#12
R6,#0xB;
                                                                     ;Since we are on L4, we should add 12 to the R0 \,
                                MOV
  49
                                                                     :Same process continues
                               STR
  50
                                              R6, [R2]
  51
                                NOP
NOP
  52
  53
                                LDR
                                             R7,[R2]
R7,R7,#4
R7,R5
  54
                                LSR
CMP
  55
  56
  57
                                ADDEQ
                                              RO,#8
                               MOV
STR
  58
                                              R6,#0xD
  59
                                             R6, [R2]
  60
                                NOP
                               NOP
NOP
  61
  62
  63
                                LDR
LSR
                                              R7, [R2]
  64
65
                                              R7,R7,#4
R7,R5
                                CMP
  66
                                ADDEQ
                                              RO,#4
  67
68
                                MOV
                                              R6, #0xE
                                STR
                                              R6, [R2]
  69
                               NOP
NOP
  70
  71
                                NOP
                                             R7,[R2]
R7,R7,#4
  72
73
                                LDR
LSR
                                CMP
                                              R7,R5
```

$\verb|C:\Users\alper\Desktop\EE\EE4-1\EE 447\Lab2\Q3\Q3.s|$

```
75
76
77
78
79
80
                                        ADDEQ
                                                           R0,#0
R8,=0xF0
R8,[R2]
                                        LDR
                                        NOP
                                        NOP
                                        NOP
81
                                        LDR
                                                           R9,[R2]
                                                                                         ;This function determines the if button is keep
        being pressed or not
82
                                        NOP
83
84
85
                                        NOP
NOP
                                        CMP
                                                           R9,R8
       CMP R9,R8
BNE out
CMP R0,#58 ;We have done lots of addition but we did not take care of letters. If R0 ig bigger that 9+48, we should add 7 to get letters
BCC noletter
ADD R0,#7
noletter BL OutChar
86
87
88
89
90
91
92
93
94
                                        B
ALIGN
                                                           start
                                        ENDP
```