```
/*Pulse init.h file
     Function for creating a pulse train using interrupts
 3
    Uses Channel 0, and a 1Mhz Timer clock ( TAPR = 15)
    Uses TimerOA to create pulse train on PF2
    #include "TM4C123GH6PM.h"
 7
 8
    void pulse init(void);
 9
    void TIMEROA Handler (void);
    void detect_init (void);
10
11
1.2
    #define LOW
                  60
13
    #define HIGH 15
14
15
    void pulse init(void) {
      volatile int *NVIC EN0 = (volatile int*) 0xE000E100;
16
17
      volatile int *NVIC PRI4 = (volatile int*) 0xE000E410;
18
      SYSCTL->RCGCGPIO |= 0x20; // turn on bus clock for GPIOF
      __ASM("NOP");
19
       __ASM("NOP");
20
       __ASM("NOP");
21
22
23
      GPIOF->DIR
                      |= 0x04; //set PF2 as output
24
      GPIOF->AFSEL
                       &= (0xFFFFFFFB); // Regular port function
                       &= 0xFFFFF0FF; // No alternate function
25
       GPIOF->PCTL
26
                       =0; //Disable analog
      GPIOF->AMSEL
                       |=0\times04; // Enable port digital
27
      GPIOF->DEN
28
29
      //GPIOF->DIR
                           \mid = 0x08; //set GREEN pin as a digital output pin
30
      //GPIOF->DEN
                           |= 0x08; // Enable PF3 pin as a digital pin
31
32
      SYSCTL->RCGCTIMER |=0x01; // Start timer0
      ___ASM("NOP");
33
      __ASM("NOP");
34
35
        ASM("NOP");
36
       TIMER0->CTL
                       &=0xFFFFFFFE; //Disable timer during setup
                       =0 \times 04; //Set 16 bit mode
37
      TIMER0->CFG
                       =0x02; // set to periodic, count down
38
       TIMERO->TAMR
      TIMERO->TAILR =LOW; //Set interval load as LOW
39
40
      TIMERO->TAPR
                       =15; // Divide the clock by 16 to get 1us
41
       TIMERO->IMR
                       =0x01; //Enable timeout intrrupt
42
43
      //TimerOA is interrupt 19
44
      //Interrupt 16-19 are handled by NVIC register PRI4
45
       //Interrupt 19 is controlled by bits 31:29 of PRI4
46
       *NVIC_PRI4 &=0x00FFFFFF; //Clear interrupt 19 priority
       *NVIC_PRI4 |=0x40000000; //Set interrupt 19 priority to 2
47
48
49
       //NVIC has to be neabled
50
      //Interrupts 0-31 are handled by NVIC register EN0
51
       //Interrupt 19 is controlled by bit 19 \,
52
       *NVIC ENO |=0 \times 000800000;
53
54
       //Enable timer
55
       TIMER0->CTL
                        |=0x03; // bit0 to enable and bit 1 to stall on debug
56
       return;
57
58
59
    void TIMEROA Handler (void) {
      GPIOF->DATA ^= 4; //toggle PF2 pin
61
62
      if (TIMERO->TAILR==LOW)
63
        TIMERO->TAILR=HIGH;
       else
64
65
        TIMERO->TAILR=LOW;
66
       TIMER0->ICR |=0\times01;
      return;
67
68
     }
69
    int main()
70
71
      pulse init();
72
       while(1){}
73
74
```