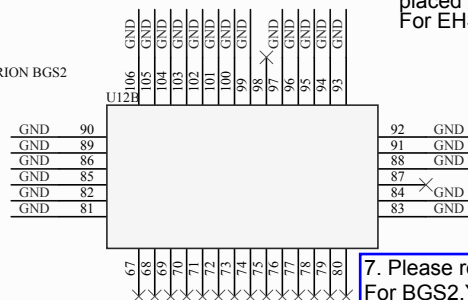
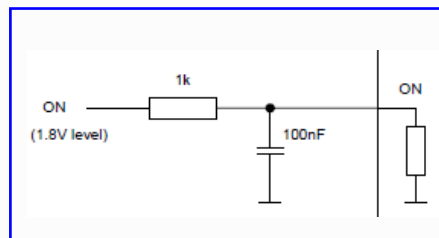
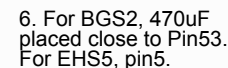
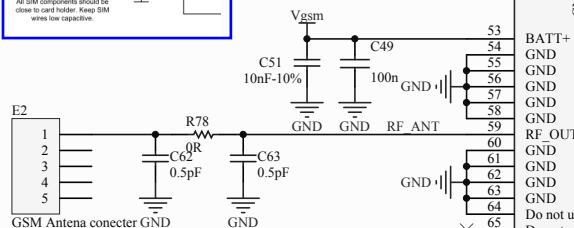
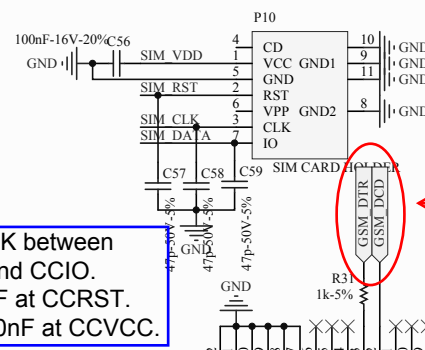


2. Add 4.7K between CCVCC and CCIO.
3. Add 1nF at CCRST.
4. Add 220nF at CCVCC.



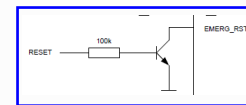
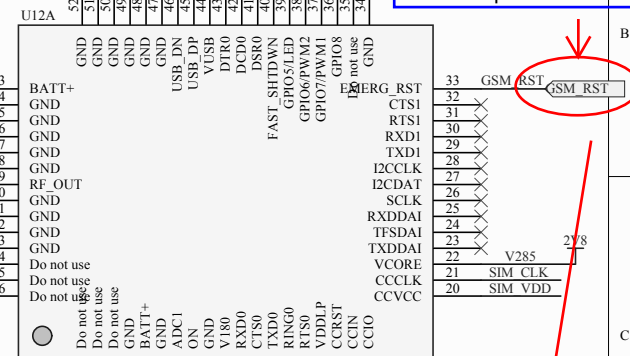
7. Please remove R35 10K.
For BGS2, You can
implement by this circuit.



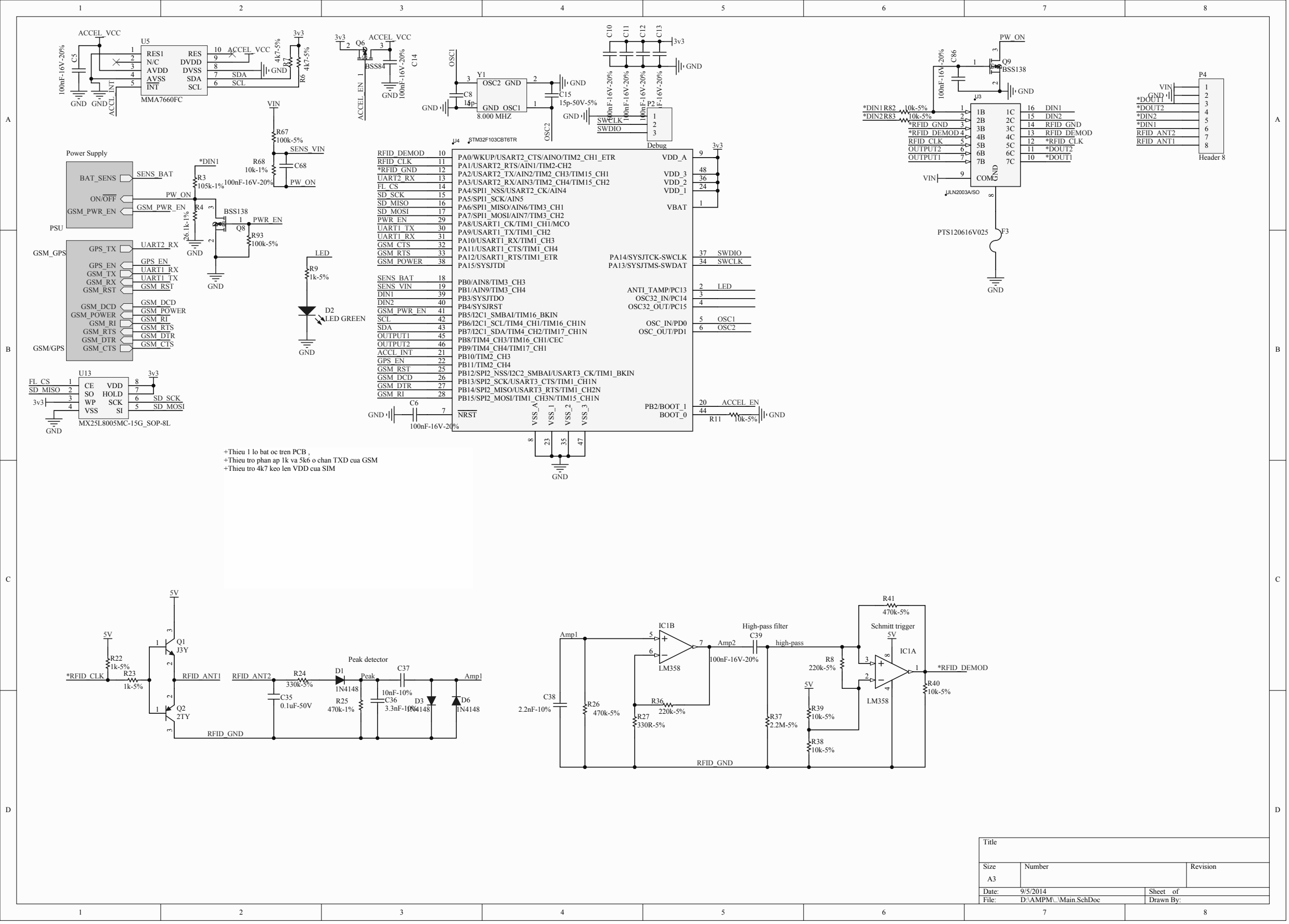
1. Please be aware these pins only are available in rel2.

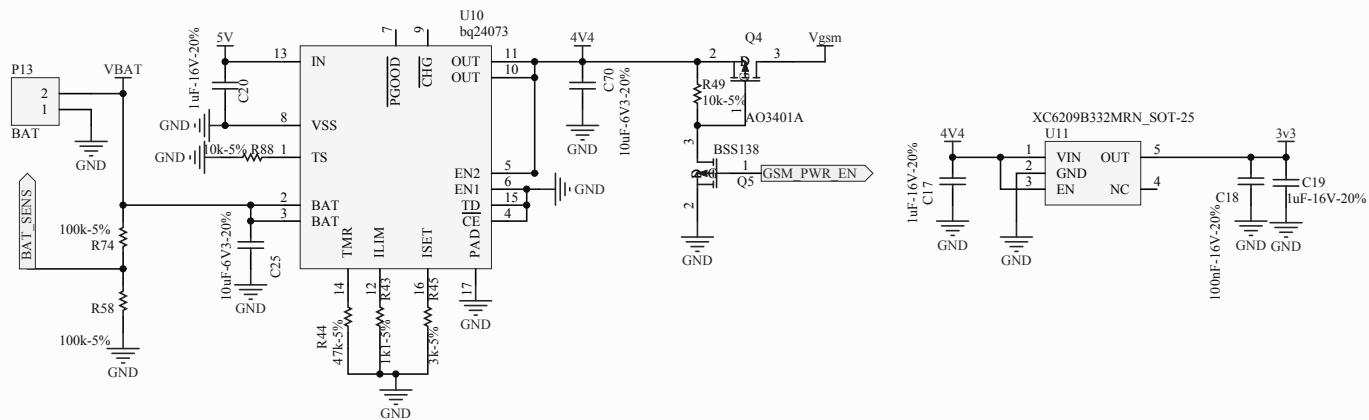
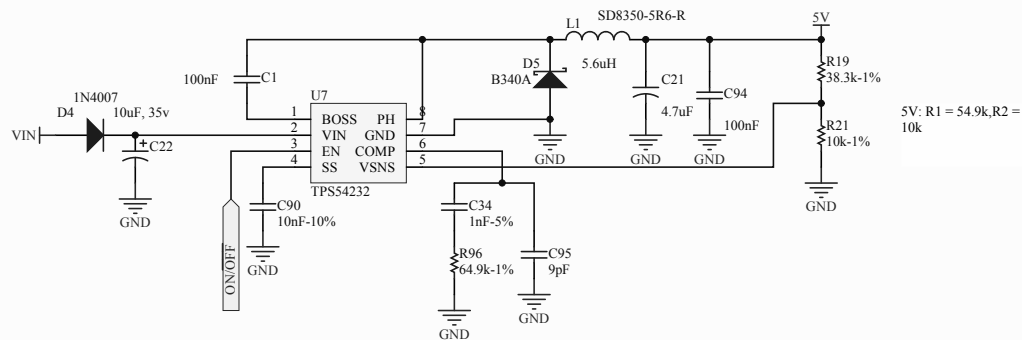
Mapping Table		
	BGS2r1	BGS2r2
DCDO	26	42
DTR0	25	43

5. EMER_RST is better to be driven low by open drain or open collector.



Title		10k-5%	
Size	Number	Revision	
A4			
Date:	9/5/2014	Sheet of	
File:	D:\AMPM\GSM	Drawn By:	
GPS.SchDoe			





Title			
Size A4	Number		Revision
Date:	9/5/2014	Sheet	of
File:	D:\AMPM\PSU.SchDoc	Drawn By:	