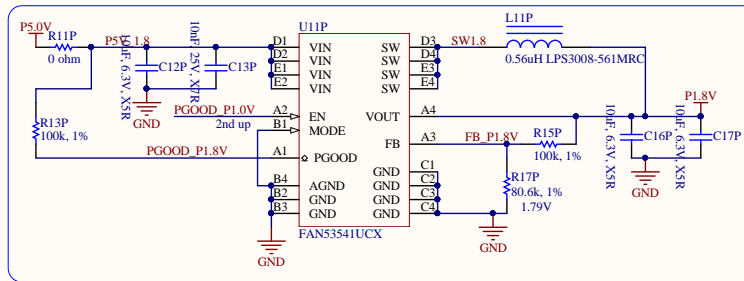
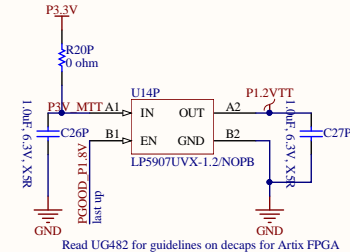
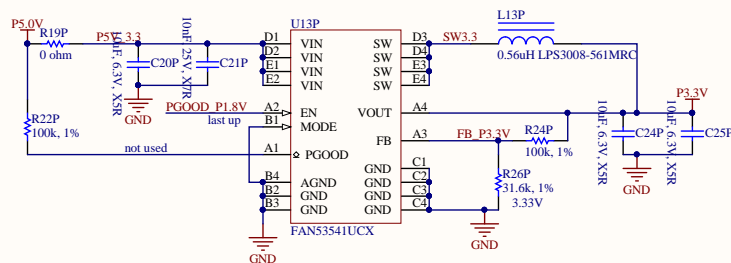
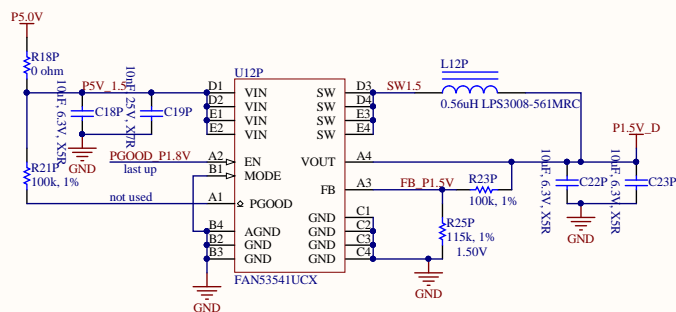


Power-on domain #1 - VCCINT/VCCBRAM



Power-on domain #2 - VCCAUX

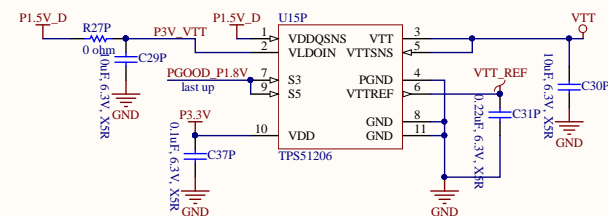
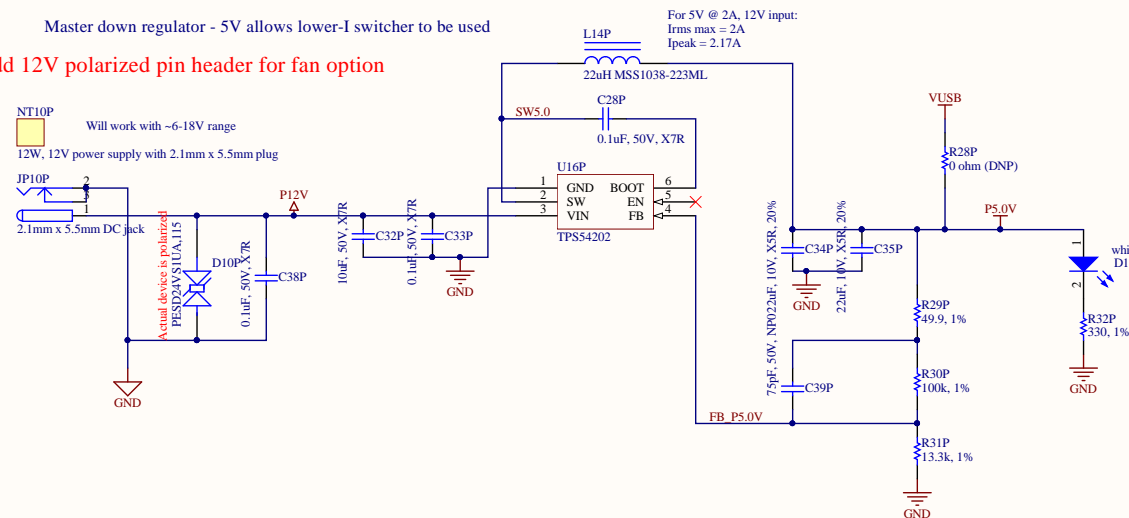
Power-on domain #3 - VCCO



Read UG482 for guidelines on decaps for Artix FPGA

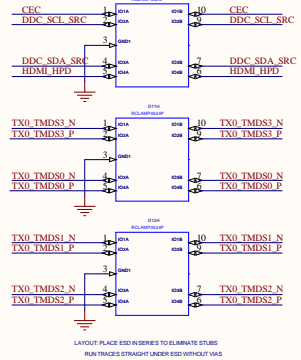
Master down regulator - 5V allows lower-I switcher to be used

Add 12V polarized pin header for fan option

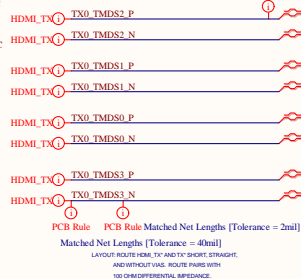


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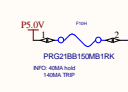
HDMI ESD



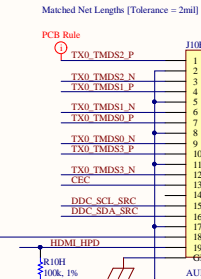
HDMI EMI



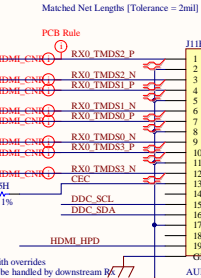
HDMI +5V fuse



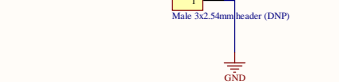
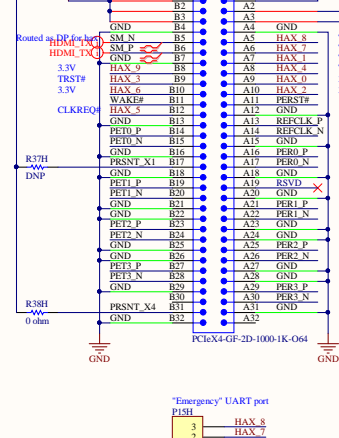
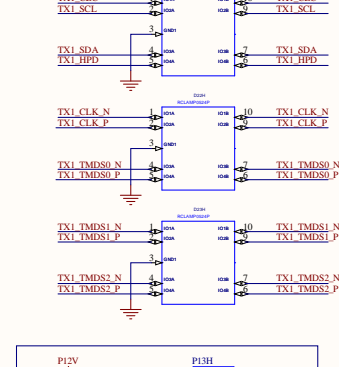
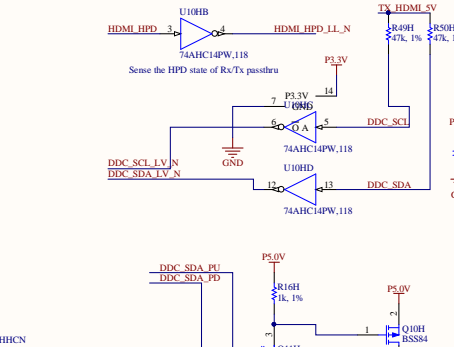
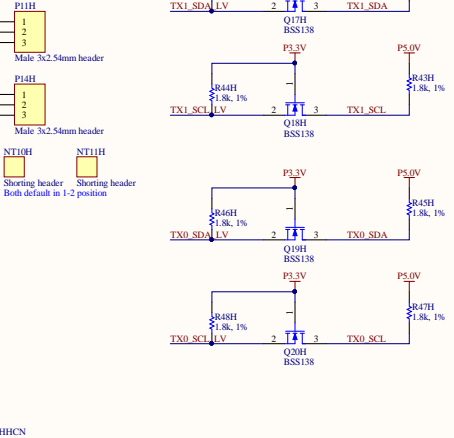
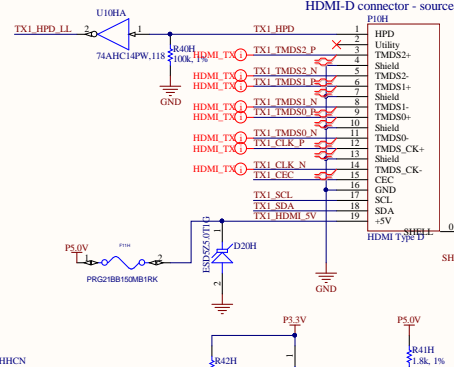
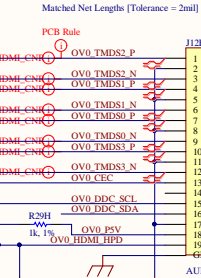
HDMI-A connector - source



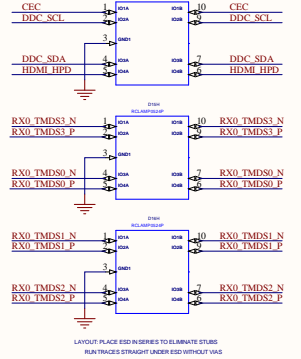
HDMI-A connector - sink



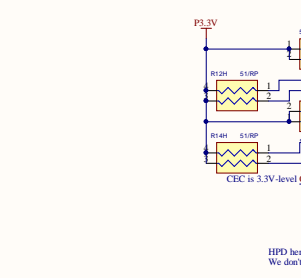
HDMI-A connector - sink



HDMI ESD



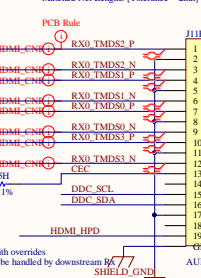
HDMI EMI



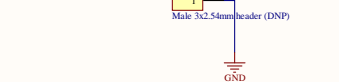
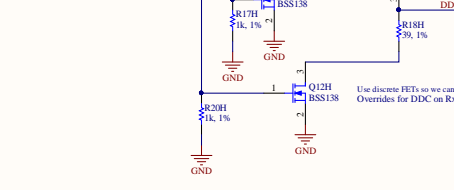
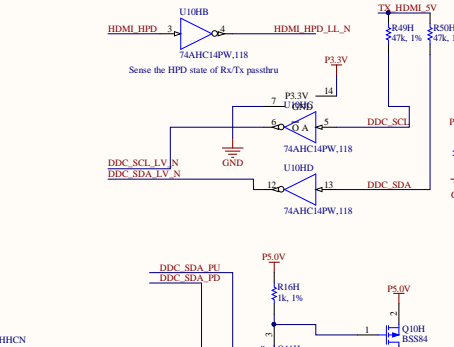
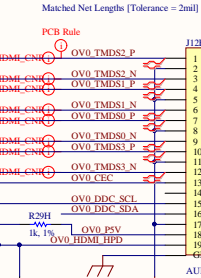
HDMI +5V fuse



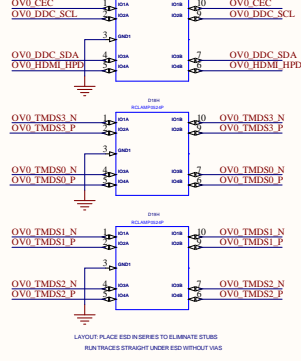
HDMI-A connector - source



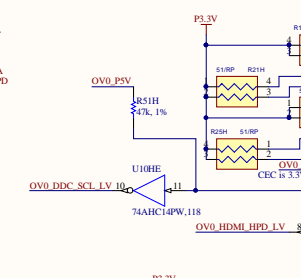
HDMI-A connector - sink



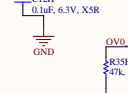
HDMI ESD



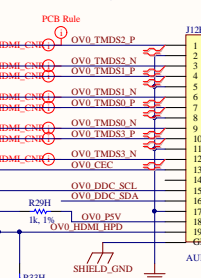
HDMI EMI



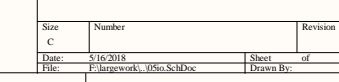
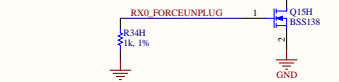
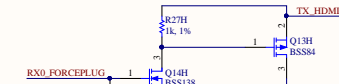
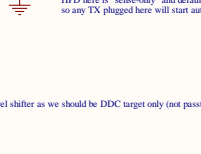
HDMI +5V fuse



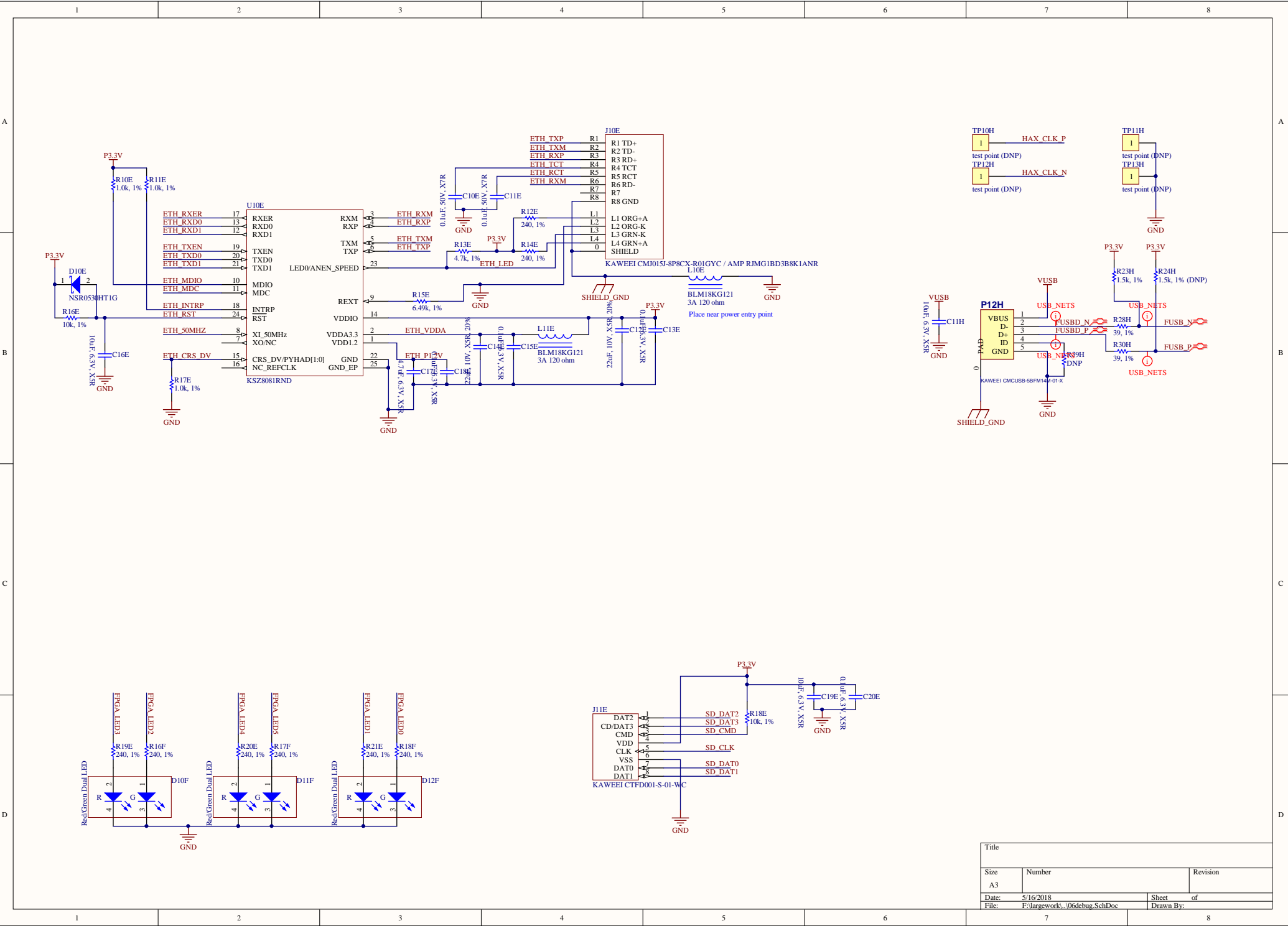
HDMI-A connector - source



HDMI-A connector - sink



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