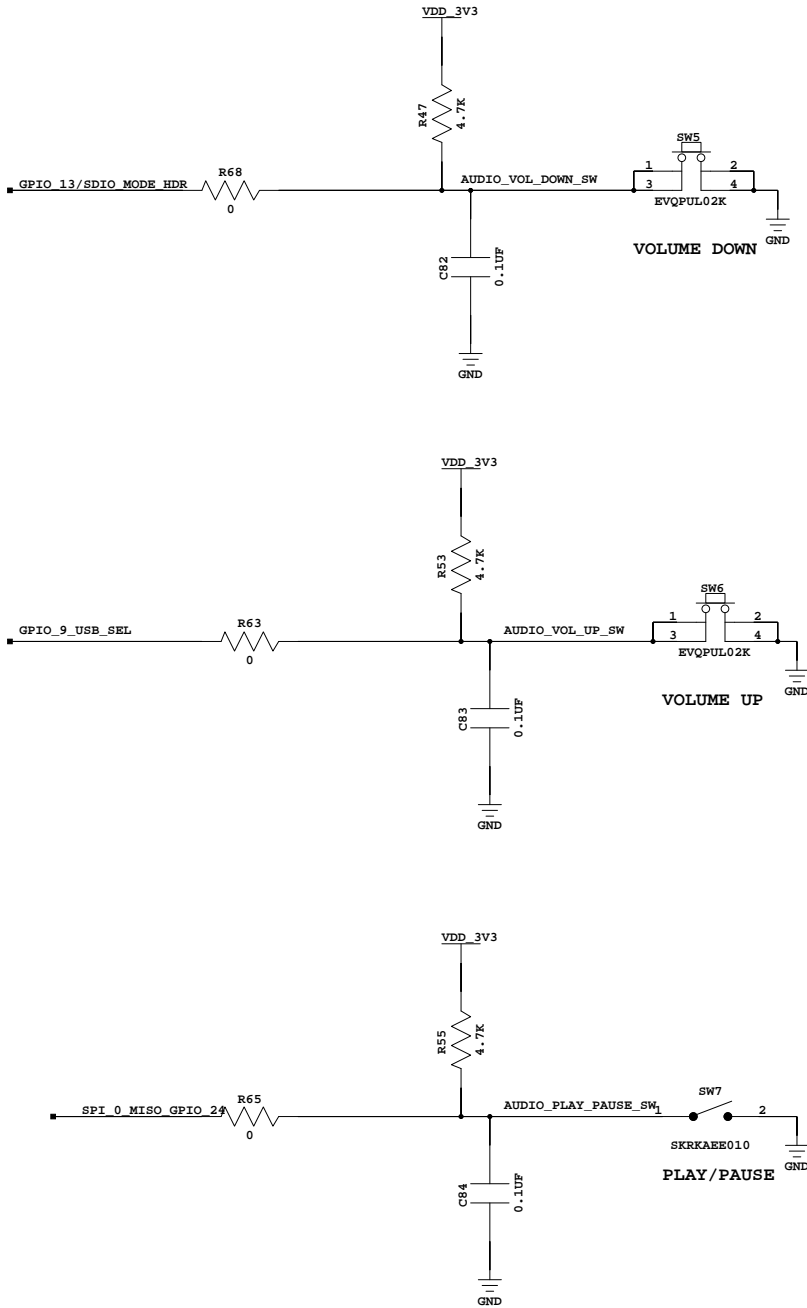
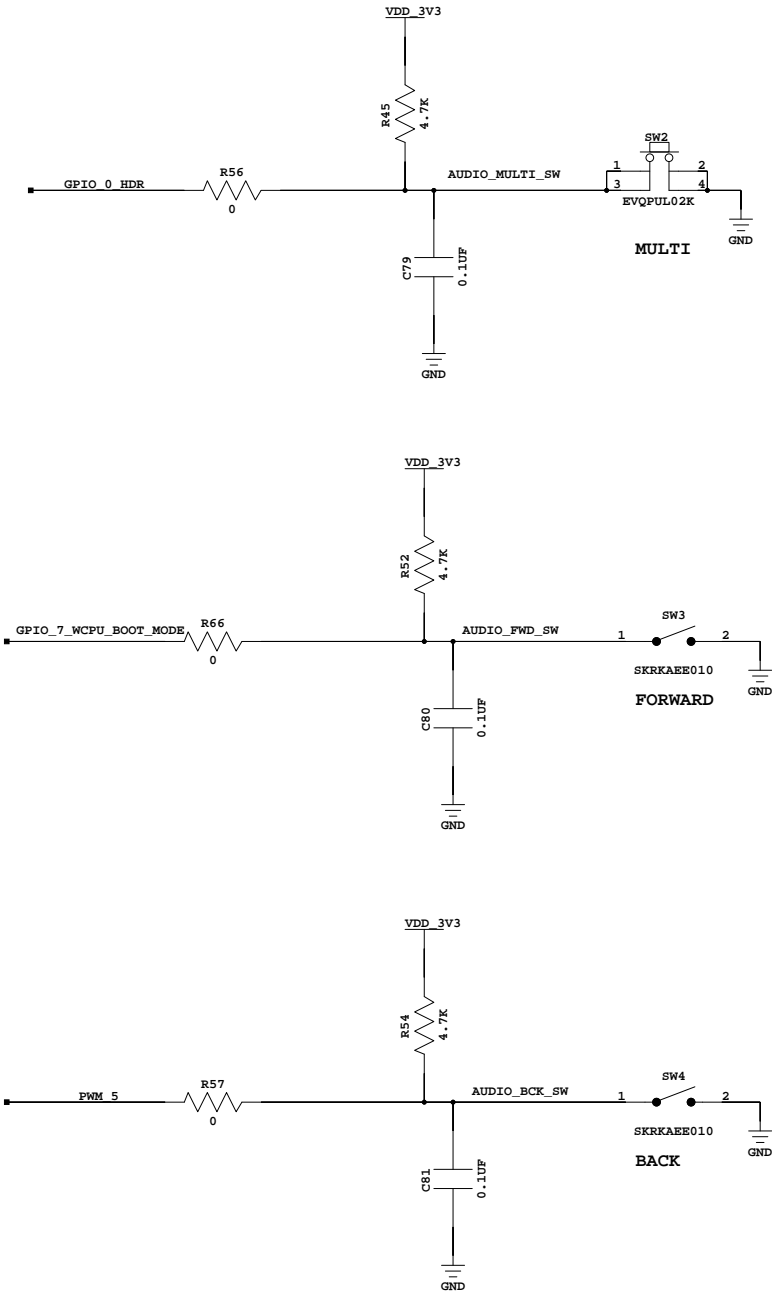


AUDIO SWITCHES ARRAY

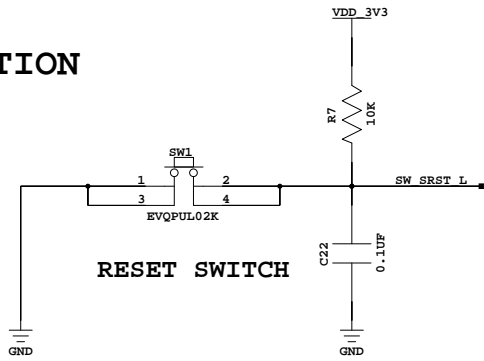


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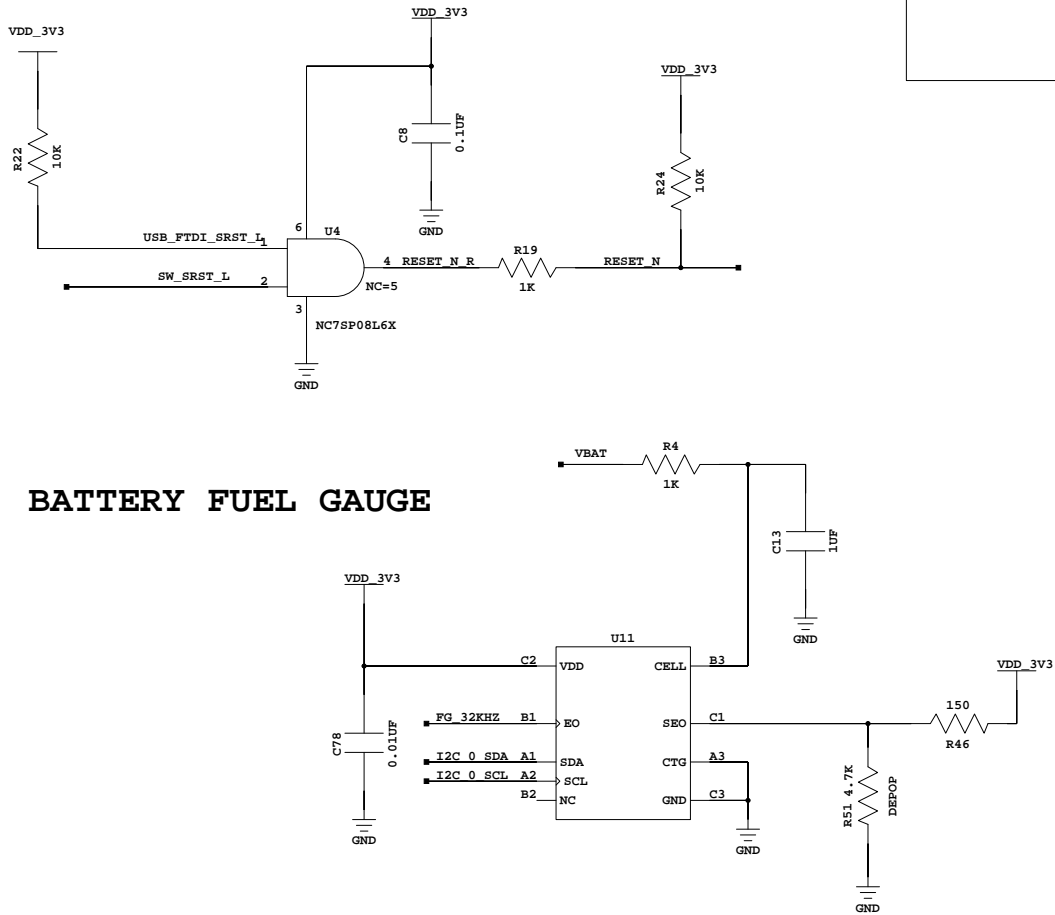
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SIZE	TITLE	
B	CYW943907WAE3	1
DATE	17/11/2016:19:47	SHEET 2 OF 7

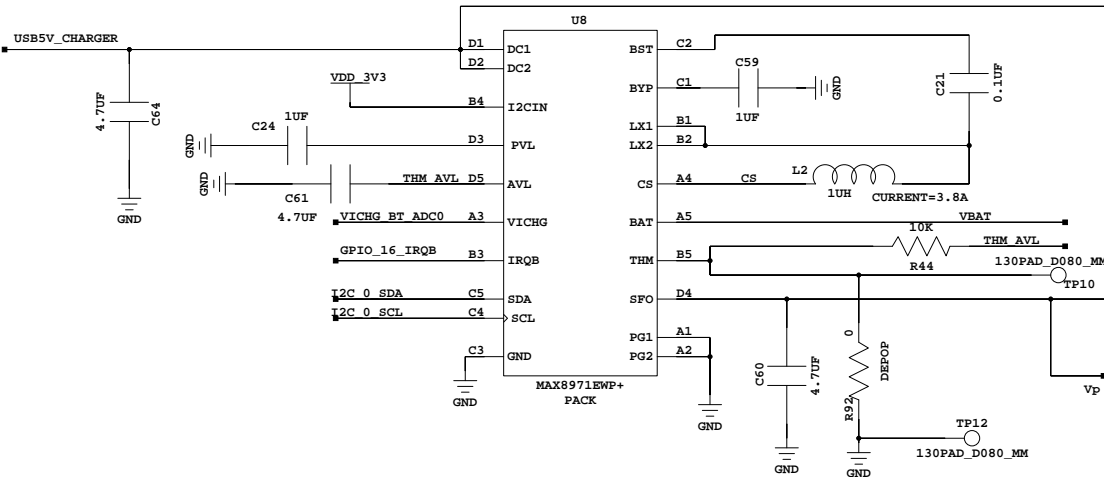
RESET FUNCTION



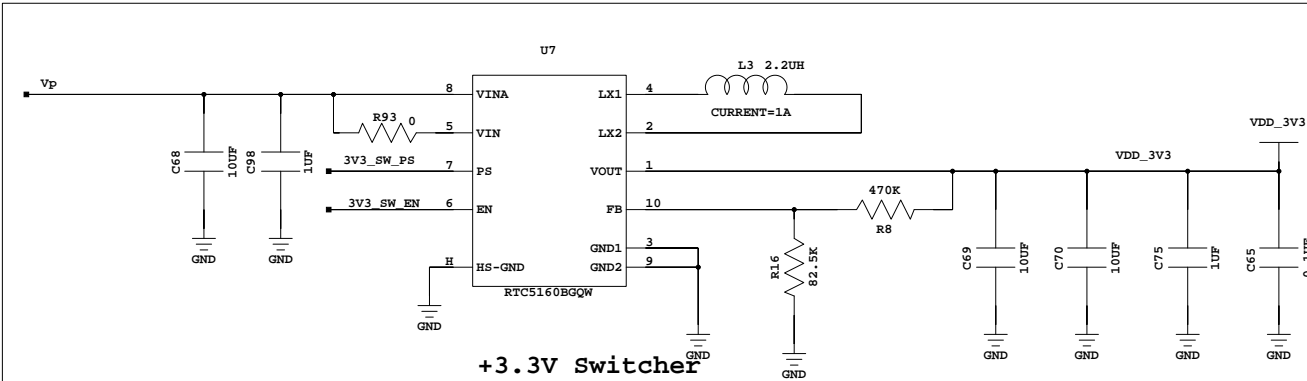
BATTERY FUEL GAUGE



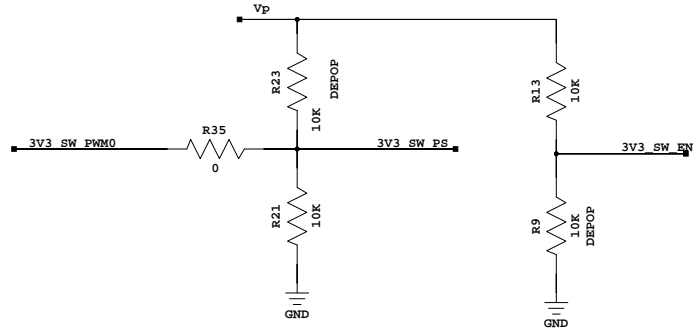
BATTERY CHARGER



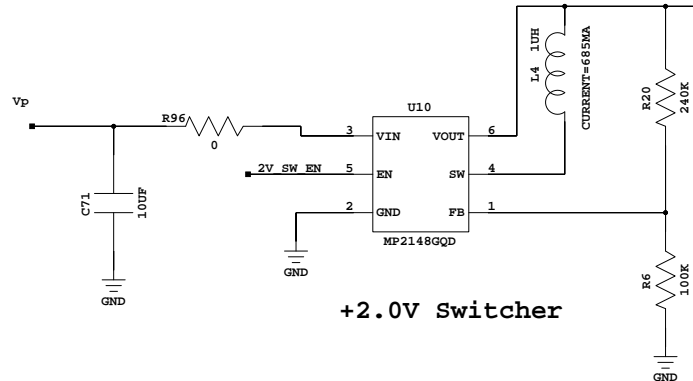
+3.3V Switcher



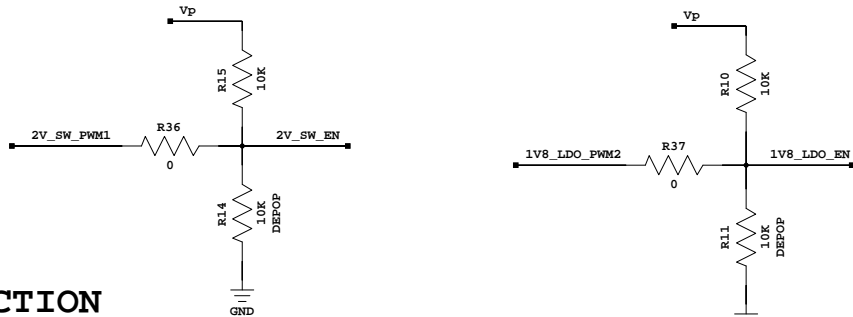
+3.3V SECTION



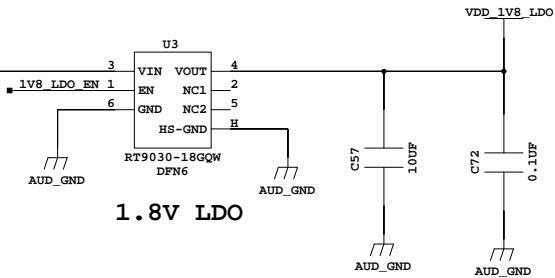
+2.0V Switcher



+1.8V SECTION



1.8V LDO



Power Supplies

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SIZE	TITLE
B	CYW943907WAE3
DATE	17/11/2016:19:43
SHEET	3 OF 7

D

C

B

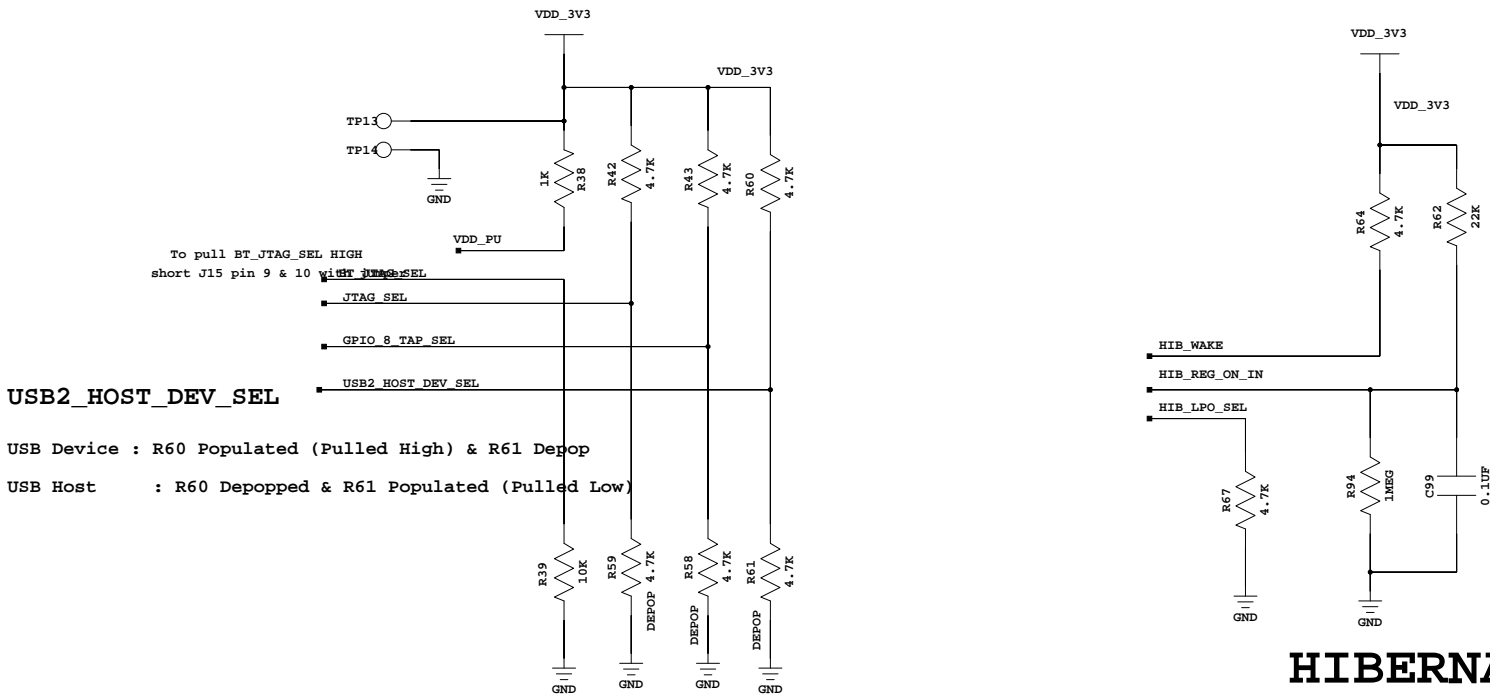
A

D

C

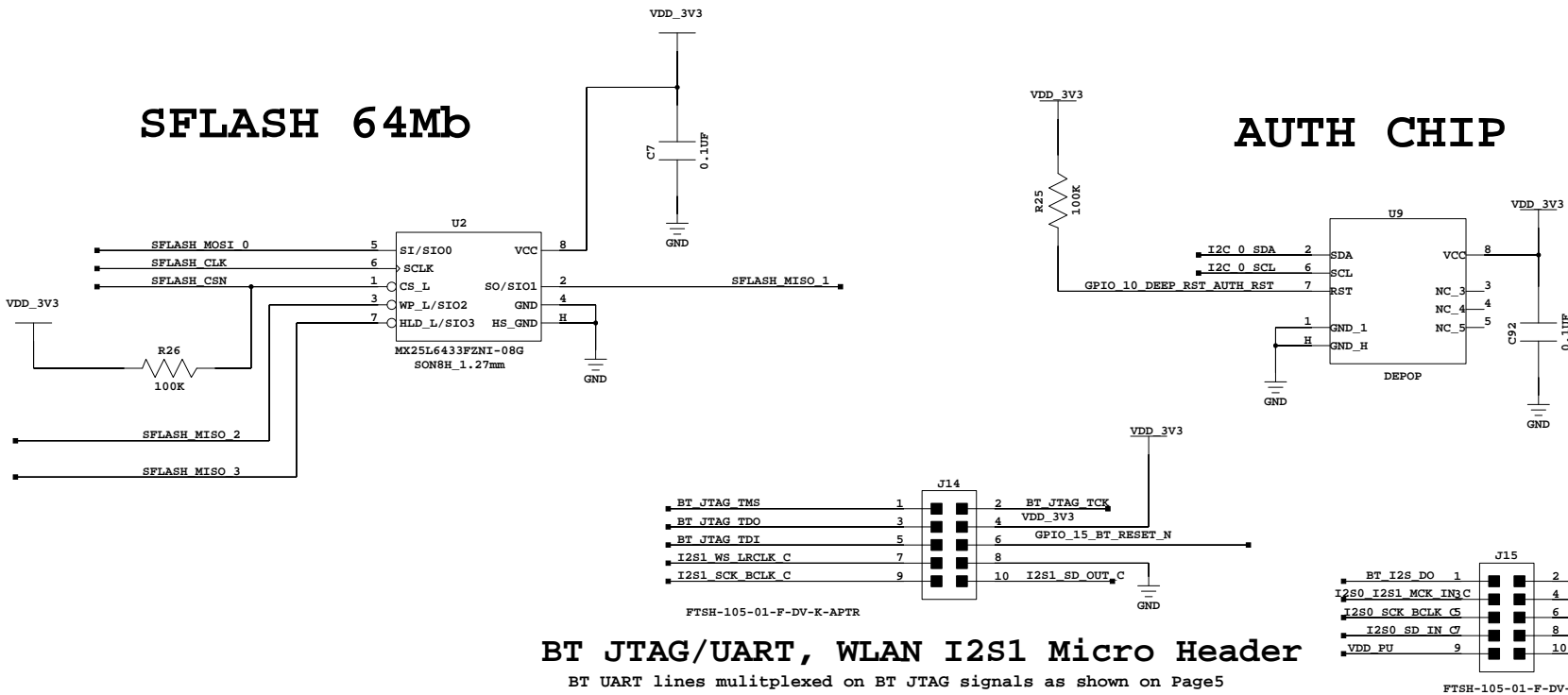
B

A

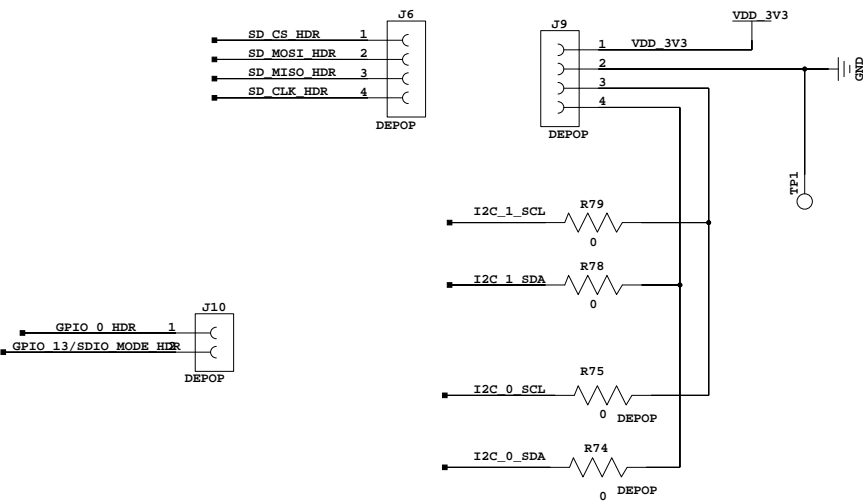


Pin	Strap Function	Strap Pull	
		Chip Default	Board Default
GPIO_1	gSPI Mode	0	
GPIO_7	WCPU Boot Mode0 := TCROM Boot 1 = TCMSRAM Boot	0	1 REFER TO AUDIO SWITCHES ARRAY
GPIO_9	USB/HSIC Sel :0 = HSIC PHY 1 = USB PHY	0	1 REFER TO AUDIO SWITCHES ARRAY
GPIO_11	ACPU Boot Mode0 := SOCRAM Boot 1 = SOCSRAM Boot	0	
GPIO_13	SDIO Mode : 0 = SDIO Device 1 = SDIO Host	0	
RF_SW_CTRL_5	Host DAP Clock Sel	0	
RF_SW_CTRL_7	Host RSRC Init	0	

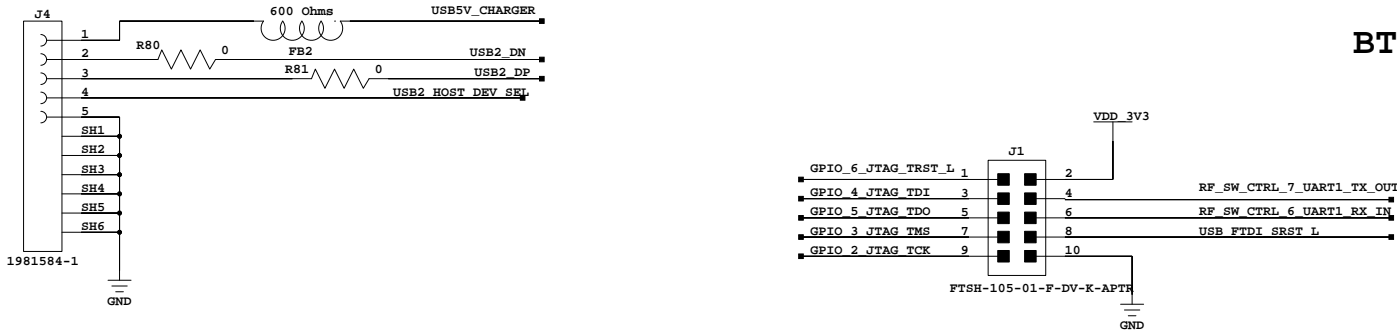
HIBERNATION STRAPPING



Display 2.54mm Header



BT I2S, WLAN I2S0 Micro Header



Micro USB WLAN JTAG/UART1 Micro Header

Straps + SFLASH + MI

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SIZE	TITLE		
B	CQW943907WAE3		
DATE	13/12/2016:11:16	SHEET	4 OF 7

D

C

B

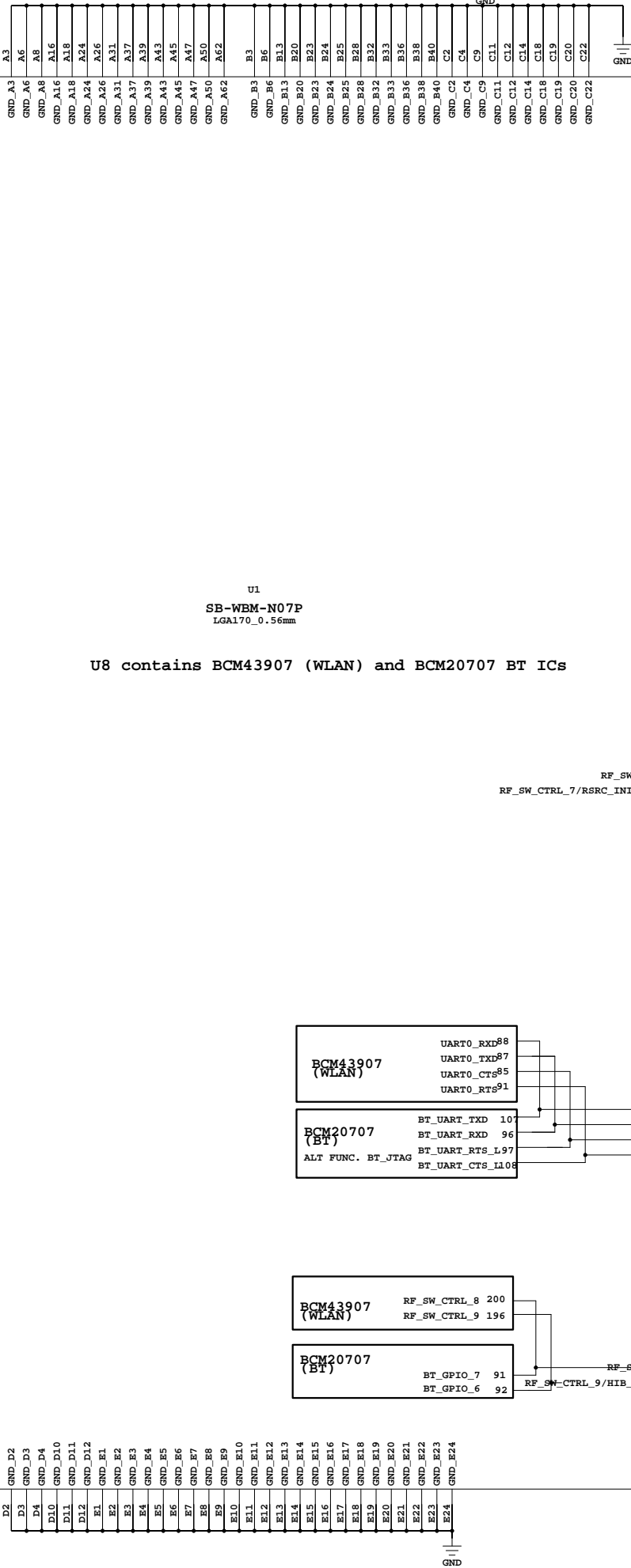
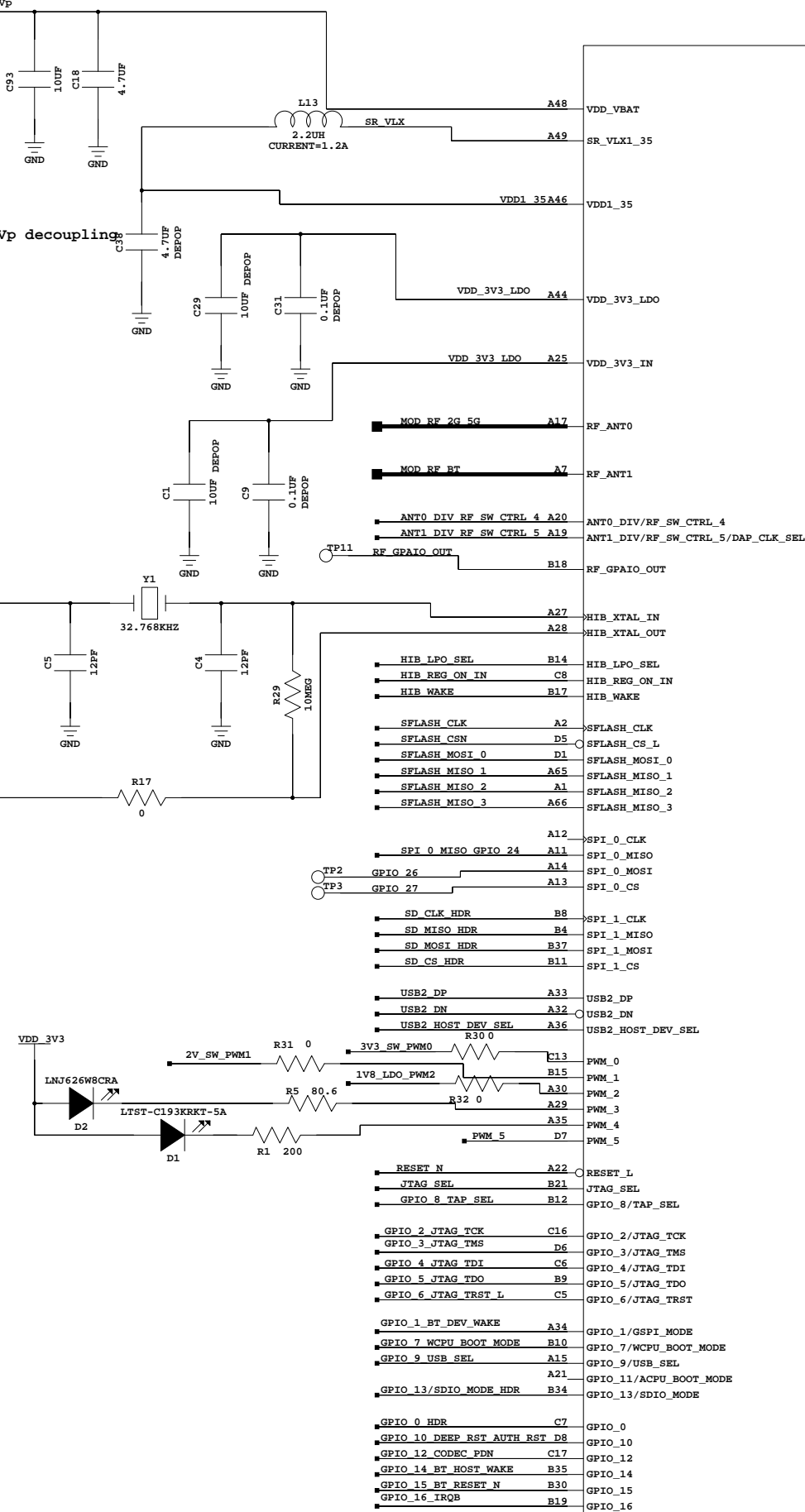
A

D

C

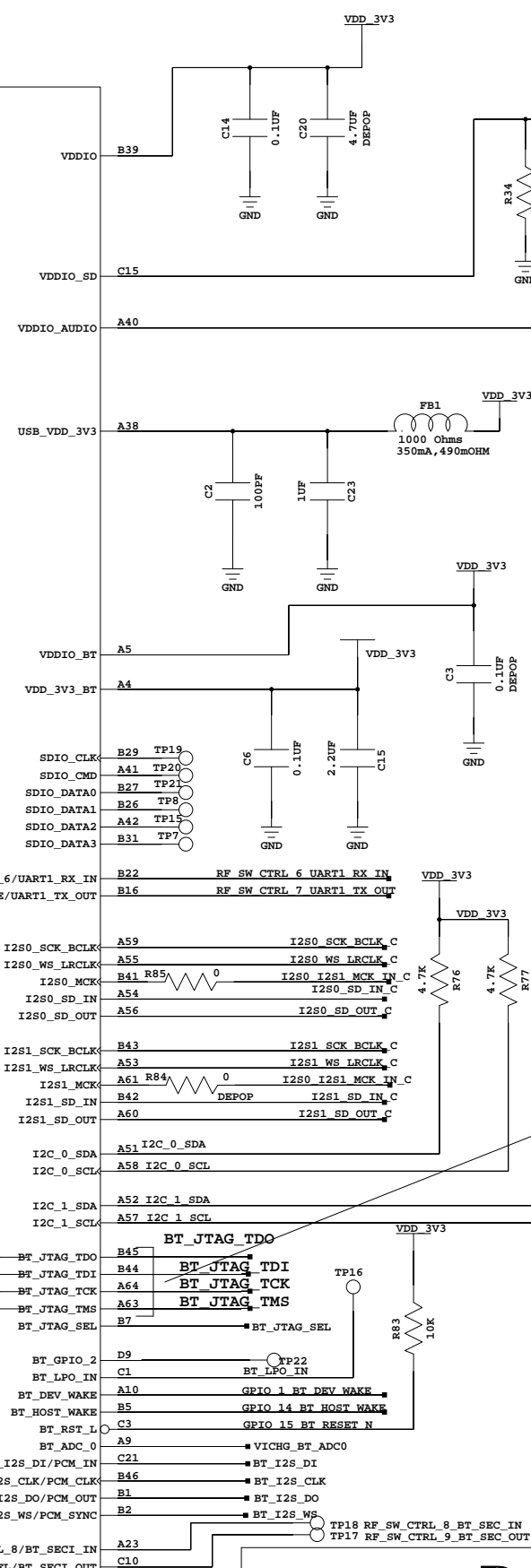
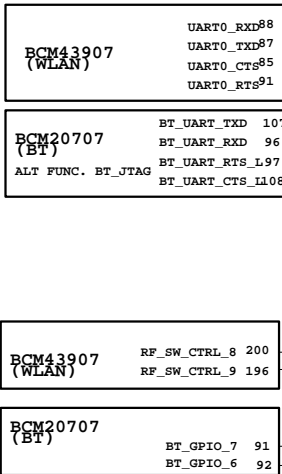
B

A

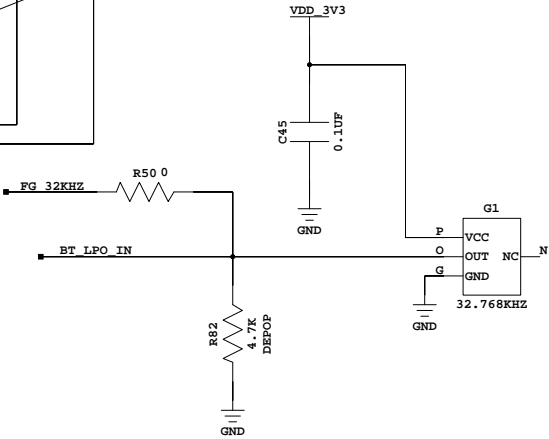


U1
SB-WBM-N07P
LGA170_0.56mm

U8 contains BCM43907 (WLAN) and BCM20707 BT ICs

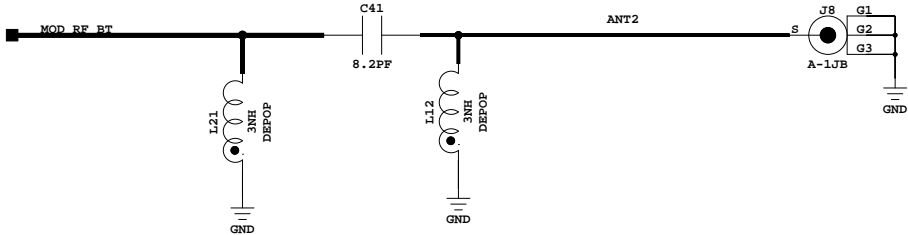
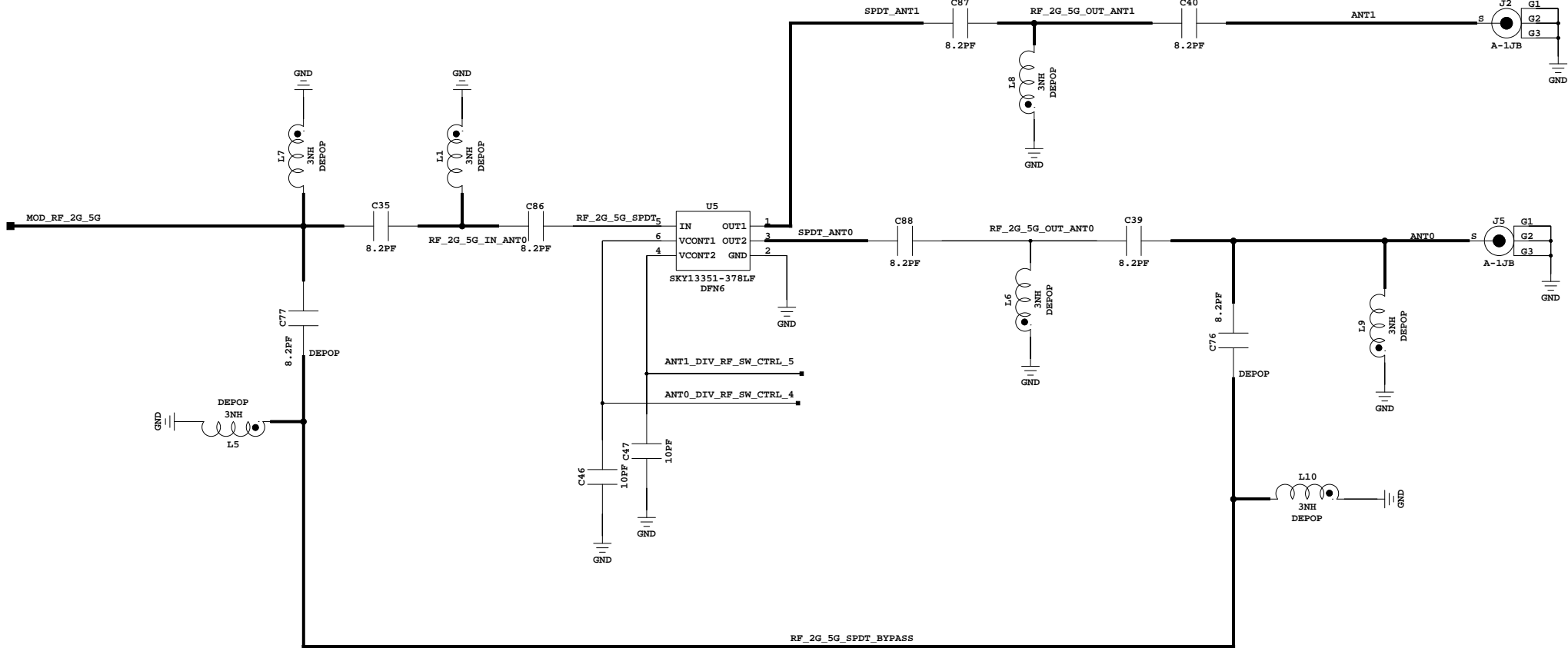


When WLAN is in reset, BT uart may be used.
Likewise, when BT is in reset, WLAN uart may be used.
Leave this interface unconnected when both WLAN and BT



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SIZE	TITLE	DATE	SHEET	OF	7
B	CYW943907WAE3	13/12/2016:18:19	5	OF	7



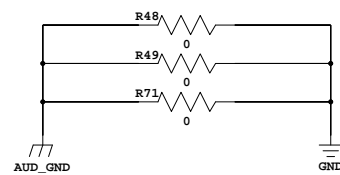
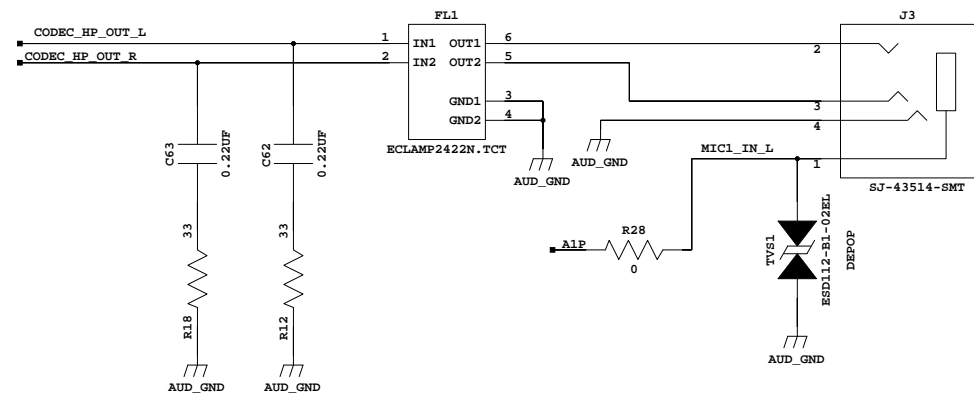
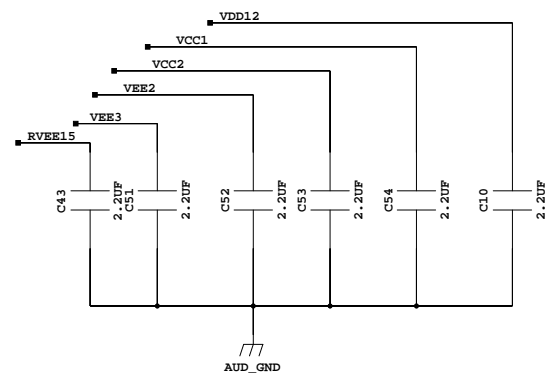
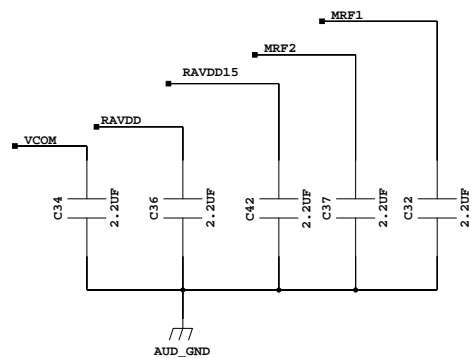
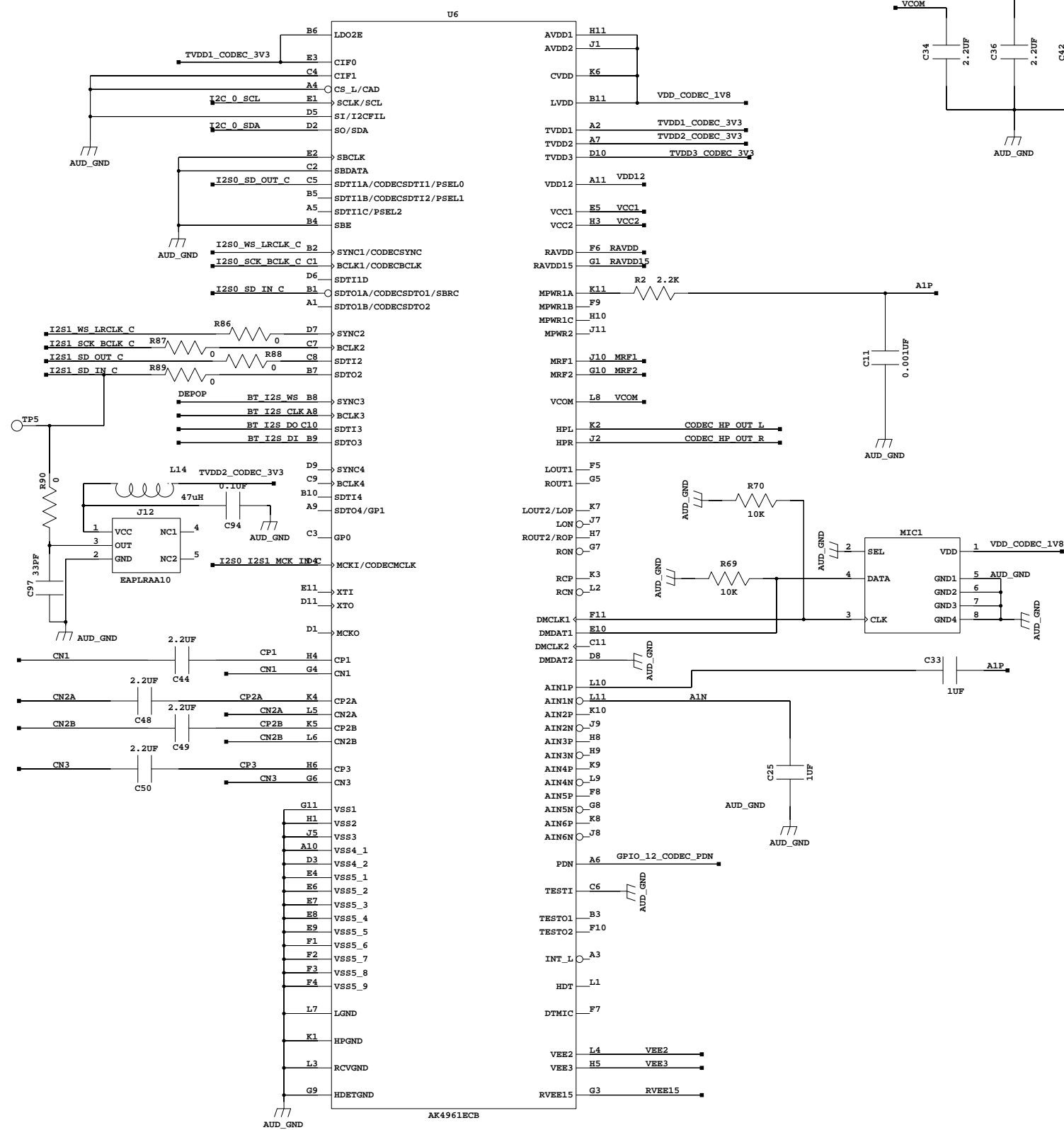
U5 2G/5G SPDT Antenna Diversity Switch Control Table		
MODE OF OPERATION Ant Diversity	ANT0_DIV_RF_SW_CTRL_4 SPDT (U5 VCONT1)	ANT1_DIV_RF_SW_CTRL_5 DPDT (U5 VCONT2)
RF_2G_5G to Ant0	1	0
RF_2G_5G to Ant1	0	1

ALL OTHER STATES UNDEFINED

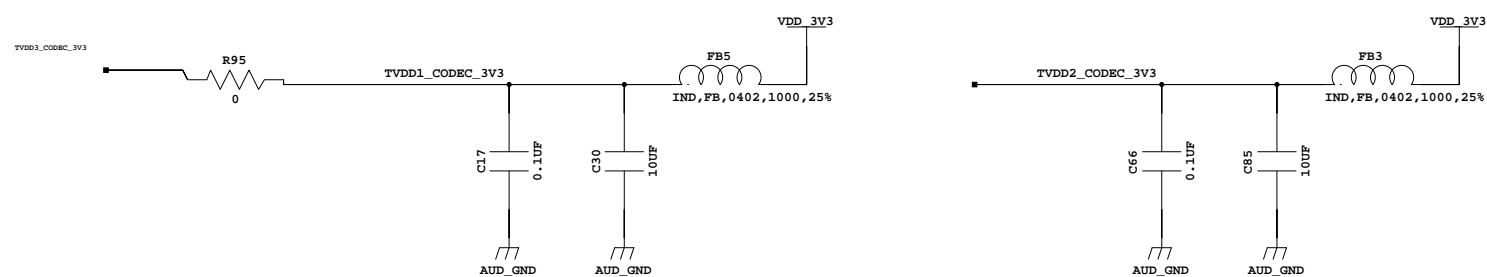
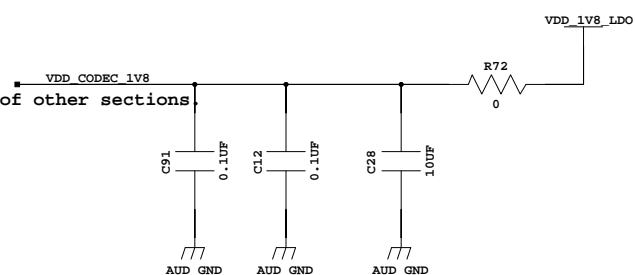
ANTENNA DIVERSITY

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SIZE	TITLE
B	CYW943907WAE3
DATE	17/11/2016:19:43
SHEET	6 OF 7



Audio Section has a separate Audio GND to isolate from noise of other sections
It connects to common GND (GND) with a 0 Ohms Resistors



AUDIO CODEC

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SIZE	TITLE		
B	CYW943907WAE3 1		
DATE	13/12/2016:18:18	SHEET	7 OF 7