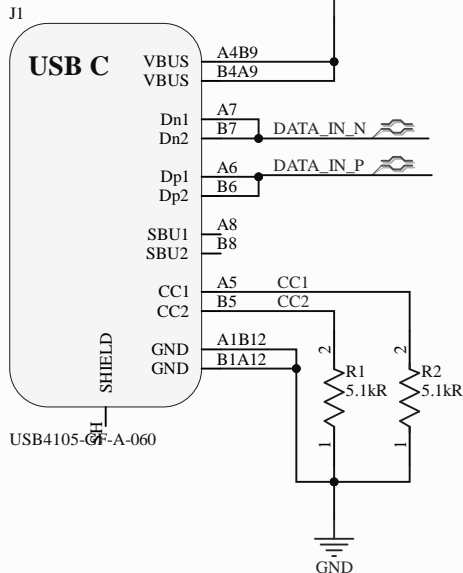
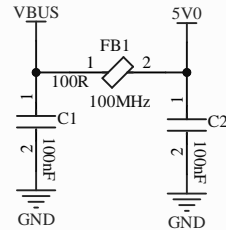


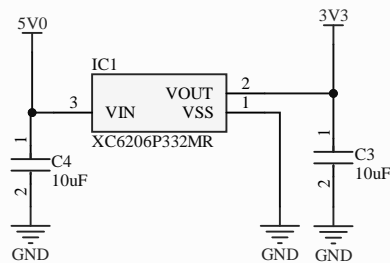
# UCB\_Type C



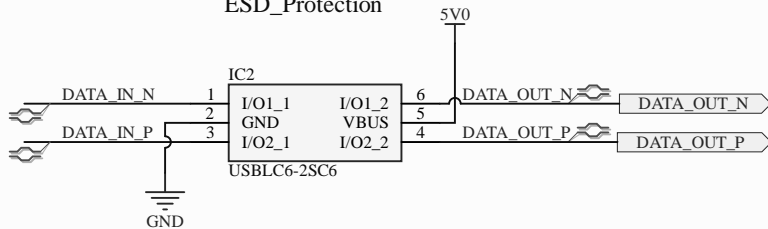
# Pi\_Filter



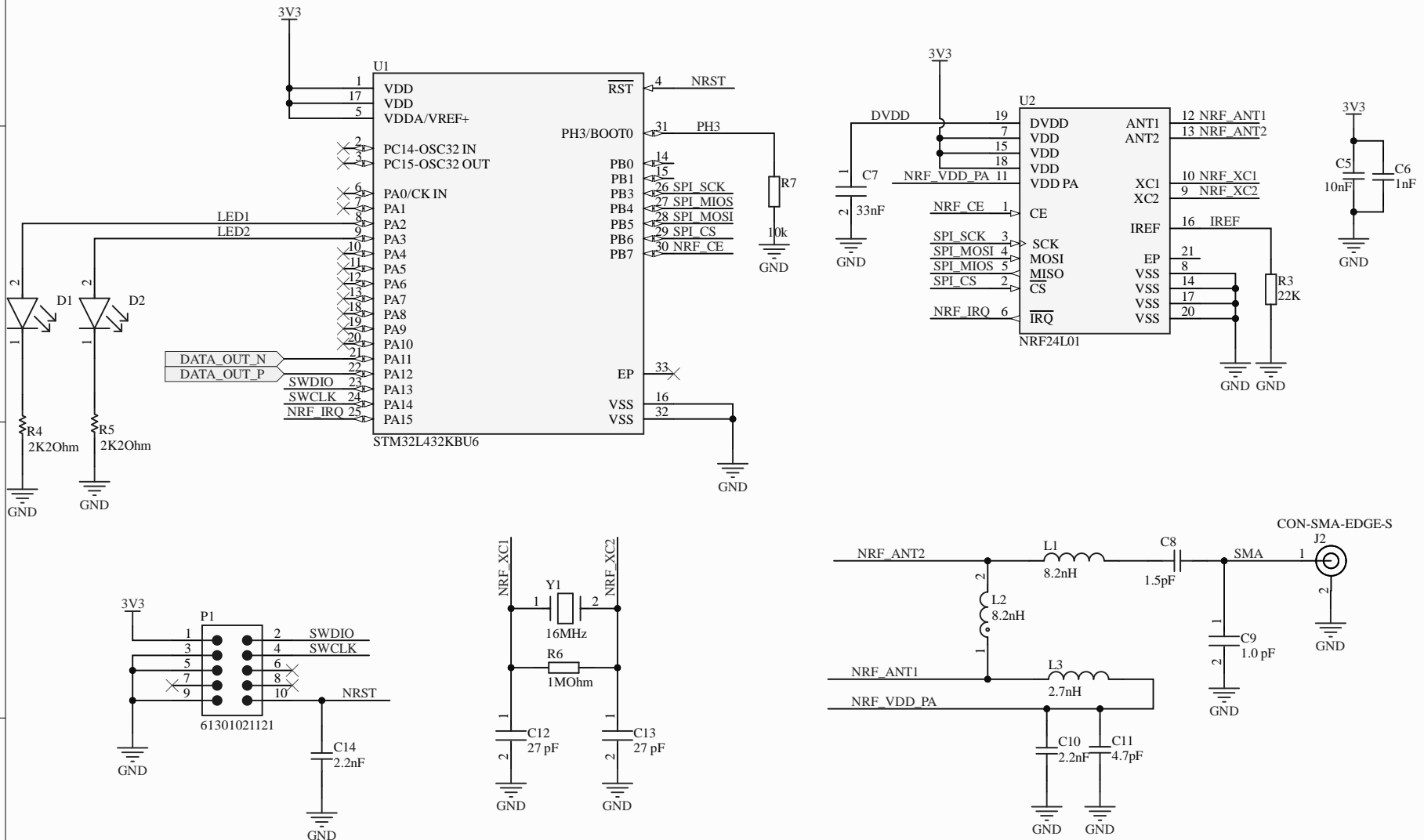
# Stepdown\_Regulator\_5V0\_3V3



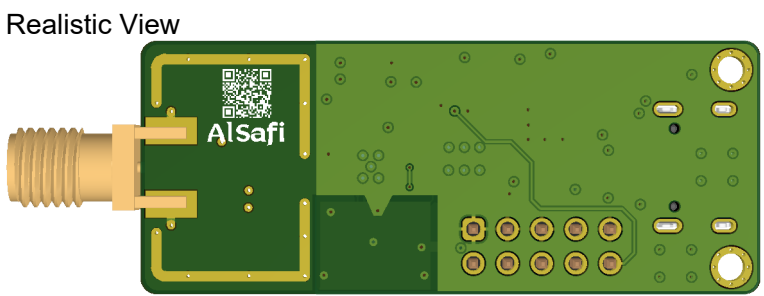
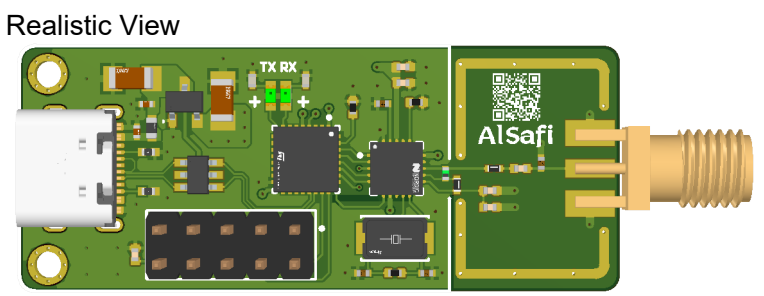
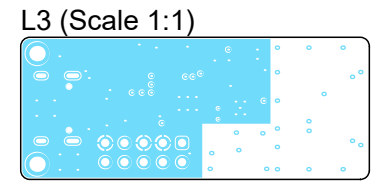
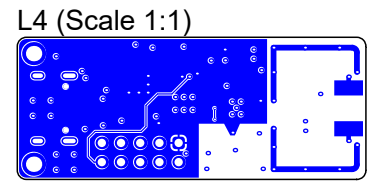
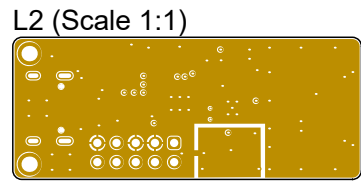
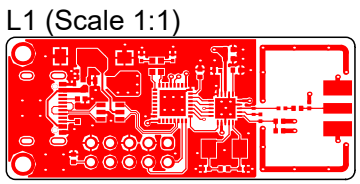
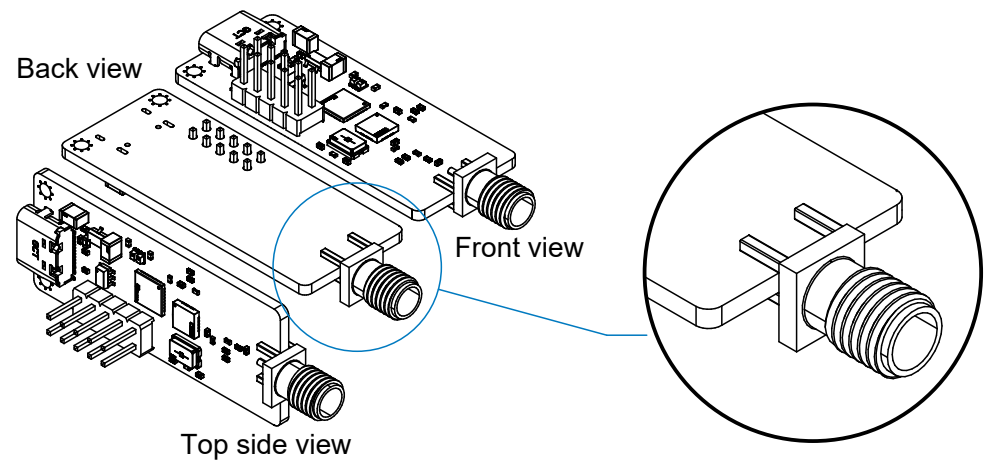
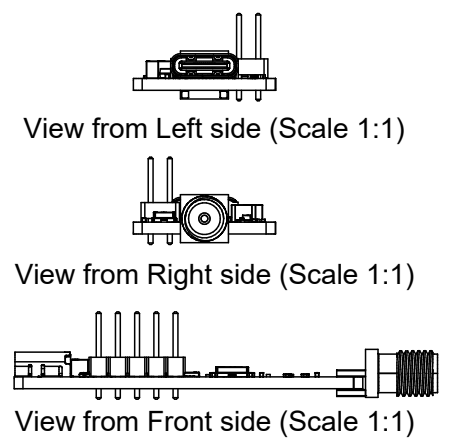
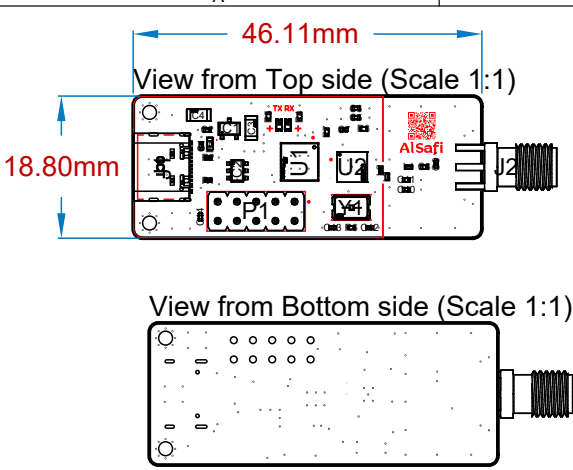
# ESD\_Protection



STM32 and nRF24L01 with single ended matching network crystal, bias resistor, and decoupling capacitors.



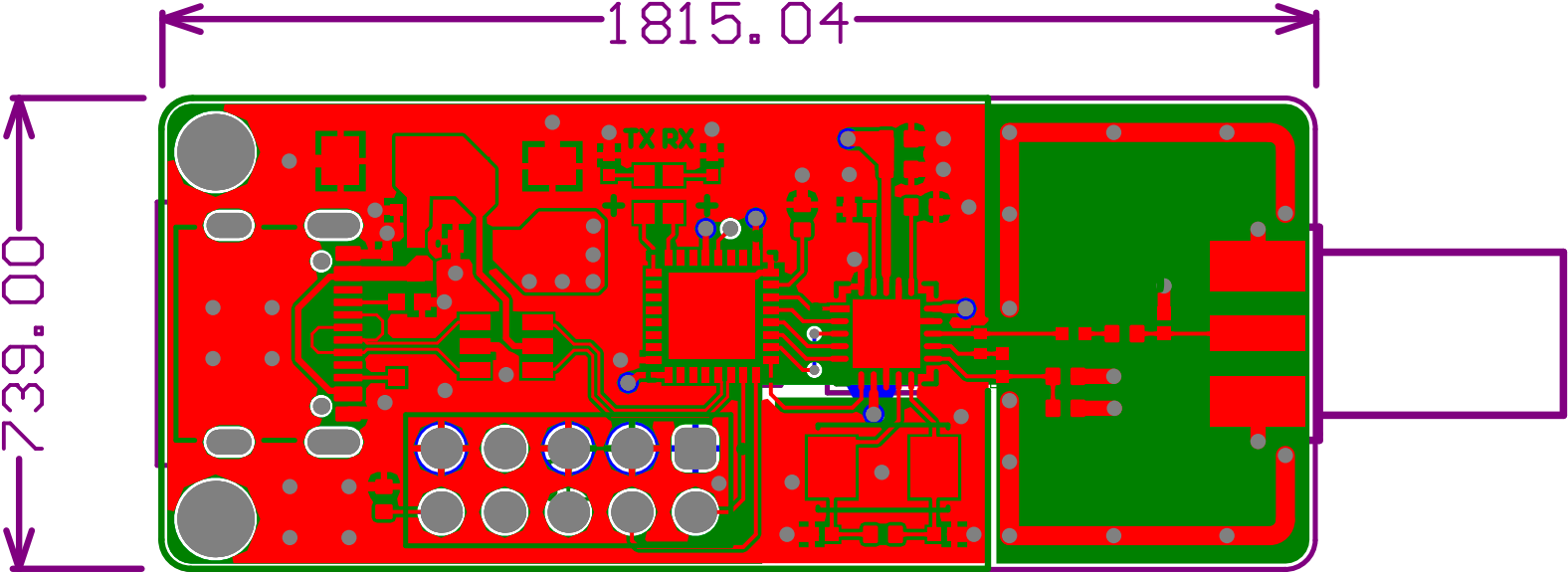
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Size	Number	Revision
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Date:	3/24/2025	Sheet of
File:	D:\Altium Design Projects\STM32_nRF24L01\STM32_nRF24L01.schDoc	

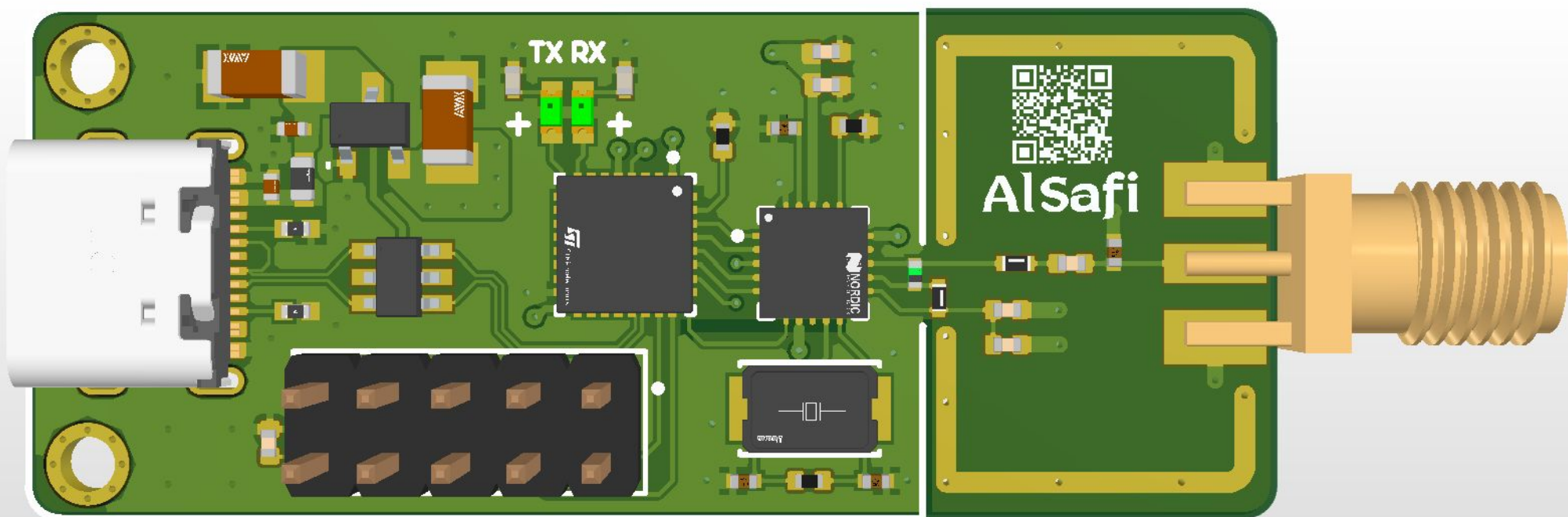


Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type
	Top Overlay			Legend
Surface Material	Top Solder	0.030mm	Solder Resist	Solder Mask
Copper	L1	0.035mm		Signal
Prepreg		0.099mm	PP-006	Dielectric
CF-004	L2	0.015mm		Signal
Prepreg		1.265mm	FR-4	Dielectric
CF-004	L3	0.015mm		Signal
Prepreg		0.099mm	PP-006	Dielectric
Copper	L4	0.035mm		Signal
Surface Material	Bottom Solder	0.030mm	Solder Resist	Solder Mask
	Bottom Overlay			Legend

Total thickness: 1.625mm





## Design Rules Verification Report

Filename : D:\Altium Design Projects\STM32\_RF\_USB\STM32\_RF\_USB.PcbDoc

Warnings 0  
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=5mil) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ( All )	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=10mil) (Max=100mil) (Preferred=24mil) (InNet('5V0'))	0
Width Constraint (Min=6.169mil) (Max=100mil) (Preferred=10mil) (All)	0
Width Constraint (Min=10mil) (Max=100mil) (Preferred=24mil) (InNet('GND'))	0
Width Constraint (Min=10mil) (Max=100mil) (Preferred=24mil) (InNet('VBUS'))	0
Width Constraint (Min=10mil) (Max=100mil) (Preferred=24mil) (InNet('3V3'))	0
Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4)	0
Hole Size Constraint (Min=1mil) (Max=100mil) (All)	0
Hole To Hole Clearance (Gap=5mil) (All),(All)	0
Minimum Solder Mask Sliver (Gap=3.5mil) (All),(All)	0
Silk To Solder Mask (Clearance=10mil) (IsPad),(All)	0
Silk to Silk (Clearance=10mil) (All),(All)	0
Net Antennae (Tolerance=0mil) (All)	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All)	0
Total	0