

This page describes supported displays. Ucglib only supports color displays with internal controller and local display RAM. The setup for Ucglib depends on the internal controller, which is build into the color display.

ST7735

- Type: Color TFT
- Dimension: 128x160
- Color Depth: 18 Bit
- Interfaces: HW SPI, SW SPI
- Tutorial: [How to Connect a ST7735 Display](#)

Arduino Constructor

Constructor	Description
Ucglib_ST7735_18x128x160_SWSPI ucg(sclk, data, cd, cs, [reset])	Software SPI
Ucglib_ST7735_18x128x160_HWSPI ucg(cd, cs, [reset])	Hardware SPI

Device Procedures

- Controller Device: `ucg_dev_st7735_18x128x160`
- Extensions: `ucg_ext_st7735_18`, `ucg_ext_none`

ILI9341

- Type: Color TFT
- Dimension: 240x320
- Color Depth: 18 Bit
- Interfaces: HW SPI, SW SPI

- Tutorial: [How to connect a ILI9341 display](#)

Arduino Constructor

Constructor	Description
Ucglib_ILI9341_18x240x320_SWSPI ucg(sclk, data, cd, cs, [reset])	Software SPI
Ucglib_ILI9341_18x240x320_HWSPI ucg(cd, cs, [reset])	Hardware SPI

Device Procedures

- Controller Device: `ucg_dev_ili9341_18x240x320`
- Extensions: `ucg_ext_ili9341_18`, `ucg_ext_none`

PCF8833

- Type: Color TFT
- Dimension: 132x132
- Color Depth: 16 Bit
- Interfaces: HW SPI, SW SPI
- Tutorial: [How to connect a PCF8833 display](#)

Arduino Constructor

Constructor	Description
Ucglib_PCF8833_16x132x132_SWSPI ucg(sclk, data, cd, cs, [reset])	Software SPI
Ucglib_PCF8833_16x132x132_HWSPI ucg(cd, cs, [reset])	Hardware SPI

Device Procedures

- Controller Device: `ucg_dev_pcf8833_16x132x132`

- Extensions: `ucg_ext_pcf8833_16`, `ucg_ext_none`

SSD1351

- Type: Color OLED
- Dimension: 128x128
- Color Depth: 18 Bit
- Interfaces: HW SPI, SW SPI
- Tutorial: n.a.

Arduino Constructor

Constructor	Description
<code>Ucglib_SSD1351_18x128x128_SWSPI ucg(sclk, data, cd, cs, [reset])</code>	Software SPI, GPIO set to 0
<code>Ucglib_SSD1351_18x128x128_HWSPI ucg(cd, cs, [reset])</code>	Hardware SPI, GPIO set to 0
<code>Ucglib_SSD1351_18x128x128_FT_SWSPI ucg(sclk, data, cd, cs, [reset])</code>	Software SPI, GPIO set to 1
<code>Ucglib_SSD1351_18x128x128_FT_HWSPI ucg(cd, cs, [reset])</code>	Hardware SPI, GPIO set to 1

Device Procedures

- Controller Devices: `ucg_dev_ssd1351_18x128x128_ilsoft`, `ucg_dev_ssd1351_18x128x128_ft`
- Extensions: `ucg_ext_ssd1351_18`, `ucg_ext_none`