

Mjerilo:	1 : 1	List:	2 / 16	Datum	03.10.2018.	Naziv dokumenta:	Mixed Signal Management Board Blok dijagram
		Konstruirao/la	0113				
		Crtao/la	M. Osonjacki - FER				
		Kontrolirao/la	0076				
		Odobrio/la	0113				
Rev.	Datum	Ime i prezime	Int.ozn.: *	ALTPRO Velika cesta 41 10000 Zagreb C.R.O.A.T.I.A.		Kataloški broj: AA300100	Broj dokumenta: AA300100-020301010101
						ZAMJ.ZA: *	

028-8-1201

1	2	3	4	5	6	7	8
[03] Voltages - Contacts.SchDoc							
A							
B							
C							
D							
1	2	3	4	5	6	7	8

PRAZNA STRANICA

Mjerilo: 1 : 1	List: 3 / 16	Datum 03.10.2018.	Naziv dokumenta:	
		Konstruirao/la 0113	Mixed Signal Management Board	
		Crtao/la M. Osonjacki - FER	Popis naponskih razina i kontakata	
		Kontrolirao/la 0076		
		Odobrio/la 0113		
Rev.	Datum	Ime i prezime	Int.ozn.: *	

 ALTPRO Velika cesta 41 10000 Zagreb CROATIA	Kataloški broj: AA300100	Broj dokumenta: AA300100-020301010101
ZAMJ.ZA: *		

08.8.2018.

1	2	3	4	5	6	7	8
[04] Voltages - Nets.SchDoc							
A							
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PRAZNA STRANICA

Mjerilo: 1 : 1	List: 4 / 16	Datum 03.10.2018.	Naziv dokumenta:	
		Konstruirao/la 0113	Mixed Signal Management Board Popis naponskih razina i Net-ova	
		Crtao/la M. Osonjacki - FER		
		Kontrolirao/la 0076		
		Odobrio/la 0113		
			Kataloški broj: AA300100	
			Broj dokumenta: AA300100-020301010101	
Rev.	Datum	Ime i prezime	Int.ozn.: *	ZAMJ.ZA: *



Velika cesta 41
10000 Zagreb
CROATIA

08.8.2018

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<table><tr><th>J2 PIN</th><th>NAZIV</th><th>J3 PIN</th><th>NAZIV</th><th>MCU PIN</th><th>NAZIV</th></tr><tr><td>1</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>2</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>3</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>4</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>5</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>6</td><td>UART1_RX</td><td></td><td></td><td>56</td><td>PTC8/LPUART1_RX</td></tr><tr><td>7</td><td>UART1_TX</td><td></td><td></td><td>55</td><td>PTC9/LPUART1_TX</td></tr><tr><td>8</td><td>SPI_SCK</td><td></td><td></td><td>48</td><td>PTB2/LPSPI0_SCK</td></tr><tr><td>9</td><td>SPI_SIN</td><td></td><td></td><td>47</td><td>PTB3/LPSPI0_SIN</td></tr><tr><td>10</td><td>SPI_SOUT</td><td></td><td></td><td>28</td><td>PTB4/LPSPI0_SOUT</td></tr><tr><td>11</td><td>SPI_CS</td><td></td><td></td><td>27</td><td>PTB5/LPSPI0_PCS1</td></tr><tr><td>12</td><td>GND</td><td></td><td></td><td></td><td></td></tr><tr><td>13</td><td>3V3</td><td></td><td></td><td></td><td></td></tr><tr><td>14</td><td>GND</td><td></td><td></td><td></td><td></td></tr><tr><td>15</td><td>I2C_SCL</td><td></td><td></td><td>41</td><td>PTD9/LPI2C_SCL</td></tr><tr><td>16</td><td>I2C_SDA</td><td></td><td></td><td>42</td><td>PTD8/LPI2C_SDA</td></tr><tr><td>17</td><td>CAN_H</td><td></td><td></td><td>29</td><td>PTC3/CAN0_TX</td></tr><tr><td>18</td><td>CAN_L</td><td></td><td></td><td>30</td><td>PTC2/CAN0_RX</td></tr><tr><td>19</td><td>GND</td><td></td><td></td><td></td><td></td></tr><tr><td>20</td><td>3V3</td><td></td><td></td><td></td><td></td></tr><tr><td>21</td><td>GND</td><td></td><td></td><td></td><td></td></tr><tr><td>22</td><td>DAC_OUT</td><td></td><td></td><td>23</td><td>DAC0_OUT</td></tr><tr><td>23</td><td>PWM_CH0</td><td></td><td></td><td>4</td><td>PTD0/FTM2_CH0</td></tr><tr><td>24</td><td>PWM_CH1</td><td></td><td></td><td>3</td><td>PTD1/FTM2_CH1</td></tr><tr><td>25</td><td>PWM_CH2</td><td></td><td></td><td>9</td><td>PTE4/FTM2_CH2</td></tr><tr><td>26</td><td>PWM_CH3</td><td></td><td></td><td>8</td><td>PTE5/FTM2_CH3</td></tr><tr><td>27</td><td>PWM_CH4</td><td></td><td></td><td>6</td><td>PTE10/FTM2_CH4</td></tr><tr><td>28</td><td>PWM_CH5</td><td></td><td></td><td>5</td><td>PTE11/FTM2_CH5</td></tr><tr><td>29</td><td>PWM_CH6</td><td></td><td></td><td>2</td><td>PTE15/FTM2_CH6</td></tr><tr><td>30</td><td>PWM_CH7</td><td></td><td></td><td>1</td><td>PTE16/FTM2_CH7</td></tr><tr><td></td><td></td><td>1</td><td>ADC0_CH7</td><td>39</td><td>PTC1/ADC0_SE9</td></tr><tr><td></td><td></td><td>2</td><td>ADC0_CH6</td><td>40</td><td>PTC0/ADC0_SE8</td></tr><tr><td></td><td></td><td>3</td><td>ADC0_CH5</td><td>43</td><td>PTC17/ADC0_SE15</td></tr><tr><td></td><td></td><td>4</td><td>ADC0_CH4</td><td>44</td><td>PTC16/ADC0_SE14</td></tr><tr><td></td><td></td><td>5</td><td>ADC0_CH3</td><td>45</td><td>PTC15/ADC0_SE13</td></tr><tr><td></td><td></td><td>6</td><td>ADC0_CH2</td><td>46</td><td>PTC14/ADC0_SE12</td></tr><tr><td></td><td></td><td>7</td><td>ADC0_CH1</td><td>53</td><td>PTB1/ADC0_SE5</td></tr><tr><td></td><td></td><td>8</td><td>ADC0_CH0</td><td>54</td><td>PTB0/ADC0_SE4</td></tr><tr><td></td><td></td><td>9</td><td>GND</td><td></td><td></td></tr><tr><td></td><td></td><td>10</td><td>3V3</td><td></td><td></td></tr><tr><td></td><td></td><td>11</td><td>GND</td><td></td><td></td></tr><tr><td></td><td></td><td>12</td><td>DIN_CH7</td><td>63</td><td>PTB17</td></tr><tr><td></td><td></td><td>13</td><td>DIN_CH6</td><td>64</td><td>PTB16</td></tr><tr><td></td><td></td><td>14</td><td>DIN_CH5</td><td>65</td><td>PTB15</td></tr><tr><td></td><td></td><td>15</td><td>DIN_CH4</td><td>66</td><td>PTB14</td></tr><tr><td></td><td></td><td>16</td><td>DIN_CH3</td><td>67</td><td>PTB13</td></tr><tr><td></td><td></td><td>17</td><td>DIN_CH2</td><td>68</td><td>PTB12</td></tr><tr><td></td><td></td><td>18</td><td>DIN_CH1</td><td>74</td><td>PTB11</td></tr><tr><td></td><td></td><td>19</td><td>DIN_CH0</td><td>75</td><td>PTB10</td></tr><tr><td></td><td></td><td>20</td><td>GND</td><td></td><td></td></tr><tr><td></td><td></td><td>21</td><td>3V3</td><td></td><td></td></tr><tr><td></td><td></td><td>22</td><td>GND</td><td></td><td></td></tr><tr><td></td><td></td><td>23</td><td>ADC1_CTRL0</td><td>82</td><td>PTA16</td></tr><tr><td></td><td></td><td>24</td><td>ADC1_CTRL1</td><td>83</td><td>PTA15</td></tr><tr><td></td><td></td><td>25</td><td>ADC1_CTRL2</td><td>84</td><td>PTE6</td></tr><tr><td></td><td></td><td>26</td><td>ADC1_CH2</td><td>85</td><td>PTE2/ADC1_SE10</td></tr><tr><td></td><td></td><td>27</td><td>ADC2_CH2</td><td>89</td><td>PTA13/ADC2_SE4</td></tr><tr><td></td><td></td><td>28</td><td>ADC2_CTRL0</td><td>90</td><td>PTA12</td></tr><tr><td></td><td></td><td>29</td><td>ADC2_CTRL1</td><td>93</td><td>PTE1</td></tr><tr><td></td><td></td><td>30</td><td>ADC2_CTRL2</td><td>94</td><td>PTB0</td></tr></table>								J2 PIN	NAZIV	J3 PIN	NAZIV	MCU PIN	NAZIV	1						2						3						4						5						6	UART1_RX			56	PTC8/LPUART1_RX	7	UART1_TX			55	PTC9/LPUART1_TX	8	SPI_SCK			48	PTB2/LPSPI0_SCK	9	SPI_SIN			47	PTB3/LPSPI0_SIN	10	SPI_SOUT			28	PTB4/LPSPI0_SOUT	11	SPI_CS			27	PTB5/LPSPI0_PCS1	12	GND					13	3V3					14	GND					15	I2C_SCL			41	PTD9/LPI2C_SCL	16	I2C_SDA			42	PTD8/LPI2C_SDA	17	CAN_H			29	PTC3/CAN0_TX	18	CAN_L			30	PTC2/CAN0_RX	19	GND					20	3V3					21	GND					22	DAC_OUT			23	DAC0_OUT	23	PWM_CH0			4	PTD0/FTM2_CH0	24	PWM_CH1			3	PTD1/FTM2_CH1	25	PWM_CH2			9	PTE4/FTM2_CH2	26	PWM_CH3			8	PTE5/FTM2_CH3	27	PWM_CH4			6	PTE10/FTM2_CH4	28	PWM_CH5			5	PTE11/FTM2_CH5	29	PWM_CH6			2	PTE15/FTM2_CH6	30	PWM_CH7			1	PTE16/FTM2_CH7			1	ADC0_CH7	39	PTC1/ADC0_SE9			2	ADC0_CH6	40	PTC0/ADC0_SE8			3	ADC0_CH5	43	PTC17/ADC0_SE15			4	ADC0_CH4	44	PTC16/ADC0_SE14			5	ADC0_CH3	45	PTC15/ADC0_SE13			6	ADC0_CH2	46	PTC14/ADC0_SE12			7	ADC0_CH1	53	PTB1/ADC0_SE5			8	ADC0_CH0	54	PTB0/ADC0_SE4			9	GND					10	3V3					11	GND					12	DIN_CH7	63	PTB17			13	DIN_CH6	64	PTB16			14	DIN_CH5	65	PTB15			15	DIN_CH4	66	PTB14			16	DIN_CH3	67	PTB13			17	DIN_CH2	68	PTB12			18	DIN_CH1	74	PTB11			19	DIN_CH0	75	PTB10			20	GND					21	3V3					22	GND					23	ADC1_CTRL0	82	PTA16			24	ADC1_CTRL1	83	PTA15			25	ADC1_CTRL2	84	PTE6			26	ADC1_CH2	85	PTE2/ADC1_SE10			27	ADC2_CH2	89	PTA13/ADC2_SE4			28	ADC2_CTRL0	90	PTA12			29	ADC2_CTRL1	93	PTE1			30	ADC2_CTRL2	94	PTB0
J2 PIN	NAZIV	J3 PIN	NAZIV	MCU PIN	NAZIV																																																																																																																																																																																																																																																																																																																																																																																
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6	UART1_RX			56	PTC8/LPUART1_RX																																																																																																																																																																																																																																																																																																																																																																																
7	UART1_TX			55	PTC9/LPUART1_TX																																																																																																																																																																																																																																																																																																																																																																																
8	SPI_SCK			48	PTB2/LPSPI0_SCK																																																																																																																																																																																																																																																																																																																																																																																
9	SPI_SIN			47	PTB3/LPSPI0_SIN																																																																																																																																																																																																																																																																																																																																																																																
10	SPI_SOUT			28	PTB4/LPSPI0_SOUT																																																																																																																																																																																																																																																																																																																																																																																
11	SPI_CS			27	PTB5/LPSPI0_PCS1																																																																																																																																																																																																																																																																																																																																																																																
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16	I2C_SDA			42	PTD8/LPI2C_SDA																																																																																																																																																																																																																																																																																																																																																																																
17	CAN_H			29	PTC3/CAN0_TX																																																																																																																																																																																																																																																																																																																																																																																
18	CAN_L			30	PTC2/CAN0_RX																																																																																																																																																																																																																																																																																																																																																																																
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22	DAC_OUT			23	DAC0_OUT																																																																																																																																																																																																																																																																																																																																																																																
23	PWM_CH0			4	PTD0/FTM2_CH0																																																																																																																																																																																																																																																																																																																																																																																
24	PWM_CH1			3	PTD1/FTM2_CH1																																																																																																																																																																																																																																																																																																																																																																																
25	PWM_CH2			9	PTE4/FTM2_CH2																																																																																																																																																																																																																																																																																																																																																																																
26	PWM_CH3			8	PTE5/FTM2_CH3																																																																																																																																																																																																																																																																																																																																																																																
27	PWM_CH4			6	PTE10/FTM2_CH4																																																																																																																																																																																																																																																																																																																																																																																
28	PWM_CH5			5	PTE11/FTM2_CH5																																																																																																																																																																																																																																																																																																																																																																																
29	PWM_CH6			2	PTE15/FTM2_CH6																																																																																																																																																																																																																																																																																																																																																																																
30	PWM_CH7			1	PTE16/FTM2_CH7																																																																																																																																																																																																																																																																																																																																																																																
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		2	ADC0_CH6	40	PTC0/ADC0_SE8																																																																																																																																																																																																																																																																																																																																																																																
		3	ADC0_CH5	43	PTC17/ADC0_SE15																																																																																																																																																																																																																																																																																																																																																																																
		4	ADC0_CH4	44	PTC16/ADC0_SE14																																																																																																																																																																																																																																																																																																																																																																																
		5	ADC0_CH3	45	PTC15/ADC0_SE13																																																																																																																																																																																																																																																																																																																																																																																
		6	ADC0_CH2	46	PTC14/ADC0_SE12																																																																																																																																																																																																																																																																																																																																																																																
		7	ADC0_CH1	53	PTB1/ADC0_SE5																																																																																																																																																																																																																																																																																																																																																																																
		8	ADC0_CH0	54	PTB0/ADC0_SE4																																																																																																																																																																																																																																																																																																																																																																																
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		24	ADC1_CTRL1	83	PTA15																																																																																																																																																																																																																																																																																																																																																																																
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		26	ADC1_CH2	85	PTE2/ADC1_SE10																																																																																																																																																																																																																																																																																																																																																																																
		27	ADC2_CH2	89	PTA13/ADC2_SE4																																																																																																																																																																																																																																																																																																																																																																																
		28	ADC2_CTRL0	90	PTA12																																																																																																																																																																																																																																																																																																																																																																																
		29	ADC2_CTRL1	93	PTE1																																																																																																																																																																																																																																																																																																																																																																																
		30	ADC2_CTRL2	94	PTB0																																																																																																																																																																																																																																																																																																																																																																																
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D	<table><tr><td>Mjerilo: 1 : 1</td><td>List: 5 / 16</td><td>Datum Konstruirao/la Crtao/la Kontrolirao/la Odobrio/la</td><td>03.10.2018. 0113 M. Osonjacki - FER 0076 0113</td><td>Naziv dokumenta: Mixed Signal Management Board Popis konfiguracijskih postavki</td></tr><tr><td>Rev.</td><td>Datum</td><td>Ime i prezime</td><td>Int.ozn.: *</td><td>Broj dokumenta: AA300100-020301010101 ZAMJ.ZA: *</td></tr></table>							Mjerilo: 1 : 1	List: 5 / 16	Datum Konstruirao/la Crtao/la Kontrolirao/la Odobrio/la	03.10.2018. 0113 M. Osonjacki - FER 0076 0113	Naziv dokumenta: Mixed Signal Management Board Popis konfiguracijskih postavki	Rev.	Datum	Ime i prezime	Int.ozn.: *	Broj dokumenta: AA300100-020301010101 ZAMJ.ZA: *																																																																																																																																																																																																																																																																																																																																																																				
Mjerilo: 1 : 1	List: 5 / 16	Datum Konstruirao/la Crtao/la Kontrolirao/la Odobrio/la	03.10.2018. 0113 M. Osonjacki - FER 0076 0113	Naziv dokumenta: Mixed Signal Management Board Popis konfiguracijskih postavki																																																																																																																																																																																																																																																																																																																																																																																	
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[06] Mechanics.SchDoc

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A

B

B

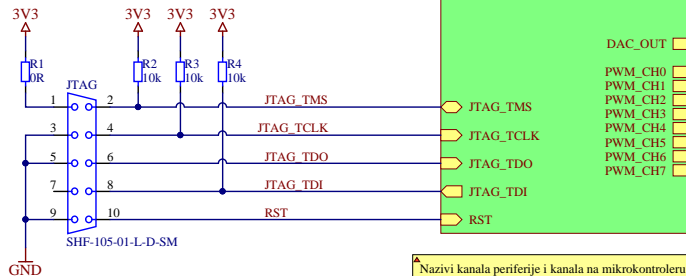
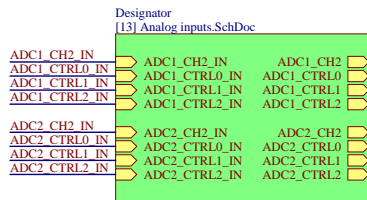
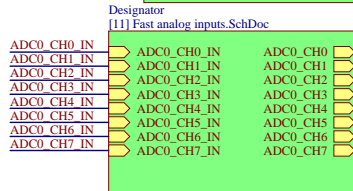
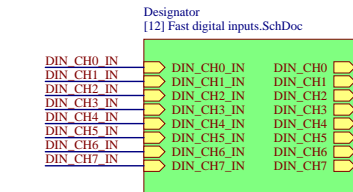
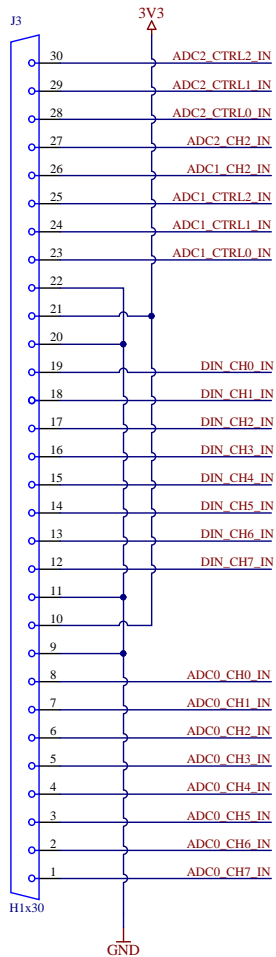
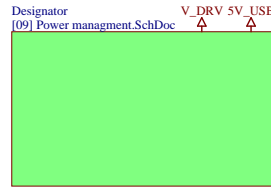
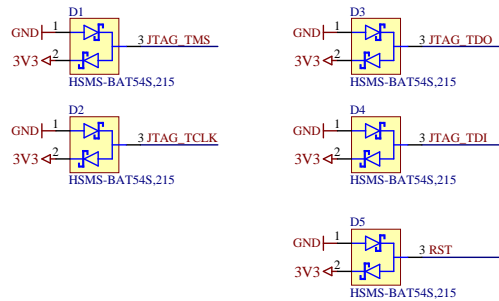
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C

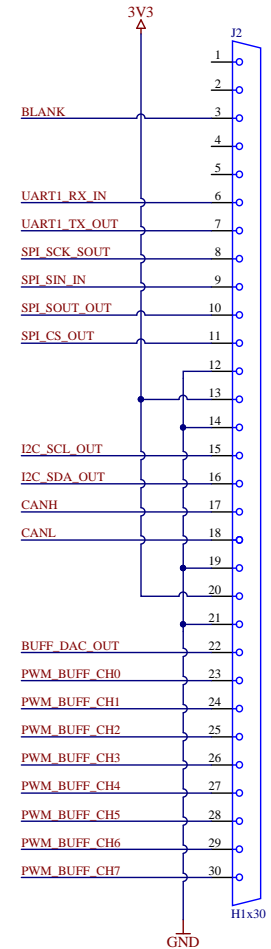
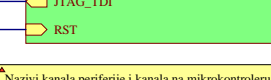
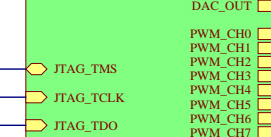
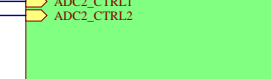
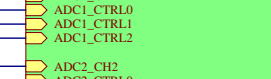
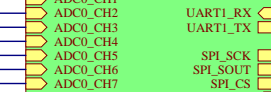
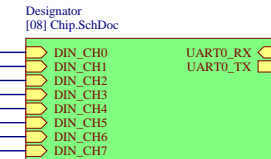
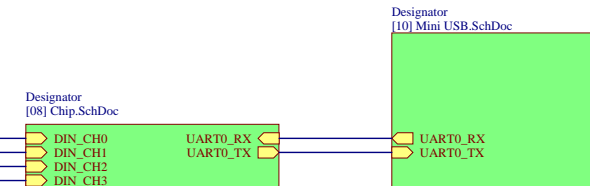
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D

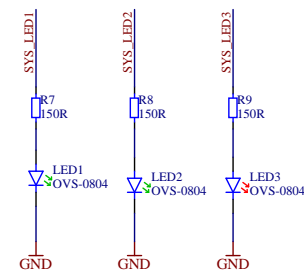
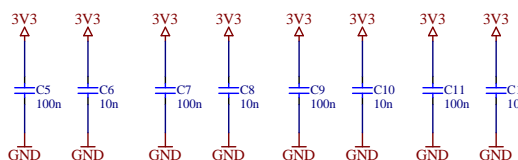
Mjefilo: 1 : 1	List: 6 / 16	Datum 03.10.2018.	Naziv dokumenta:	
		Konstruirao/la Crtao/la Kontrolirao/la Odobrio/la	0113 M. Osonjacki - FER 0076 0113	Mixed Signal Management Board Mehanika i ostali dijelovi
		 ALTPRO Veslika cesta 41 10000 Zagreb C.R.G.O.11.A	Kataloški broj: AA300100	Broj dokumenta: AA300100-020301010101
Rev.	Datum	Ime i prezime	Iniz., : *	ZAMJA : *



Nazivi kanala periferije i kanala na mikrokontroleru NISU jednoznačni. Vidi str. 5 ili str. 8.



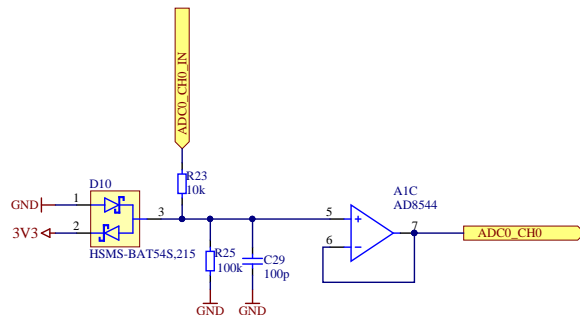
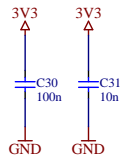
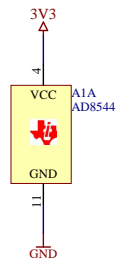
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		Kontrolirao/la: 0076	
		Odobrio/la: 0113	
		AA300100	Broj dokumenta: AA300100-020301010101
Rev.	Datum	Ime i prezime	Int.ozn.: *
			ZAMJ.ZA: *



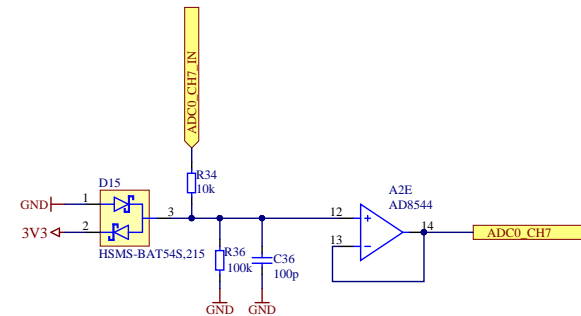
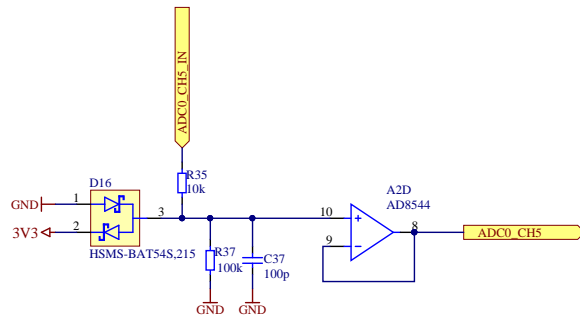
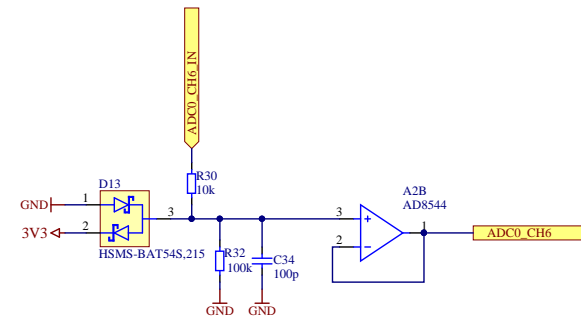
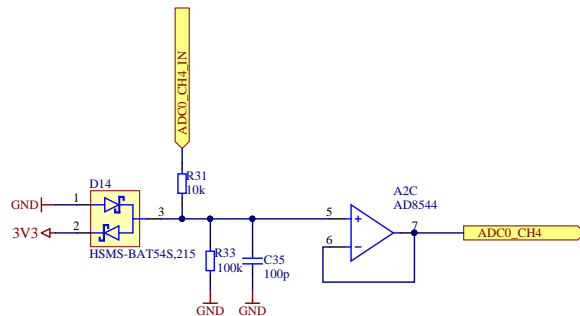
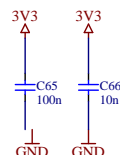
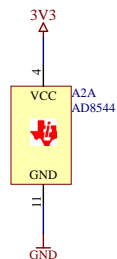
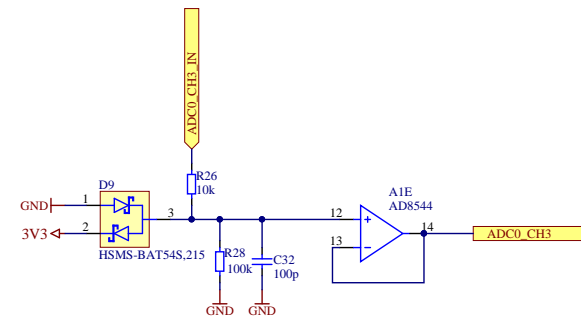
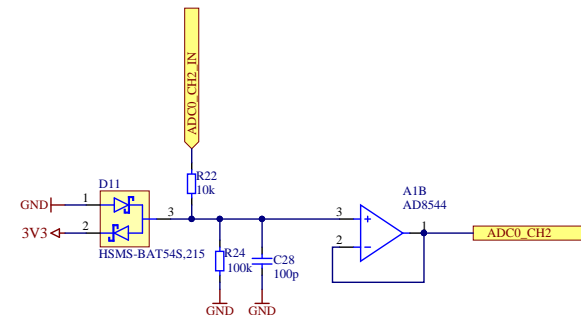
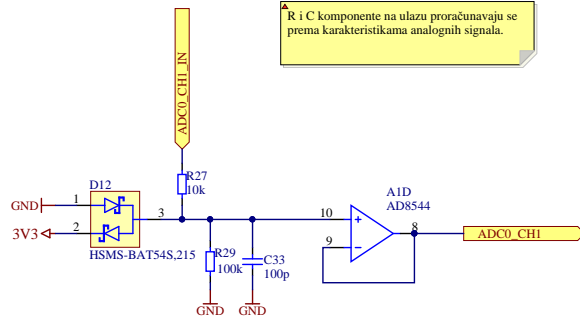
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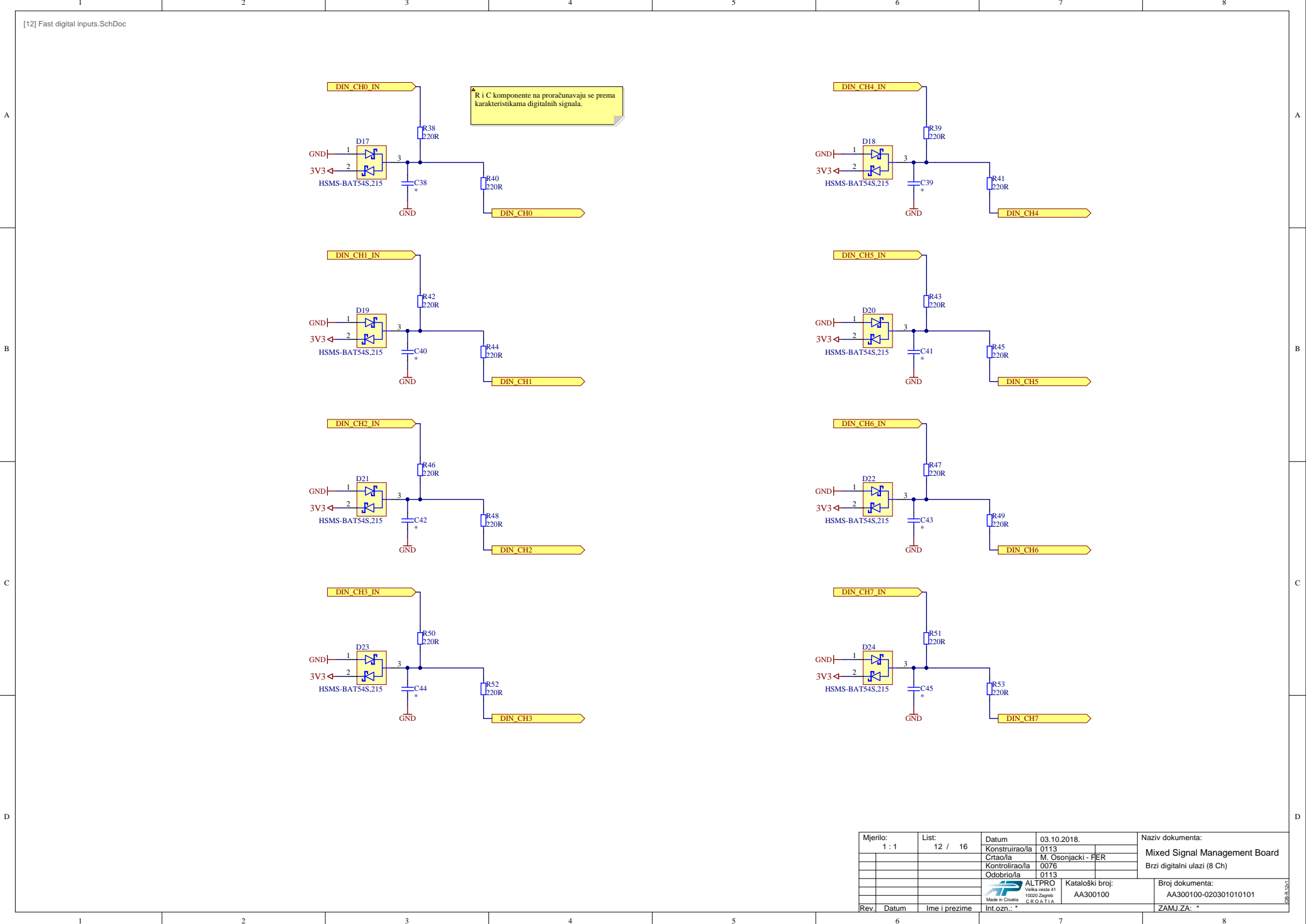
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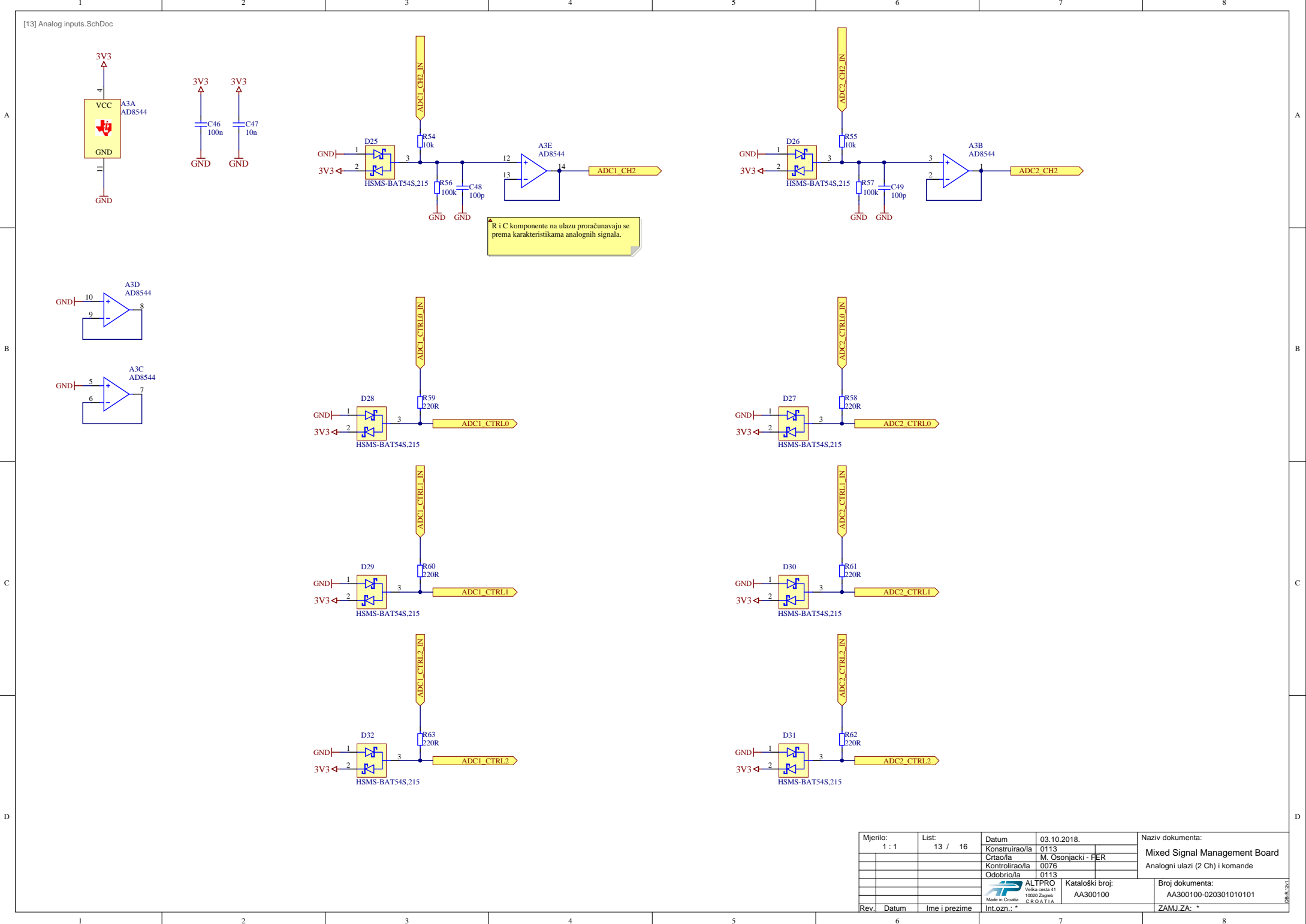
R i C komponente na ulazu proračunavaju se prema karakteristikama analognih signala.



Mjerilo: 1 : 1	List: 11 / 16	Datum 03.10.2018.	Naziv dokumenta: Mixed Signal Management Board Brzi analogni ulazi (8Ch)
		Konstruirao/la 0113	
		Crtao/la M. Osonjacki - FER	
		Kontrolirao/la 0076	
		Odobrio/la 0113	
		ALT PRO Velika cesta 41 10000 Zagreb C.R.O.A.T.I.A.	Kataloški broj: AA300100
Rev.	Datum	Ime i prezime	Broj dokumenta: AA300100-020301010101
		Int.ozn.: *	ZAMJ.ZA: *

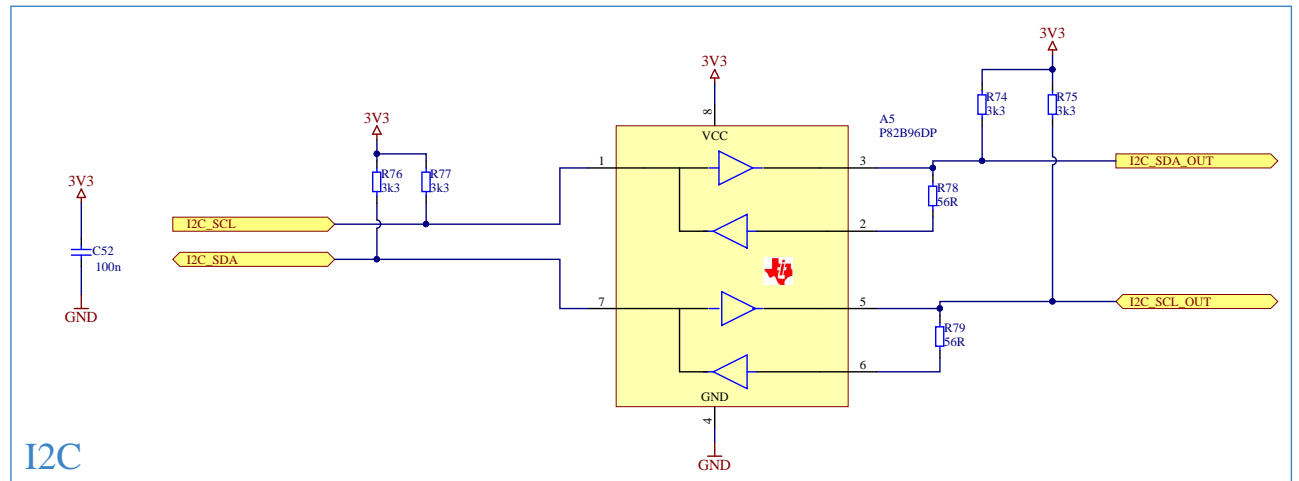
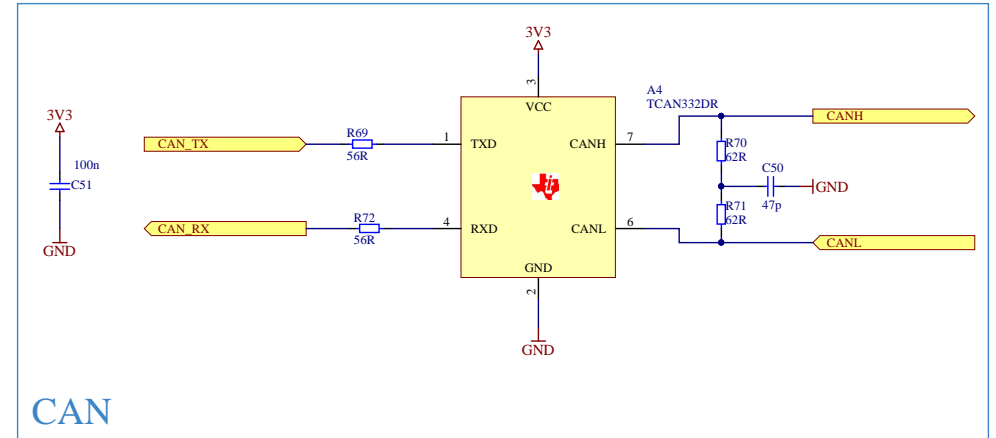
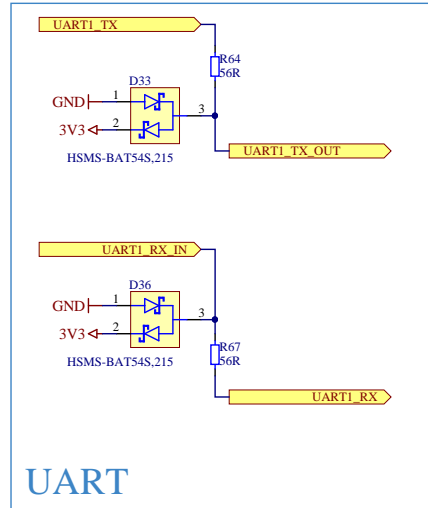
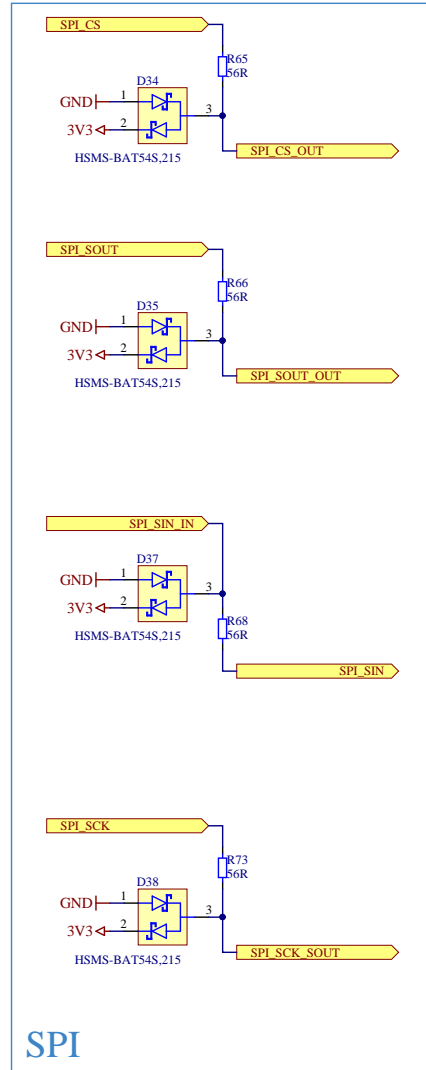


Mjerilo: 1 : 1	List: 12 / 16	Datum 03.10.2018.	Konstruirao/la 0113	Naziv dokumenta: Mixed Signal Management Board Brzi digitalni ulazi (8 Ch)
			M. Osonjacki - FER	
			Kontrolirao/la 0076	
			Odobrio/la 0113	
			ALT PRO Velika cesta 41 10000 Zagreb C.R.O.A.T.I.A.	Kataloški broj: AA300100
Rev.	Datum	Ime i prezime	Int.ozn.: *	Broj dokumenta: AA300100-020301010101
				ZAMJ.ZA: *



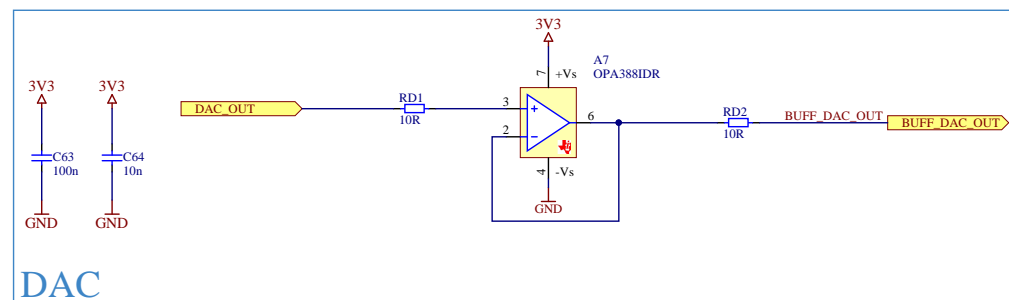
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				Konstruirao/la	0113		Analogni ulazi (2 Ch) i komande
				Crtao/la	M. Osonjacki - FER		
				Kontrolirao/la	0076		
				Odobrio/la	0113		
						Kataloški broj:	Broj dokumenta:
						AA300100	AA300100-020301010101
Rev.	Datum	Ime i prezime	Int.ozn.:	ALT PRO	Velika cesta 41		
				Made in Croatia	10000 Zagreb		
				C.R.O.A.T.I.A.			
							ZAMJ.ZA: *

028-8-1201



Mjerilo: 1 : 1	List: 14 / 16	Datum 03.10.2018.	Naziv dokumenta: Mixed Signal Management Board Komunikacija (SPI, UART, CAN, I2C)
		Konstruirao/la 0113	
		Crtao/la M. Osonjacki - FER	
		Kontrolirao/la 0076	
		Odobrio/la 0113	
		ALTPRO Velika cesta 41 10000 Zagreb C.R.O.A.T.I.A.	Katalogski broj: AA300100
			Broj dokumenta: AA300100-020301010101
Rev.	Datum	Ime i prezime	Int.ozn.: *
			ZAMJ.ZA: *

Digitalni izlazi (PWM)



DAC

Mjerilo: 1 : 1	List: 15 / 16	Datum 03.10.2018.	Naziv dokumenta: Mixed Signal Management Board Izlazi (PWM 8 Ch, DAC)	
		Konstruirao/la Crtao/la Kontrolirao/la Odobrio/la	0113 M. Osonjacki - FER 0076 0113	
		 ALTPRO Inžinjering 10000 Zagreb C.R.B. d.o.o.	Kataloški broj: AA300100	Broj dokumenta: AA300100-02301010101
Rev.	Datum	Ime i prezime	Int.ozn.:	ZAMJ.ZA: *

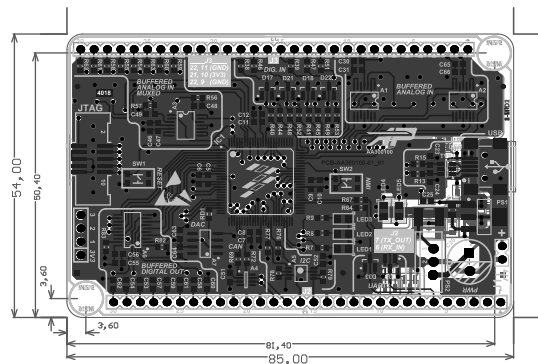
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[99] Changes - Revisions.SchDoc							
A							
B							
C							
D							
1	2	3	4	5	6	7	8

PRAZNA STRANICA

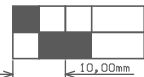
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		Konstruirao/la 0113	Mixed Signal Management Board
		Crtao/la M. Osonjacki - FER	Popis izmjena
		Kontrolirao/la 0076	
		Odobrio/la 0113	
Rev.	Datum	Ime i prezime	Int.ozn.: *

 ALTPRO Velika cesta 41 10000 Zagreb CROATIA	Kataloški broj: AA300100	Broj dokumenta: AA300100-020301010101
ZAMJ.ZA: *		

08.8.201



PCB-326-2-1990



DESIGN INFORMATION

MIN. TRACK WIDTH: 0,3 mm / ? MIL
MIN. CLEARANCE: 0,2 mm / ? MIL
MIN. VIA PAD SIZE: 0,7 mm / ? MIL

X-OUT NOT ALLOWED

Notes:

Quality standard IEC 326-3:1991 (HRN N.R7.080)
Quality check IEC 326-2:1990 (HRN N.R7.060 / HRN N.R7.064) & IPC610-H

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SUBSTRATE	FR4	MATERIAL	<input checked="" type="checkbox"/> PbSn <input type="checkbox"/> ROHS	TYPE	<input checked="" type="checkbox"/> SILK <input type="checkbox"/> PRINT
THICKNESS	1,6mm	THICKNESS	--	LAYER	<input checked="" type="checkbox"/> TOP <input type="checkbox"/> BOTTOM
LAYER COUNT	4	TYPE	HAL	COLOR	<input checked="" type="checkbox"/> WHITE <input type="checkbox"/> -----
Cu FOIL THICK	35um (1oz)	GILT	--	SOLDER RESIST	<input type="checkbox"/> GREEN <input checked="" type="checkbox"/> BLUE
SURFACE AREA	0,46 dm2	Au SURFACE	--		
		IPC Class	2		
VIA COUNT	224				
HOLE COUNT	299				
PAD COUNT	765				

Date 03.10.2018.
Projected by 0113
Drawn by M. Osonjački - FER
Controlled by 0076
Approved by 0113

Project name:

PCB-AA300100-01_01

PRINTED CIRCUIT BOARD

ALTPRO d.o.o.
RESEARCH • DEVELOPMENT • PRODUCTION

Catalogue number:
AA300100

Document number:
AA300100-020501010101

Scale 1:1 Sheet 1 / 1

Ver. Date Author

Int. Mark:

Int. Mark: