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APPLICATION SPECIFIC INSTRUCTION SET PROCESSOR DESIGN



Application Specific
Instruction Set Processor
Design

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LAP Lambert Academic Publishing Jul 2014, 2014. Taschenbuch. Condition: Neu. Neuware - A Digital Signal Processor with specific instruction sets and meant for a specific application is called as Application specific Instruction set Processor(ASIP). The optimization of an ASIP becomes handy if it is designed in a higher level of abstraction that is higher than Register Transfer Level (RTL). Several stages are required to design a processor which are architecture design implementation, software development, instruction and system verification. Verification of..

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- Authored by Umakanta Nanda
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