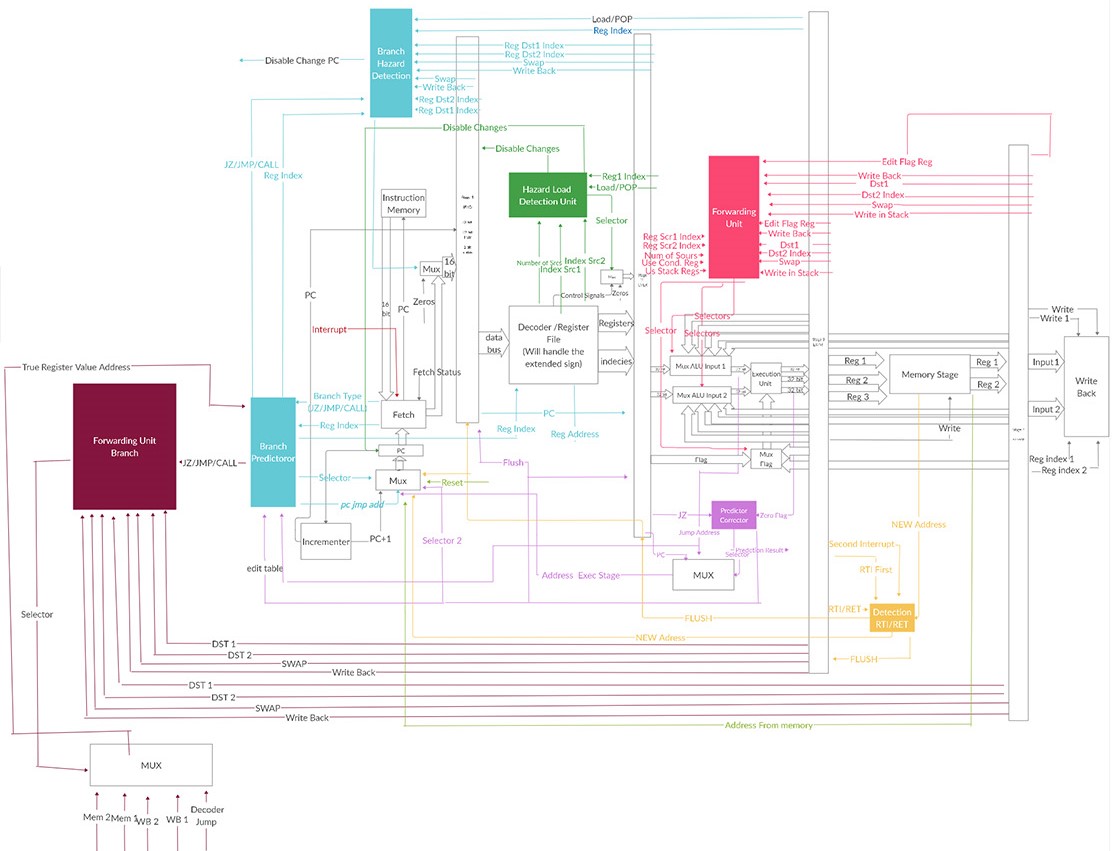
 **Cairo University**

**Faculty of Engineering**

**Project Arch**

**Final delivery**

|  |  |  |
| --- | --- | --- |
| Name | Section | B.N |
| Aly Ramzy Hassan | 1 | 36 |
| Mouhamed Khier | 2 | 12 |
| Nour Ahmed | 2 | 31 |
| Hager Ahmed | 2 | 34 |

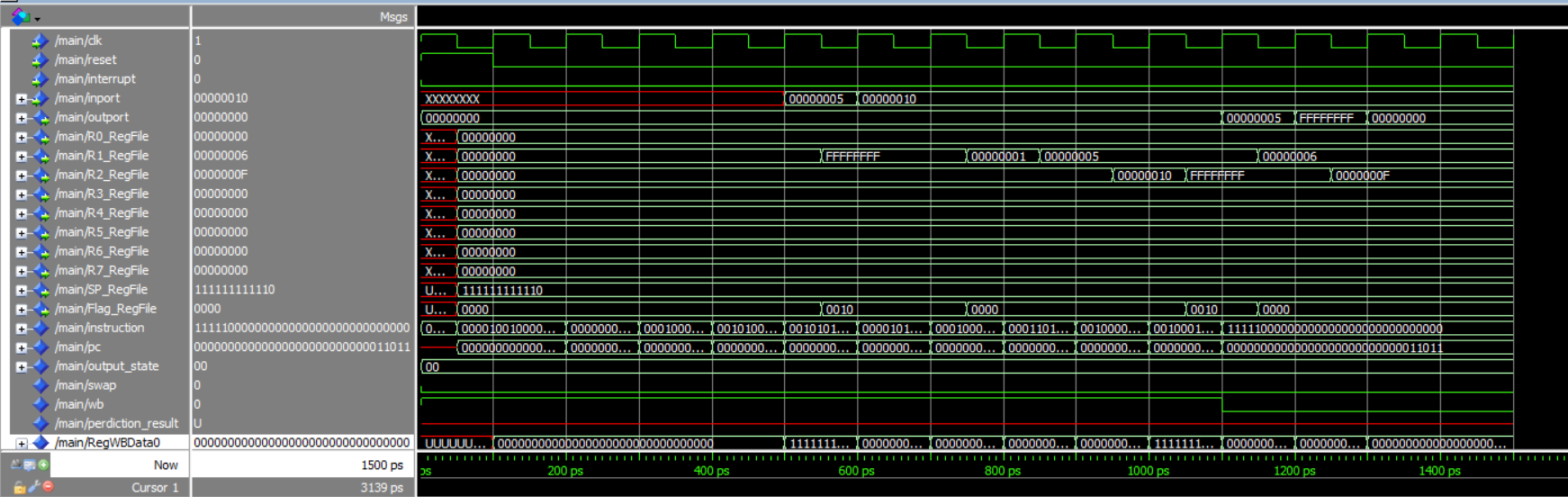
**Full design:**

**The unfinished parts of our processor:**

1. Cash memory
2. Branch prediction

**One operand file:**

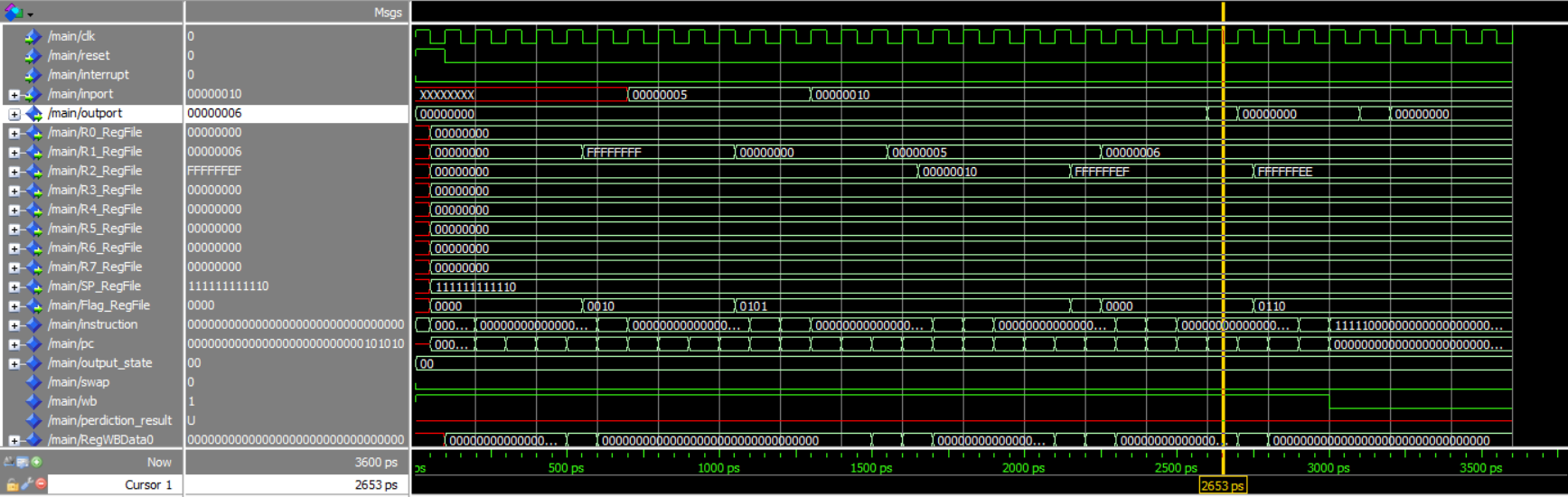
**Before forwarding without no operation**

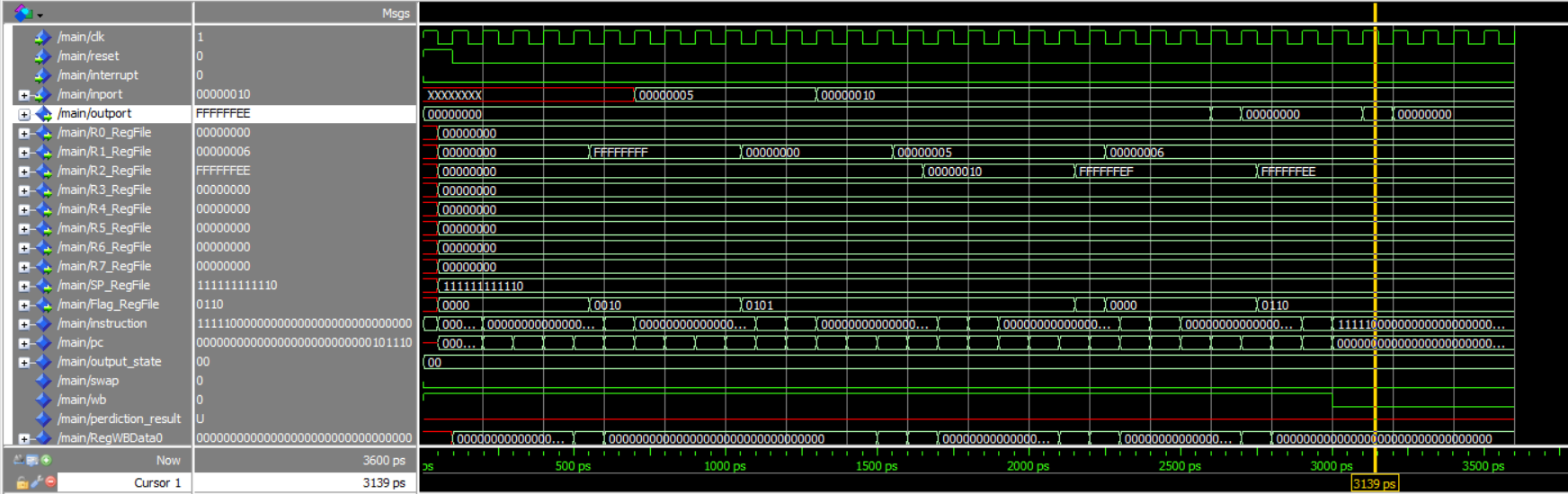
****

**Hazards faced without forwarding:**

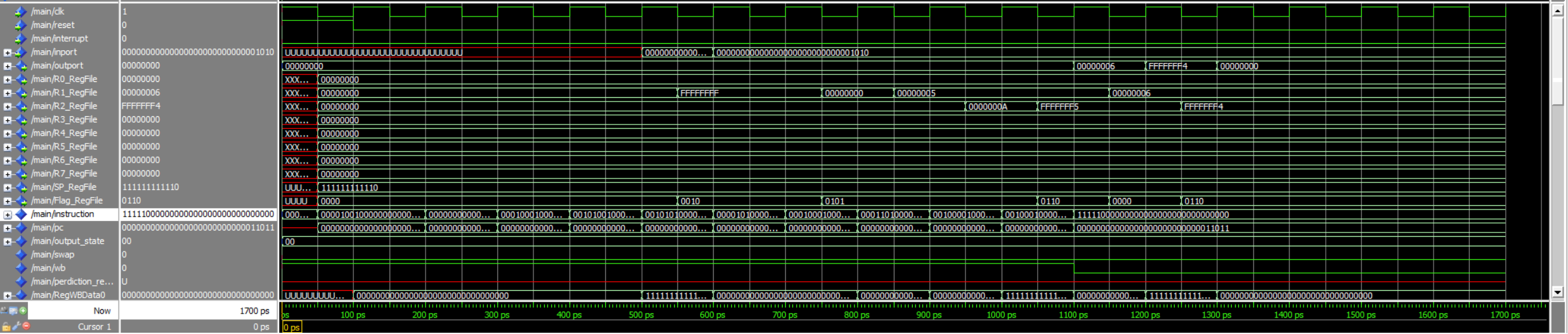
1. Inc R1 hazard is read after write.
2. Not R2 hazard is read after write.
3. Dec R2 hazard is read after write.
4. Out R1 hazard is read after write.
5. Out R2 hazard is read after write.

**Before forwarding with no operation(solving the hazards with nop)**

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**After forwarding:**

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