MCA 1

TERM WORK DEFINATION

Sr. No.	Definition	Roll No.
1	Binary addition	MA007, MA047, MA066
2	Binary to octal	MA015, MA055
3	Octal to binary	MA006, MA046, MA084
4	Binary multiplication	MA029, MA036
5	Hexadecimal to binary	MA008, MA048, MA074
6	Decimal to binary	MA024, MA064, MA071
7	Binary subtraction	MA014, MA054
8	Binary to decimal	MA030, MA037
9	Hamming code	MA005, MA045, MA067
10	Parity check	MA023, MA063, MA080
11	Hexadecimal to decimal	MA013, MA053, MA072
12	Binary to hexadecimal	MA035, MA028, MA078
13	Decimal to hexadecimal	MA004, MA044, MA083
14	Decimal to octal	MA025, MA065, MA075
15	OCTAL TO HEXADECIMAL	MA019, MA059, MA069
16	And or not logic gates with truth table	MA009, MA049
17	NOR and NAND gates with truth table	MA031, MA026, MA077
18	XOR and XNOR gates with truth table	MA001, MA041, MA070
19	5 6 7 8'S COMPLEMENT	MA034, MA052, MA073
20	9 10 11 12's complement	MA012, MA032, MA081
21	Sum of product	MA020, MA060, MA079
22	Minterm	MA003, MA043
23	Maxterm	MA027, MA033, MA082
24	Product of sum	MA016, MA056
25	RS fliflop	MA010, MA050, MA068
26	T flipflop	MA021, MA061
27	JK flipflop	MA017, MA057, MA076
28	Ascii to binary	MA002, MA042
29	4 to 2 encoder	MA038, MA039
30	8 to 3 encoder	MA011, MA051
31	3 to 8 decoder	MA022, MA062
32	4 to 1 multiplexer	MA018, MA040, MA058

Note: Create C Program for above given Definition.