L #6

(5) Indexed addressing mode → @A + DPTR

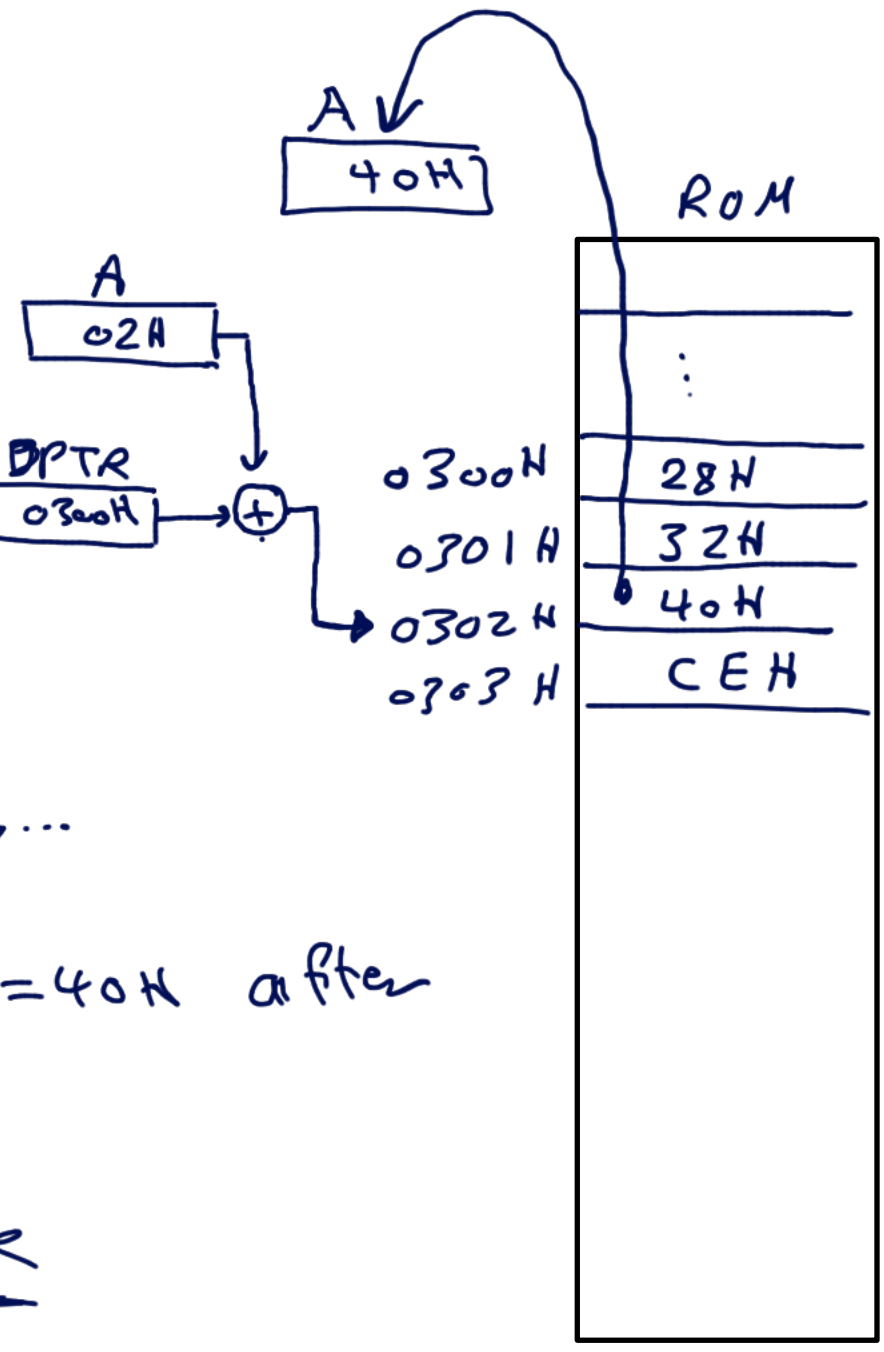
MOVC A, @A+DPTR — ROM

- MOVC → memory code
- @A+DPTR → indexed

```
MOV  A, #2
MOV  DPTR, #300H
MOVC A, @A+DPTR
```

```
ORG 300H
DB  28H, 32H, 40H, 0CEH, ...
```

A = 02H before → A = 40H after



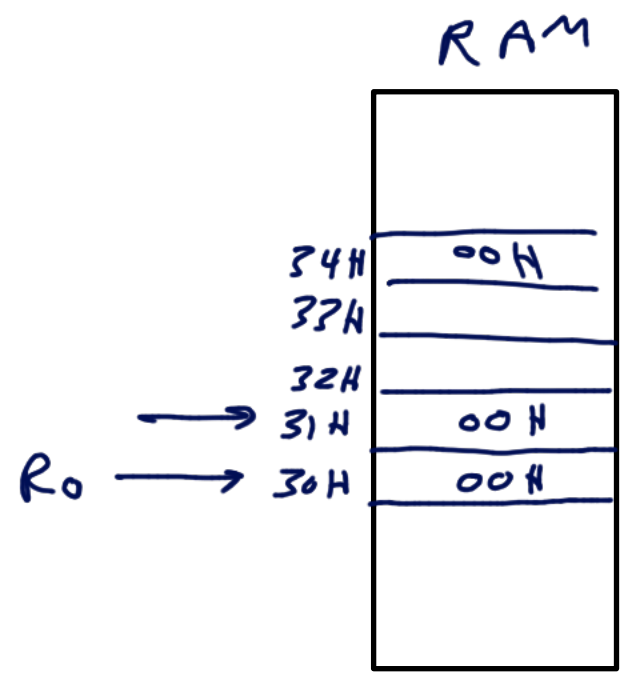
MOVX A, @DPTR

- MOVX → external memory

* Register indirect addressing mode and indexed make accessing data dynamic rather than static.

Clear all memory locations from 30H ⟶ 34H.

```
ORG 0
MOV  30H,#00H
MOV  31H,#00H
 :
MOV  34H, #00H
```



```
ORG  0
CLR  A
MOV  30H,A
MOV  31H,A
 :
MOV  34H,A
```

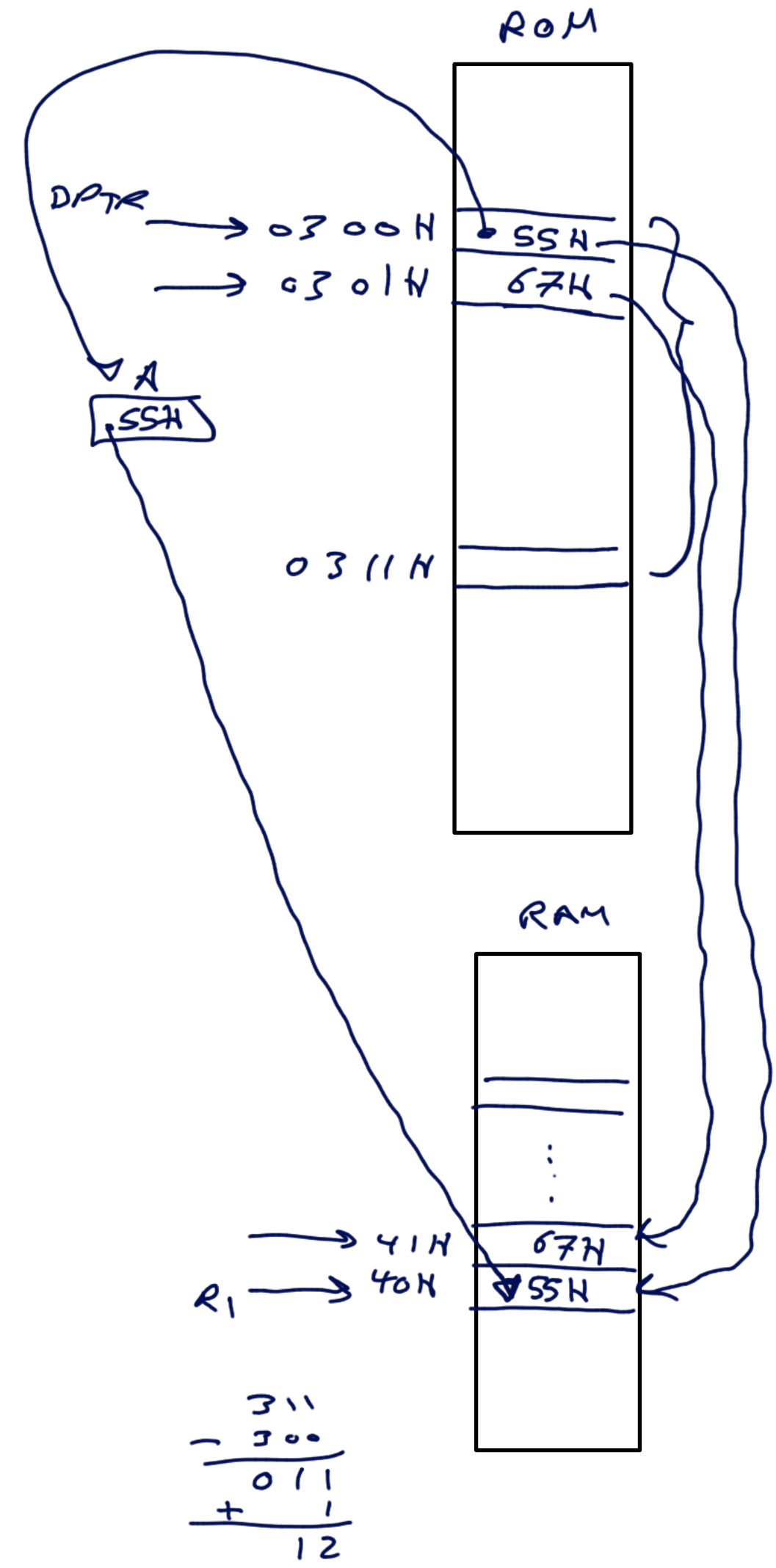
```
ORG  0
MOV  R0, #30H
MOV  R2, #5     ; counter
Back: MOV  @R0, #00H
INC  R0
DJNZ R2, Back
```

$R_2 = 0$

$R_0 = 35H$

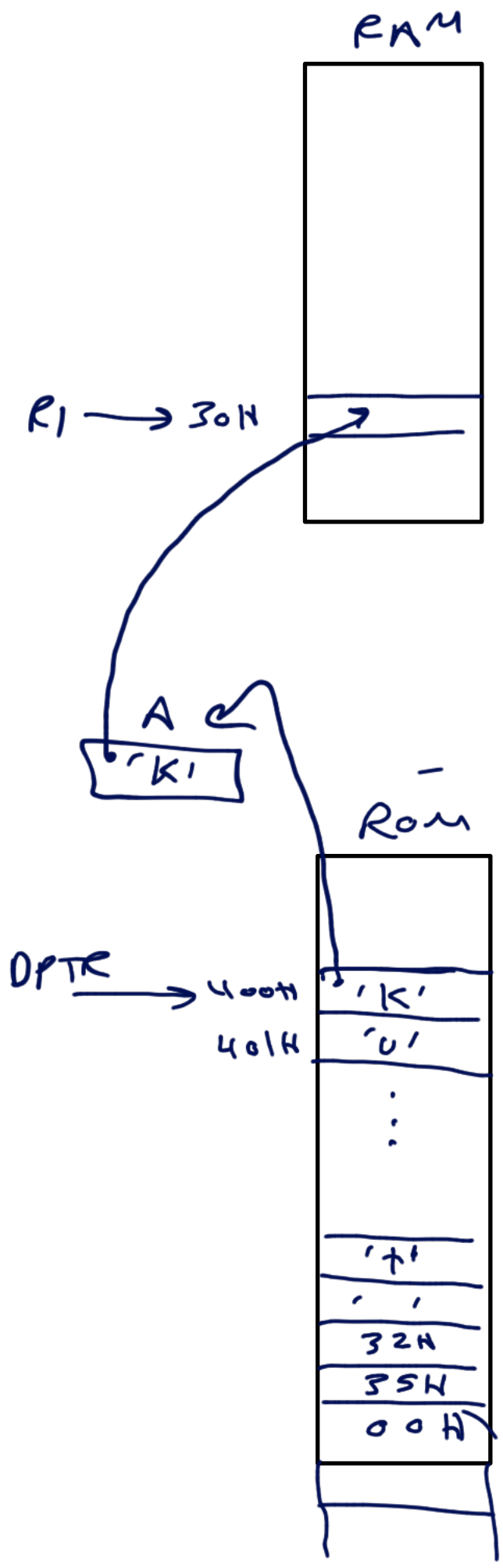
* Write a program to copy the content of memory locations from 300H to 311H and save the look-up table in RAM locations starting at 40H (3)

```
        ORG 0
        MOV DPTR, #300H
        MOV R1, #40H
        MOV R2, #12H
Back:   CLR A
        MOVC A, @A+DPTR
        MOV @R1, A
        INC DPTR
        INC R1
        DJNZ R2, Back
        SJMP $
        END
```



- Write a program to copy "kuwait 25" from ROM into memory locations starting at 30H in RAM. Assum the string starts at location 400H.

```
        ORG  0
        MOV  DPTR, #400H
        MOV  R1, #30H
Back:   CLR  A
        MOVC A, @A+DPTR
        CJNE A, #0, NOTE
        SJMP Done
NOTE:   MOV  @R1, A
        INC  R1
        INC  DPTR
        SJMP Back
Done:   SJMP $

        ORG  400H
mydata: DB   "kuwait 25", 0
        END
```



* In 8051 there is SUBB ↳ with borrow

SUBB A, source ; A = A − source − cy

SUBB → affects flags → cy, AC, P, ov

To subtract we have to clear C.

(9 − 5)

```
MOV  A, #9
CLR  C
SUBB A, #5 ; A = 09H − 05H − 0 ; A = 04H
```

```
MOV  A, #9
SETB C
SUBB A, #5 ; A = 09H − 05H − 1 ⇒ ; A = 03H
```

↳ steps in the CPU:

1. Take 2's complement of source
2. Add it to A.
3. invert cy and AC.

at the end if cy = 0 ⟶ result → positive (+)
cy = 1 ⟶ result → negative (−)

Show me cy, AC and A at the end.

```
MOV A, #5
CLR C
SUBB A, #9
```

---

① $09 = 0000\ 1001$ → 2's → $1111\ 0111$

②

```
   1 1 1
   0 0 0 0  0 1 0 1
+  1 1 1 1  0 1 1 1
-------------------
   1 1 1 1  1 1 0 0
```

③ cy = 0, AC = 0

cy = 1, AC = 1

result is negative

$A = 1111\ 1100 = -4$

$= FCH = -4$

---

* MUL

```
MUL   AB ; AXB
```

B → FF (high)

A → FF (low)

if result > FFH

then ov = 1 → result is correct

if result > FFFFH → ov = 1

result is not correct

```
MOV  A, #25H
MOV  B, #33H
MUL  AB        ; A = 5FH
                 B = 07H
```

```
      25 H
  x   33 H
  --------
     ①6F
  +  6F0
  --------
     75F H        21≥16
     \_/ \_/
   high   low
     ↓     ↓
     B     A
```

```
MOV  A, #0FCH
MOV  B, #2EH
MUL  AB
```

$4 \times 5 = \underline{20}$

```
MOV  A, #4
MOV  B, #5
MUL  AB   ;  A = 14H   B = 00H
```

```
| 00 14 H |
   ↓   ↓
   B   A
```

---

* DIV

DIV AB ; $\frac{A}{B}$ =

A → Quotient (result)

B → remainder

After DIV if OV=1

⇒ B = 0 → error

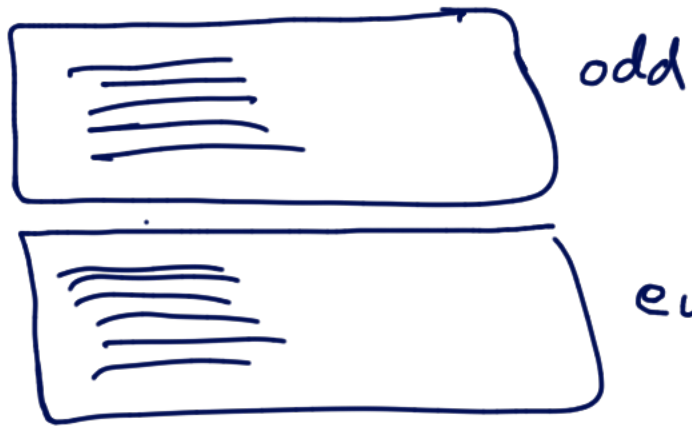
is not allowed

```
MOV   A, # 37

MOV   B, #10
DIV   AB    ;   A = 03H      B = 07H
```

---

* DIV is used to know if the number is even or odd.

```
MOV   A, # ——

MOV   B, #2
DIV   AB    ;   B = remainder
MOV   A,B
JZ    even
```



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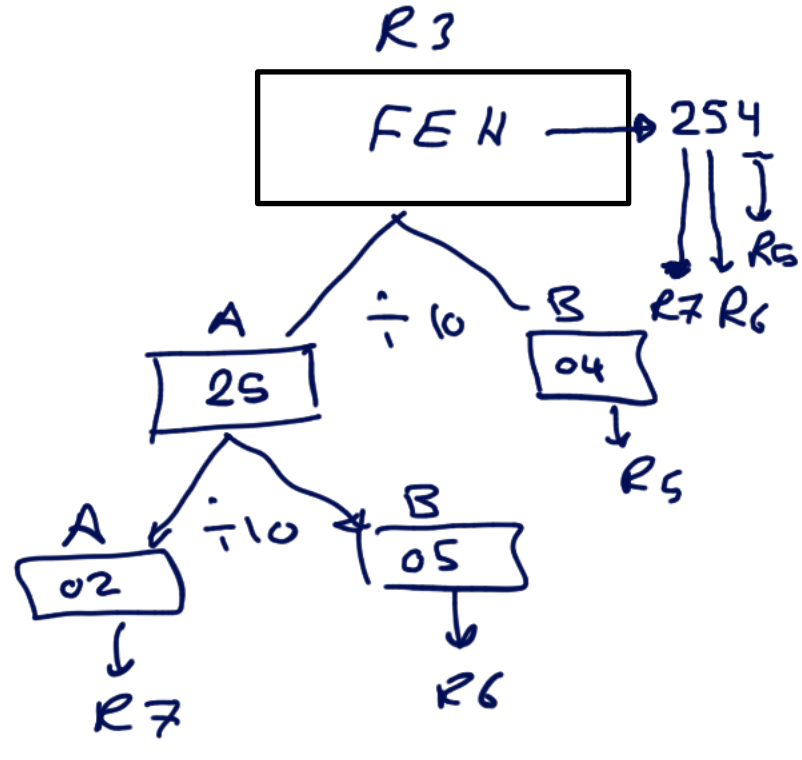
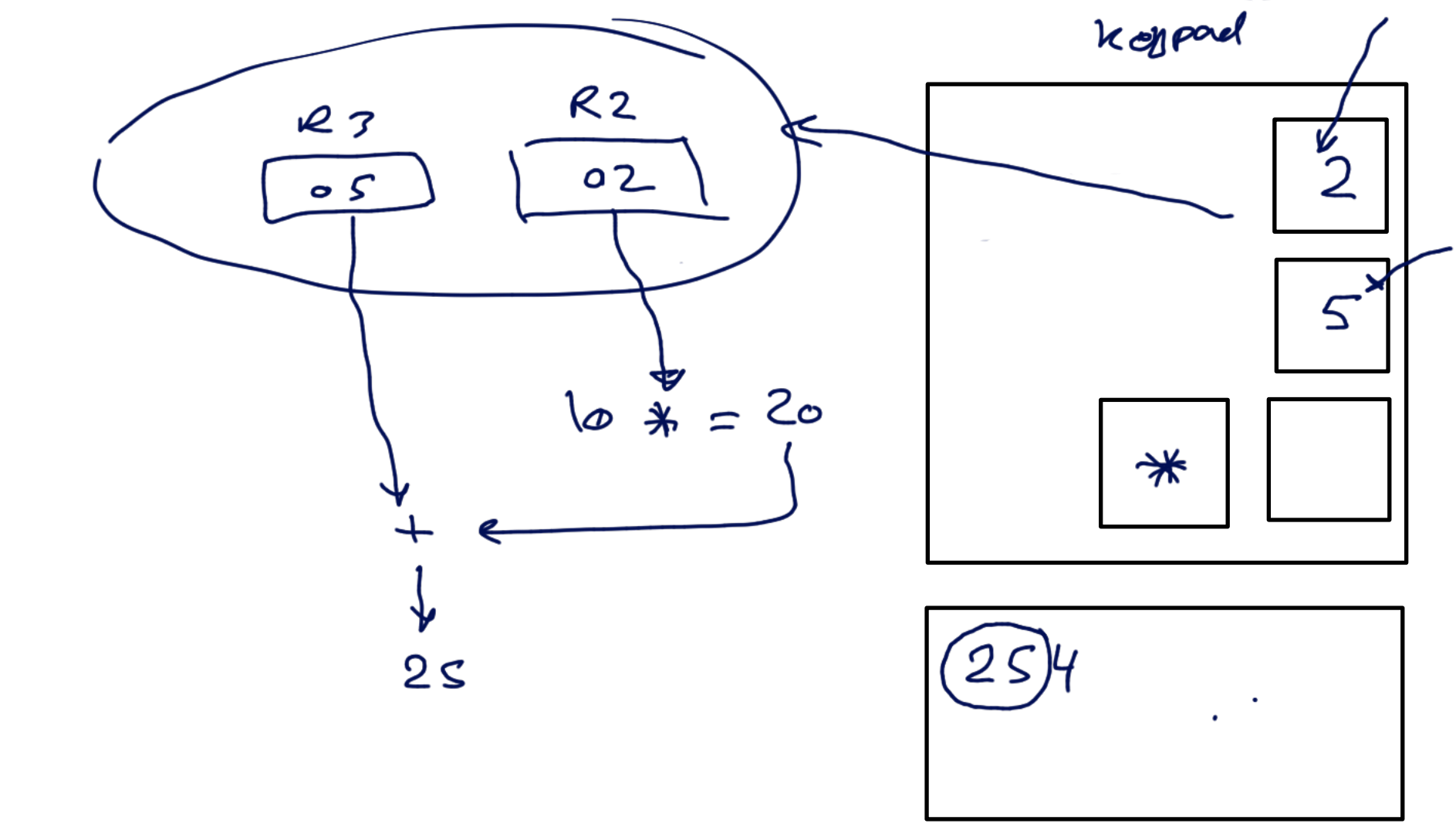
* DIV is used to convert Hex (Binary) into unpacked BCD (decimal).

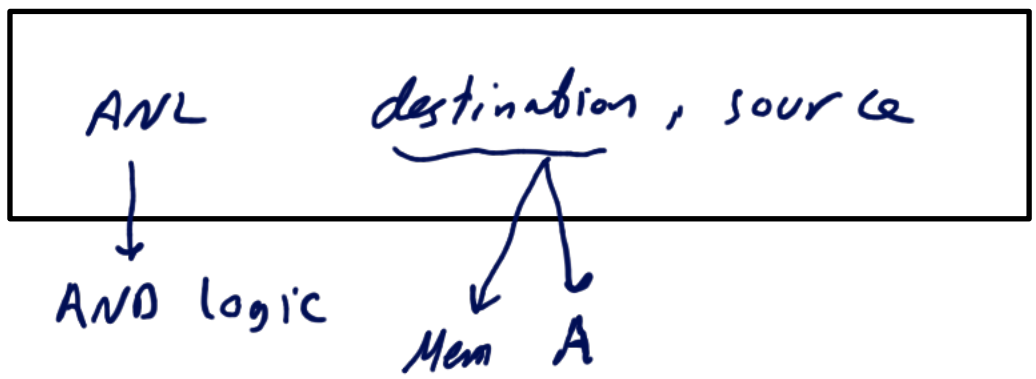
* write a program to convert the content of R3 from Hex to decimal (unpacked BCD). Save results in R5, R6, and R7.

```
MOV   A, R3
MOV   B, #10
DIV   AB      ; A = 25   B = 4
MOV   R5, B
MOV   B, #10
DIV   AB
MOV   R6, B
MOV   R7, A
```





Logical Instructions



a	b	ANL
0	0	0
0	1	0
1	0	0
1	1	1

```
MOV  A, #0C6H
ANL  A, #7AH
```

```
  1100 0110
ANL 0111 1010
----------------
  0100 0010
```

A = 42H

---

* ANL is used to mask some bits
  clear

```
MOV  A, #32H
ANL  A, #0F0H ; A=30H
```

mask ≙

---

clear bits 0, 1, 7 of A

→ ANL A, #0111 1100B

→ ANL A, #7CH

* ANL is used to convert from ASCII to unpacked BCD (decimal)

ASCII

MOV A, #32H

ANL A, #0FH ; A = 02H

	0011 0010
ANL	0000 1111
A =	0000 0010

---

* ORL : OR logic

a	b	ORL
0	0	0
0	1	1
1	0	1
1	1	1

ORL destination, source

mem A

---

* ORL is used to set (1) some bits
* ORL is used to convert from unpacked BCD (decimal) to ASCII.

MOV A, #2

ORL A, #30H ; A = 32H (ASCII)

ORL A, #0001 0001B

set bit4 bit0