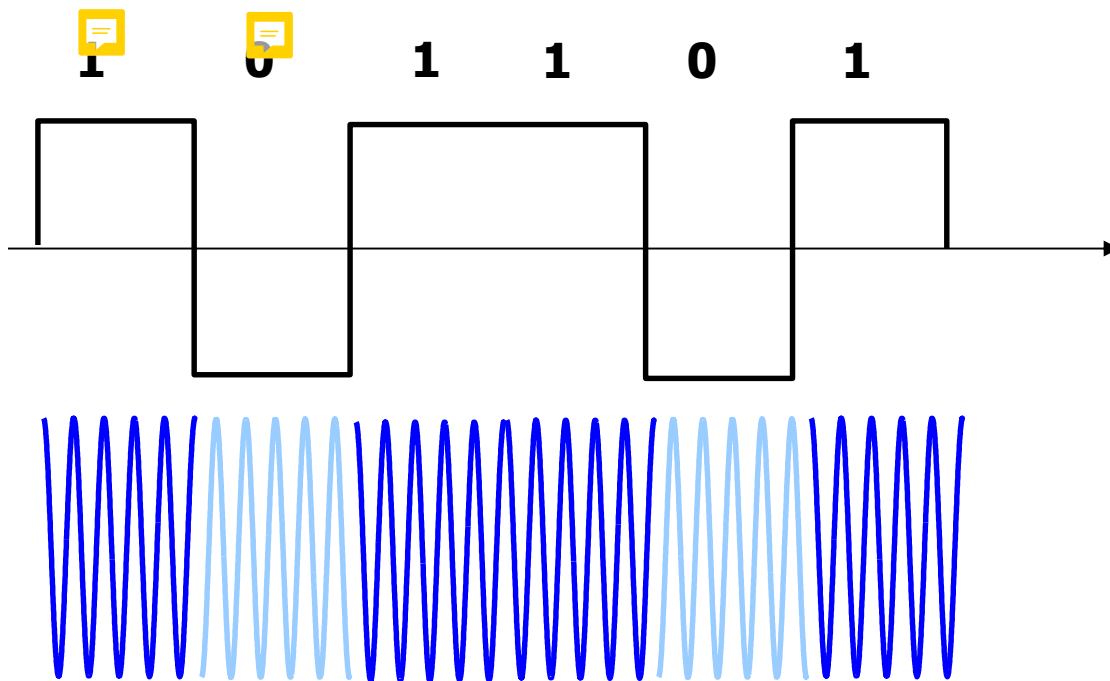


Project

The Figure Below shows the concept of BPSK Modulation. In BPSK a sinusoidal signal is transmitted with phase 0 if Binary 1 should be transmitted and is transmitted with phase 180 if Binary 0 should be transmitted. Use the **BJT Amplifier** configurations to design a single circuit that could be used to implement BPSK modulation.



The Project is divided into Two Milestones:

1. Schematic Circuit Design (Deadline Dec. 31st 2020).
 - a. Screenshot of your Schematic on PSpice (or any other simulation tool)
 - b. Hand Analysis of the Circuit (DC and AC)
2. Circuit Simulation Using PSpice (Deadline Jan. 10th 2020)
 - a. Screenshots including your steps.
 - b. The simulation results of your analysis on PSpice (Pictures and typed comments explaining your results)

Maximum Group Size is 4 Students per Project