Semester: 7<sup>th</sup> Networks, 5<sup>th</sup> Communication

Course: |ELCT508| Communication Microelectronics

Dr. Tallal ElShabrawy



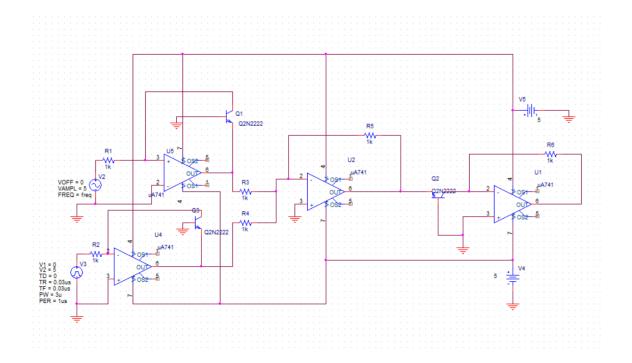
## Milestone 02 Deadline: 14 Jan 2021

You are supposed to:

- 1) Simulate the circuit on Pspice
- 2) Show the simulation results of your analysis on Pspice
- 3) Submit screenshots including the schematic and all your steps and comment on your results.

You can benefit from any of the two schematics chose and you are required to simulate ONLY ONE of them. You can also use your own from Milestone 1.

## 1. OPAMP Schematic



German University in Cairo Winter (2021)

## 2. BJT Aplifier Schematic

