process is accomplished with the carry propagation reversed for fast Fourier transforms (FFTs)

Operand	Option	Example
*	No increment or decrement	LT* Loads the temporary register (TREG) with the content of the data memory address referenced by the current AR.
*+	Increment by 1	LT*+ Loads the temporary reg. (TREG) with the content of the data memory address referenced by the current AR and then subtracts 1 from the content of the current AR.
*0+	Increment by amount	LT*0+ Loads the temporary reg. (TREG) with the content of the data memory address referenced by the current AR and then add the content of AR0 to the content of the current AR.
*BRD+	Increment by Index amount adding with reverse carry.	LT*BRD+ Loads the temporary register (TREG) with the content of the data memory address referenced by the current AR and then adds the

the content of ARO to be content of the current AR, adding with reverse carry propagation

Next Auxiliary Register

- In addition to updating the current auxiliary register, a number of instructions can also specify the next auxiliary register or next AR.
- This register will be the current auxiliary reg. when the instruction execution is complete.