

THE UNIVERSITY OF SHEFFIELD

School of Electrical and Electronic Engineering

3rd Year Individual Project — Project Initialisation Document

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|---------------|-------------------------------------------|---------------|-------------|
| Student Name | Amaan Mujawar | | |
| Project Title | Hardware Accelerator for RISC-V Processor | | |
| Supervisor | Mr Neil Powell | Second Marker | FName LName |

Project Motivation:

Introduce your project and the topic area so that someone with no engineering background could understand it. Include enough theory to justify the project and clarify the goals.

Project Specification:

This specification outlines the project's goals and deliverables. It should be clear enough for another person to carry out the work based solely on this document, without delving into required theory.

Project Schedule:

Identify the main tasks and work packages associated with the project. Outline a clear plan to reach the project's completion, indicating interdependencies and critical paths. Include milestones in a Gantt chart.

Risk Register:

Identify key risks that could delay project completion. Evaluate their likelihood and impact, and propose mitigation strategies. Consider if certain risks expire after specific project phases.

| No. | Description of Risk | Risk Evaluation (L/M/H) | Chance of Risk Occurring (L/M/H) | Mitigation of Risk |
|-----|---------------------|-------------------------|----------------------------------|--------------------|
| 1 | Loss of data | M | L | Multiple backups |