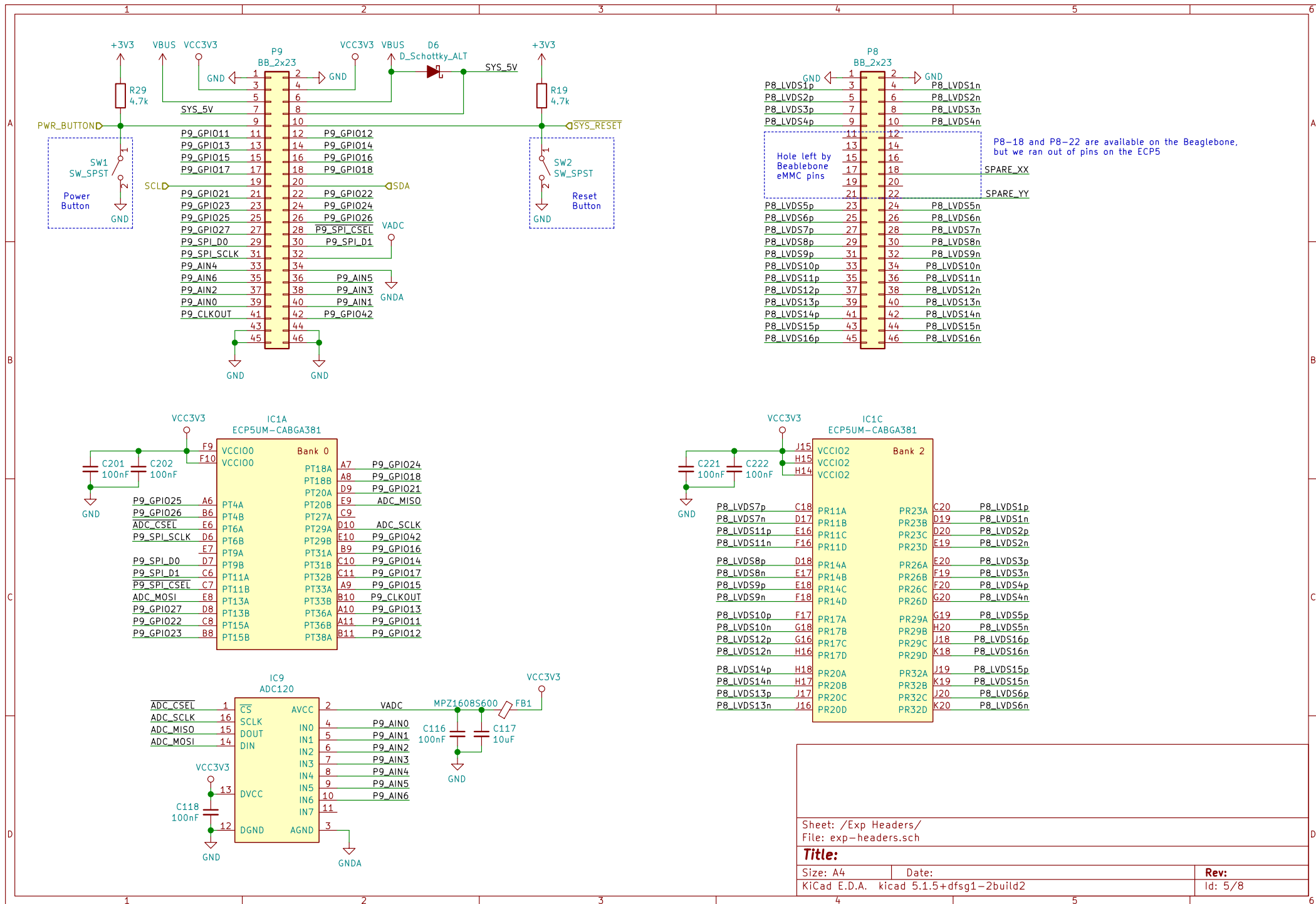
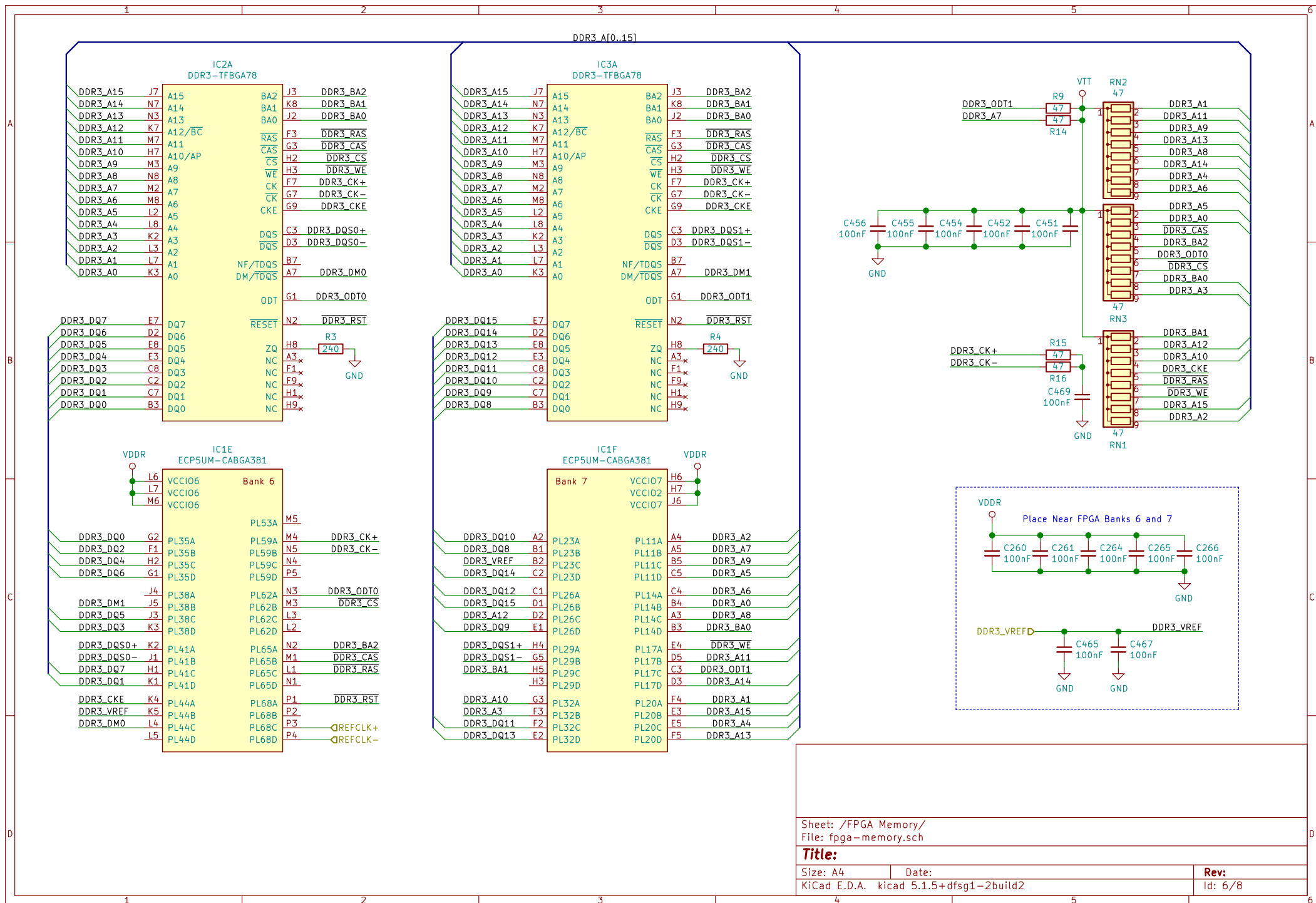


Sheet: /Ethernet/  
File: ethernet.sch

**Title:**

Size: USLegal    Date:    Rev:     
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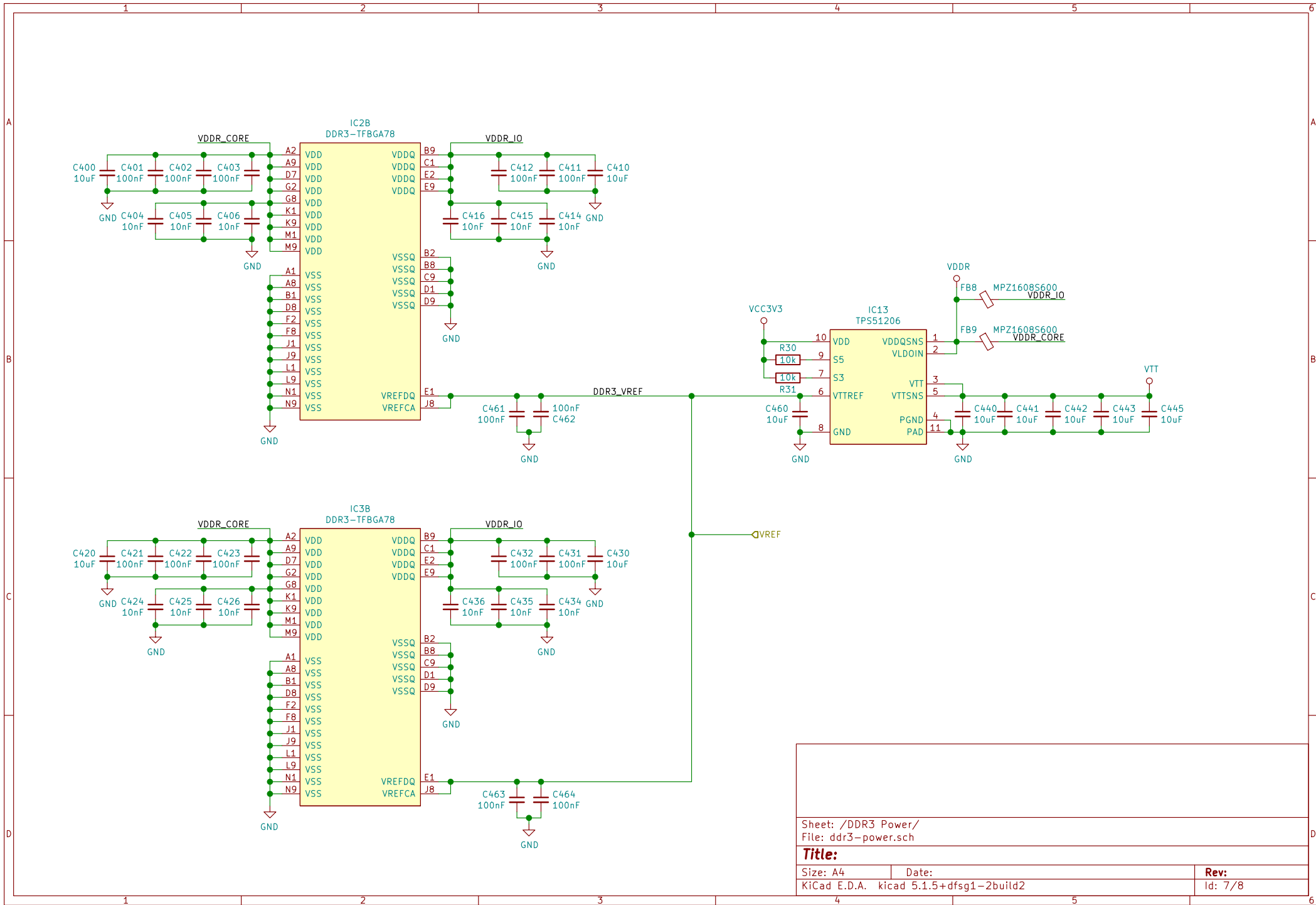


Sheet: /FPGA Memory/  
File: fpga-memory.sch

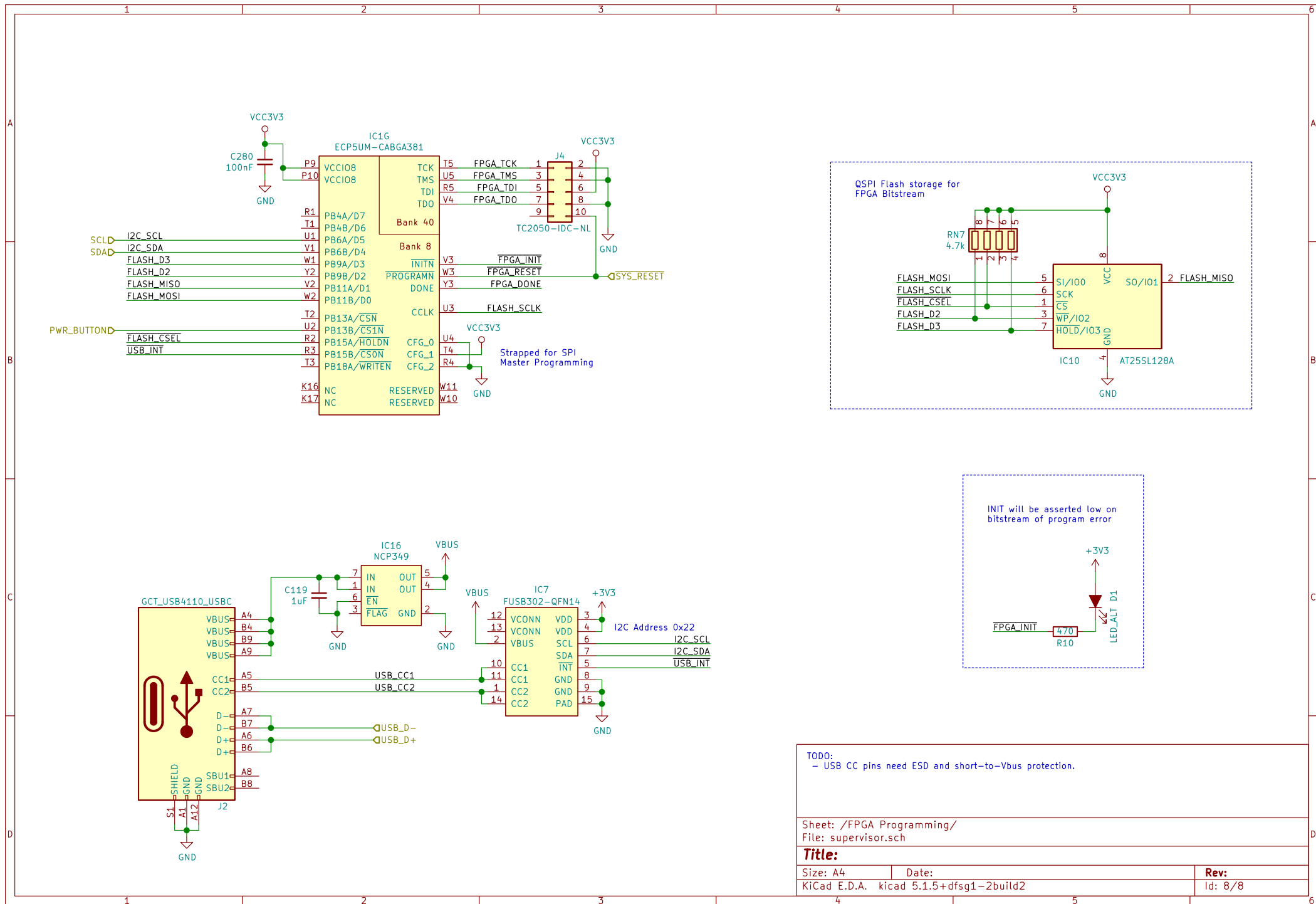
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Size: A4 Date: KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

**Rev:**  
Id: 6/8



Sheet: /DDR3 Power/ File: ddr3-power.sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2		Id: 7/8



TODO:  
- USB CC pins need ESD and short-to-Vbus protection.

Sheet: /FPGA Programming/  
File: supervisor.sch

**Title:**

Size: A4  
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

Date:  
Rev:  
Id: 8/8