AMAN AGGARWAL

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ACADEMIC DETAILS

Degree	Discipline	Institute	Year	CGPA/Percentage
Bachelors of Technology	Electronics and Communication	Jaypee Institute of Information Technology, Sector 62, Noida	2018	6.7 / 10
Senior Secondary	CBSE	Sri Guru Nanak Public School, New Delhi	2014	85.2% / 100%
High School	CBSE	Sri Guru Nanak Public School	2012	8.4 / 10

ELECTIVES/TECHNICAL PROFICIENCY

- **Electives:** Operating Systems, Data Analytics
- **Language and Tools:** C/C++, Python , JavaScript(Basic), HTML, PostgreSQL, Django, Cadence OrCAD, Silvaco ATLAS
- **Hardware Skills:** Arduino IDE, PCB Fabrication

SUMMER INTERNSHIP

DRDO, Civil Lines, Delhi **May 2017- Jul 2017**

Worked on the Project "Wireless Data Communication Using RF module SX1278"

In this project we use SX1278 RF Module which has a range of 3km. Using Arduino IDE, a program is made for transmitting and receiving the data, the results were observed on the serial monitor.

KEY PROJECTS UNDERTAKEN & INTERNSHIPS

- **RNS TO BINARY USING VERILOG HDL** **Aug 2016- Dec 2016**
  The Aim of the project is to minimize the error correction hardware for single fault tolerance in residue number system (RNS).With this objective, a new approach for the design of an error calculator for single fault tolerance in RNS arithmetic is presented. It corrects the error concurrently during normal operation.

- **INTELLIGENT HELMET** **Jan 2017- May 2017**
  The Aim of the project to implement such a technique which is not easy to bypass the basic rule of wearing helmet. We are designing such a system which will make the helmet necessary to wear, thus improve the safety of the biker by using infrared system.

- **ULTRA-LOW-POWER NEGATIVE CAPACITANCE FET(NCFET)** **Aug 2017- May 2018**
  Negative Capacitance FET (NCFET) can be viewed as a FET with built-in voltage amplification with < 60 mV/decade subthreshold swing (SS). FinFET has superior scalability and is the preferred sub-20 nm technology. Using Silvaco.