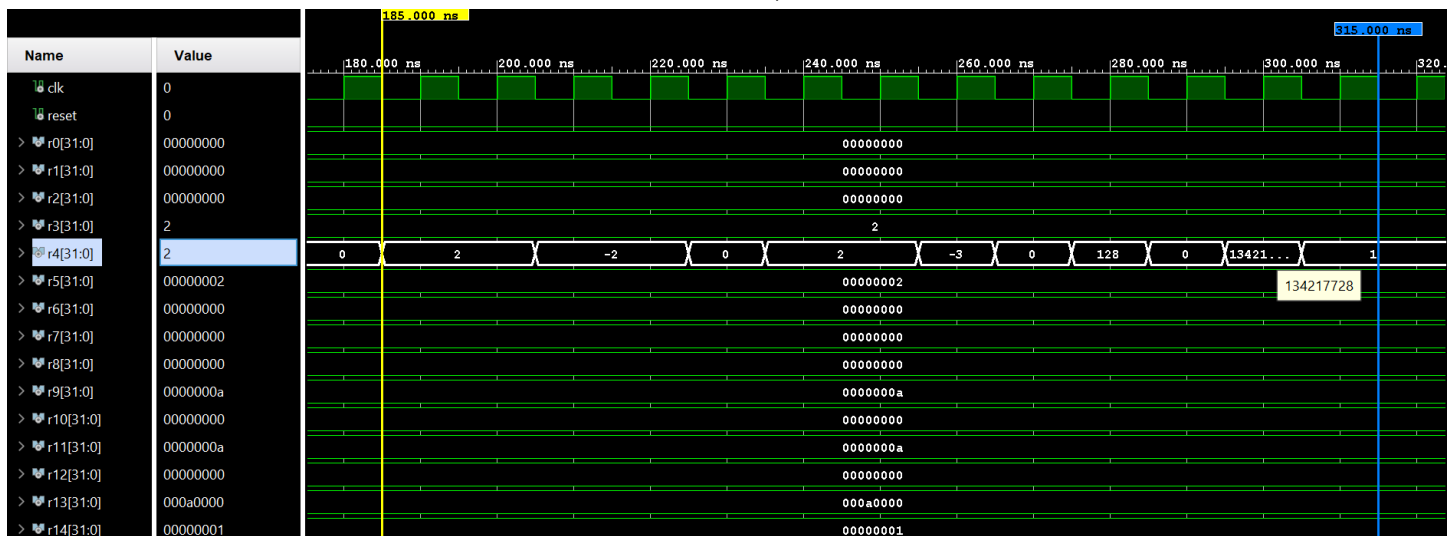


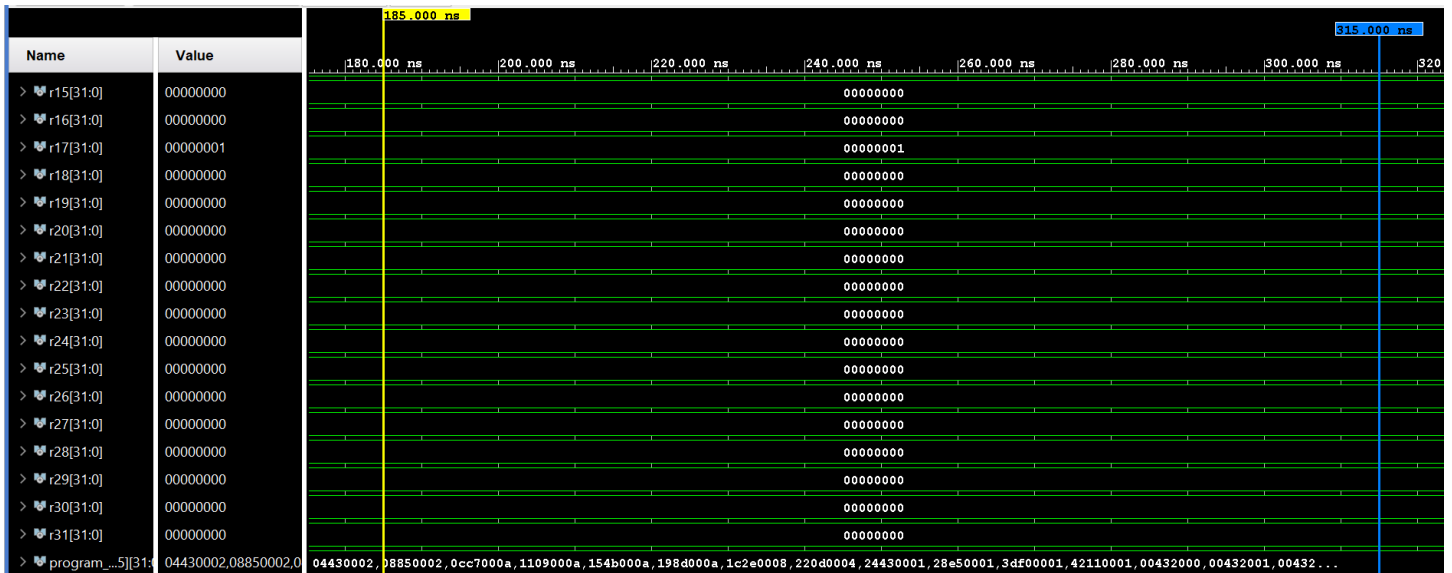
Outputs For testbench for Reduced ISA

I Type Instructions (55ns – 175ns) [outputs are shown in hexadecimal]



R Type Instructions (185ns – 315ns)[outputs are shown in signed decimal]

[illegible][illegible]



*First output received at yellow marker and last output received at blue marker.

J Type Instructions (jump occurs at 310ns and output begins to come at 355ns)[outputs are shown in hexadecimal]

