



BRIEF SUMMARY

- SOFTWARE PROGRAMMING;
SQL DBMS ; Relational Database Management System (RDBMS) ; Database Management Systems (DBMS) ; Python (Programming Language) ; C (Programming Language) ; C++ (Programming Language)
- HARDWARE DESIGNING
Digital & Analog Simulation ; Analog Modeling , Designing & Synthesis ; VLSI Technology , Circuit Design & Testing ; Microarchitecture/RTL/Logic Development ; RTL ; SYNTHESIZABLE RTL SIMULATION MODELS & DESIGN , Antenna Designing
- COMMUNICATION
Analog & Digital Communication System ; Communication Network & related Technology ; Computer Networking ; Cisco Network Design ; OPTICAL FIBER COMMUNICATION (Systems , Sensors & Transmission) ; Network & Communication Management ; Telecommunications ; Cryptography and Encryption
- DIGITAL & Analog
Electronic Devices ; DIGITAL & ANALOG Signal Processing ; Digital Audio , VIDEO & Image Processing ; DAC & ADC inter-conversion
- SYSTEM DESIGN
Xilinx ISE Design Suite ; VHDL Hardware Description Language (VHDL) Programming & DESIGN ; Verilog ; Digital IC Design ; Microprocessor

Softwares Used

- VS Code
- MySQL
- MATLAB
- OrCAD Capture CIS
- PSpice
- LT Spice
- MultiSIM
- EasyEDA
- Ansys HFSS
- OPTSIM
- Cisco Packet Tracer
- Xilinx
- Emu 8085 & 8086

EDUCATION

University School of Information, Communication and Technology, GGSIPU B.Tech. - Electronics & Communication Engineering CGPA: 9.00 / 10.00	2020 - 2024
Himalaya Public Senior Secondary School, New Delhi 12 th CBSE Percentage: 86.00 / 100.00	2020
Himalaya Public Senior Secondary school, New Delhi 10 th CBSE Percentage: 92.00 / 100.00	2018

INTERSHIPS

Eduversity startup india Trainee Engineer	25 Jul, 2022 - 05 Sep, 2022
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ACHIEVEMENTS

- GATE ECE 2023 QUALIFIED - 92 Percentile

PERSONAL DETAILS

Gender: Male	Date of Birth: 28 Jul, 2002
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