

# Microprocessor - I/O Interfacing Overview

In this chapter, we will discuss Memory Interfacing and IO Interfacing with 8085.

**Interface** is the path for communication between two components. Interfacing is of two types, memory interfacing and I/O interfacing.

## Memory Interfacing

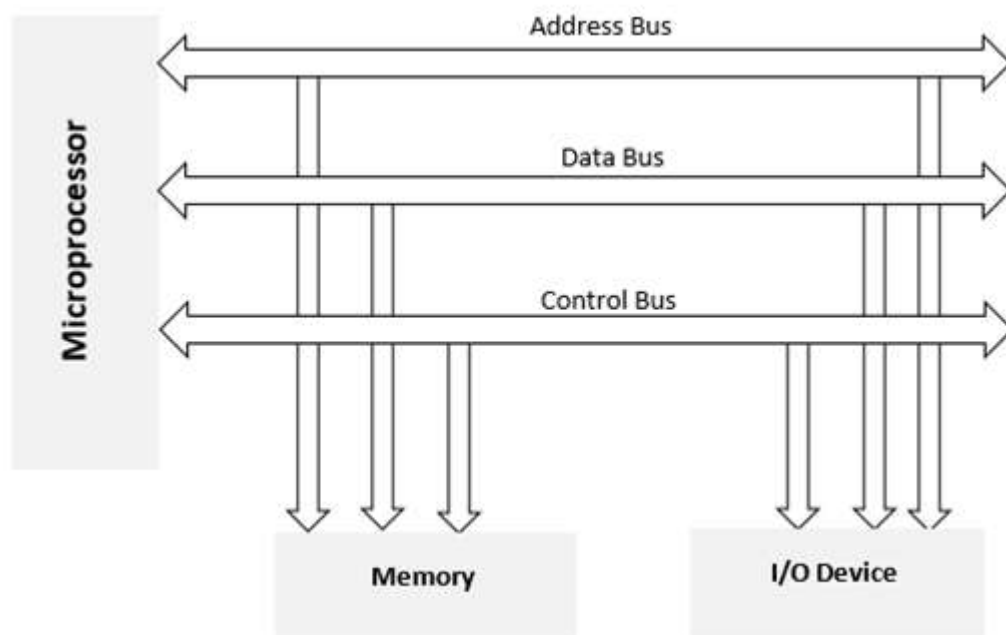
When we are executing any instruction, we need the microprocessor to access the memory for reading instruction codes and the data stored in the memory. For this, both the memory and the microprocessor requires some signals to read from and write to registers.

The interfacing process includes some key factors to match with the memory requirements and microprocessor signals. The interfacing circuit therefore should be designed in such a way that it matches the memory signal requirements with the signals of the microprocessor.

## IO Interfacing

There are various communication devices like the keyboard, mouse, printer, etc. So, we need to interface the keyboard and other devices with the microprocessor by using latches and buffers. This type of interfacing is known as I/O interfacing.

## Block Diagram of Memory and I/O Interfacing



## 8085 Interfacing Pins

Following is the list of 8085 pins used for interfacing with other devices –

- A<sub>15</sub> - A<sub>8</sub> (Higher Address Bus)
- AD<sub>7</sub> - AD<sub>0</sub> (Lower Address/Data Bus)
- ALE
- RD
- WR
- READY

## Ways of Communication – Microprocessor with the Outside World?

There are two ways of communication in which the microprocessor can connect with the outside world.

- Serial Communication Interface
- Parallel Communication interface

**Serial Communication Interface** – In this type of communication, the interface gets a single byte of data from the microprocessor and sends it bit by bit to the other system serially and vice-a-versa.

**Parallel Communication Interface** – In this type of communication, the interface gets a byte of data from the microprocessor and sends it bit by bit to the other systems in simultaneous (or) parallel fashion and vice-a-versa.