SRM Institute of Science and Technology
College of Engineering and Technology
School of Computing

Mode of Exam
OFFLINE

Common to Computing Technologies/Networking and Communication/Computational Intelligence/Data science and Business systems

SRM Nagar, Kattankulathur – 603203, Chengalpattu District, Tamilnadu

Academic Year: 2021-22 (ODD)

Test: CLAT-1   **Date: 26/10/2021**
Course Code & Title: 18CSS201J – Analog and Digital Electronics   **Duration:** 50 Mins
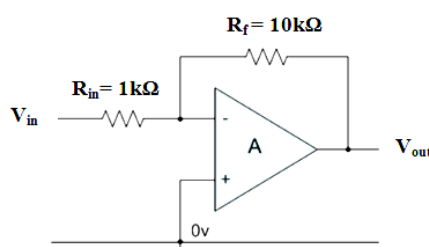**Year & Sem: II & III**   **Max. Marks: 25**

Course Articulation Matrix:

Course Outcomes	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
CO1	H	H													
CO2	H	H													
CO3	H		H	H											
CO4	H	H	H	H	H							H			
CO5	H		H	H	H										
CO6			H	H		H			H						

	Part - A (3 x 4 Marks = 12 Marks)					
Instructions: Answer any three questions						
Q. No	**Question**	**Marks**	**BL**	**CO**	**PO**	**PI Code**
1	Distinguish between JFET and BJT	4	2	1	1	1.6.1
2	Show the output characteristics of common emitter configuration and mark the 3 regions of operation	4	1	1	1	1.6.1
3	Determine the closed loop gain of the given inverting amplifier circuit.	4	3	1	1,2	1.2.1, 2.5.1
4	Briefly explain the operation of voltage series feedback amplifier with diagram	4	2	1	1	1.6.1
	Part – B (1 x 13 Marks = 13 Marks)					
Instructions: Answer all the questions						
5(a)	Draw and explain the operation of the circuit, where the collector current flows at all times during the full cycle of the input signal with transformer coupled and also derive its efficiency.	13	2	1	1,2	1.6.1
	(or)					
5(b)	Discuss the following with a neat Diagram (i) Comparator (ii) IC 555 Timer	13	2	1	1,2	1.6.1