

INDEX SHEET

Exp. No	Date of Experiment	Name of the Experiment	Page No	Marks (20)	Staff Signature
1		Design and Implementation of Half Wave and Full Wave Rectifiers using simulation package and demonstrate its working			
2		Design and implement a Schmitt trigger using Op-Amp using a simulation package and demonstrate its working			
3		Design and implement a rectangular waveform generator (Op-Amp relaxation oscillator) using a simulation package and demonstrate the working of it			
CLA P1 (5 Marks)					
4		Design and implementation of transistor as a switch			
5		Design CMOS Inverter and measure its propagation delay for both the rising edge and the falling edge			
6		Design and implementation of Binary to gray code converters and gray to binary code conversion using logic gates. Hardware Implementation of Code Converters Using NI Analog Discovery			
7		Design and implementation of Magnitude Comparator combinational circuits using simulation package. Hardware Implementation Using NI Analog Discovery			
CLA P2 (7.5 Marks)					

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8		Design and implementation of Synchronous sequential circuits using Simulation Package			
9		Implementation of SISO, SIPO, PISO and PIPO shift registers using Flip- flops			
10		Design and simulation of 3-bit Synchronous up and down counter using Multi sim			
11		Digital Applications			
CLA P3 (7.5 Marks)					