SRM Institute of Science and Technology
College of Engineering and Technology

Mode of Exam
OFFLINE

Common to EEE, ECE, Mechanical, Mechatronics and CSE

SRM Nagar, Kattankulathur – 603203, Chengalpattu District, Tamilnadu

Academic Year: 2021-22 (EVEN) **SET-B**

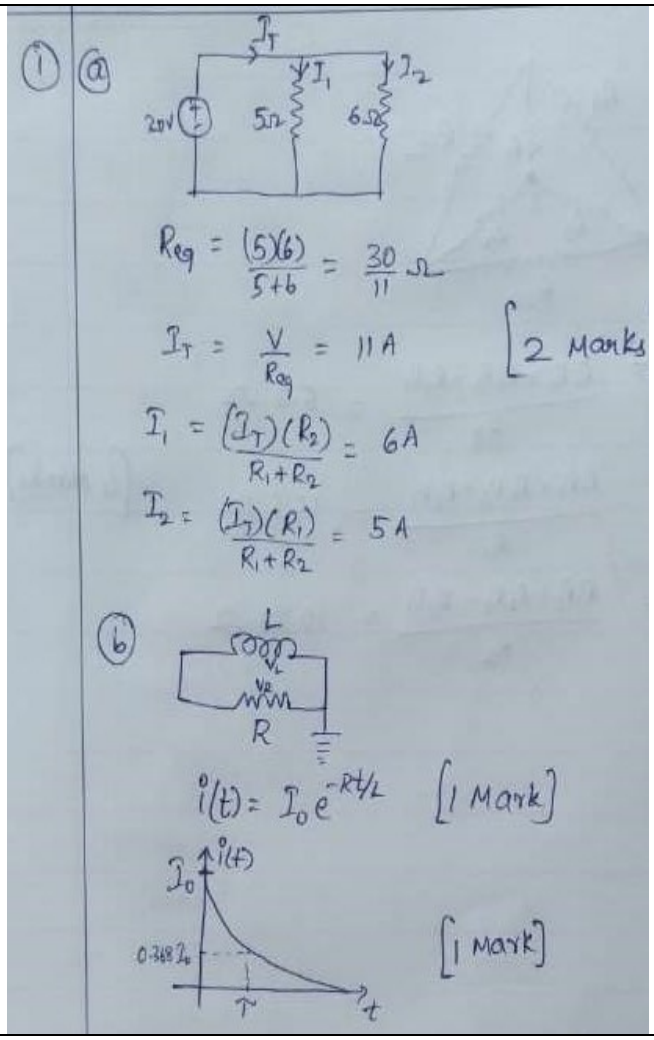
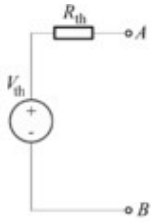
Test: CLAT-1 **Date: 21/04/2022**
Course Code & Title: 18EES101J – Basic Electrical and Electronics Engineering **Duration:** 50 Mins
Year & Sem: I & II **Max. Marks:** 25

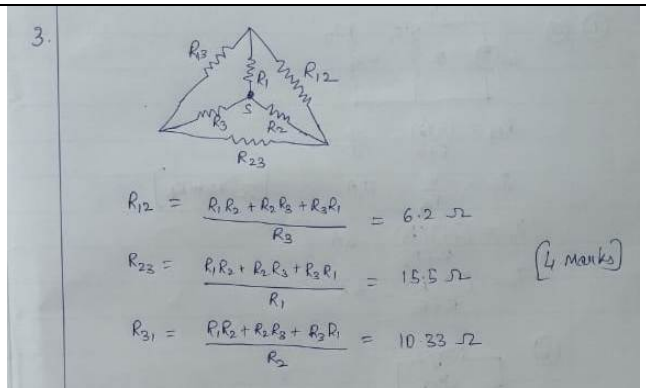
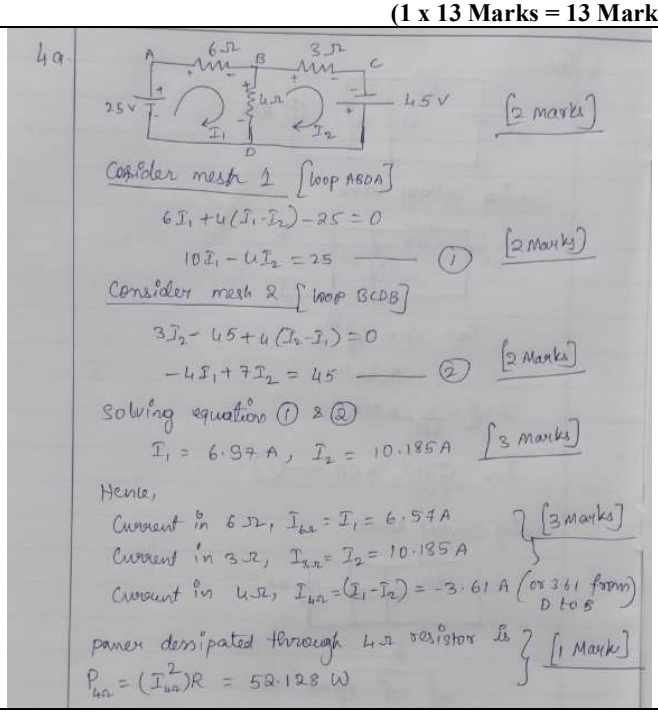
Course Articulation Matrix:

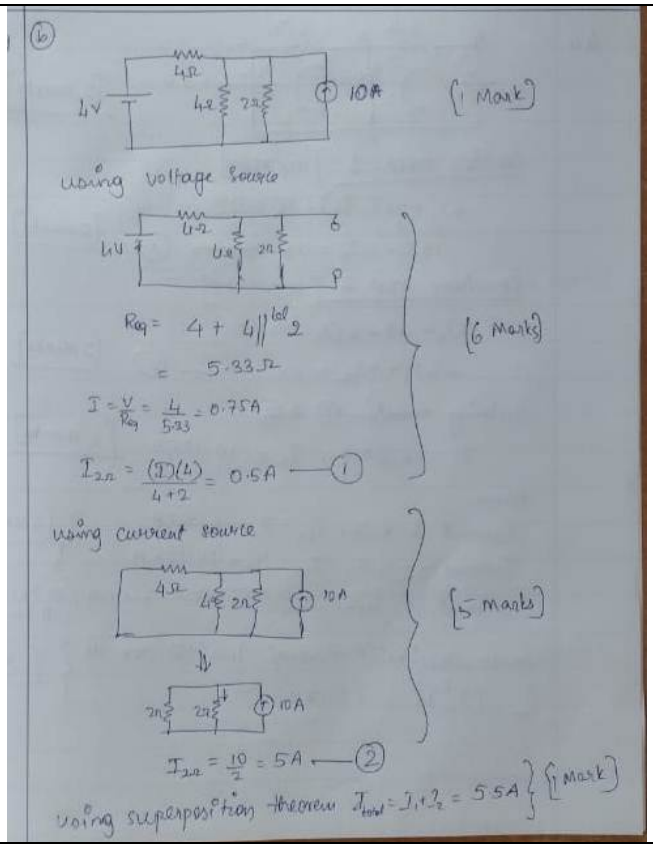
Course Outcomes	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
CO1	H	M	L	L	M	-	M	M	M	M	-	M	-	-	-
CO2	H	M	L	L	M	-	M	M	M	M	-	M	-	-	-
CO3	H	-	L	L	M	-	M	M	M	M	-	M	-	-	-
CO4	H	-	L	M	M	-	M	M	M	M	-	M	-	-	-
CO5	H	M	M	M	M	-	M	M	M	M	-	M	-	-	-
CO6	-	-	L	2	M	-	M	M	M	M	-	M	-	-	-

Part - A
(3 x 4 Marks = 12 Marks)

Q. No	Answer all the questions	Marks	BL	CO	PO	PI Code

1		4		1		
2	For any linear electrical network containing only voltage sources, current sources and resistances can be replaced at terminals A–B by an equivalent combination of a voltage source V_{th} in a series connection with a resistance R_{th}. – **1 mark** The equivalent voltage V_{th} is the voltage obtained at terminals A–B of the network with terminals A–B open circuited. **– 1 mark** The equivalent resistance R_{th} is the resistance that the circuit between terminals A and B would have if all ideal voltage sources in the circuit were replaced by a short circuit and all ideal current sources were replaced by an open circuit **– 1 mark**  **- 1 mark**	4		1		

3		4		1		
	1Mark for circuit and each value 1 Mark each					
	Part – B **(1 x 13 Marks = 13 Marks)**					
4(a)		13		1		
	(or)					

4(b)			13		1	

Question Paper Setter

Approved by Audit Professor/ Course Coordinator