

**SCHOOL OF COMPUTING**  
**SRMIST, KATTANKULATHUR**

**INDEX**

**Name of the student:**

**Semester:**

**Branch & Section:**

**Reg. No:**

**Subject Code: 18CSC203J**

**Subject Title: Computer Organization  
and Architecture**

| <b>Exp. No</b> | <b>Date</b> | <b>Title of the Experiment</b>   | <b>Signature</b> |
|----------------|-------------|--|------------------|
| 1              |             | Exploring Components of Virtual Desktop  |                  |
| 2              |             | Assembling and Disassembling of Virtual Desktop  |                  |
| 3              |             | 8086 Simulator [8-bit] (Immediate, Direct Addressing Mode)   |                  |
| 4              |             | 16-bit Addition and Subtraction  |                  |
| 5              |             | Multiplication of 8-bit numbers and Factorial  |                  |
| 6              |             | Half Adder, Full Adder   |                  |
| 7              |             | Ripple Carry Adder   |                  |
| 8              |             | Carry Look Ahead Adder   |                  |
| 9              |             | Design of Array Multiplier / Binary Multiplier (2-bit multiplier)  |                  |
| 10             |             | Carry Save Multiplier  |                  |
| 11             |             | Multiplexer (4:1 multiplexer, 8:1 and 16:1 Multiplexer)  |                  |
| 12             |             | Arithmetic Logic Unit (Addition and subtraction arithmetic operations and AND, OR, XOR logical operations) |                  |

