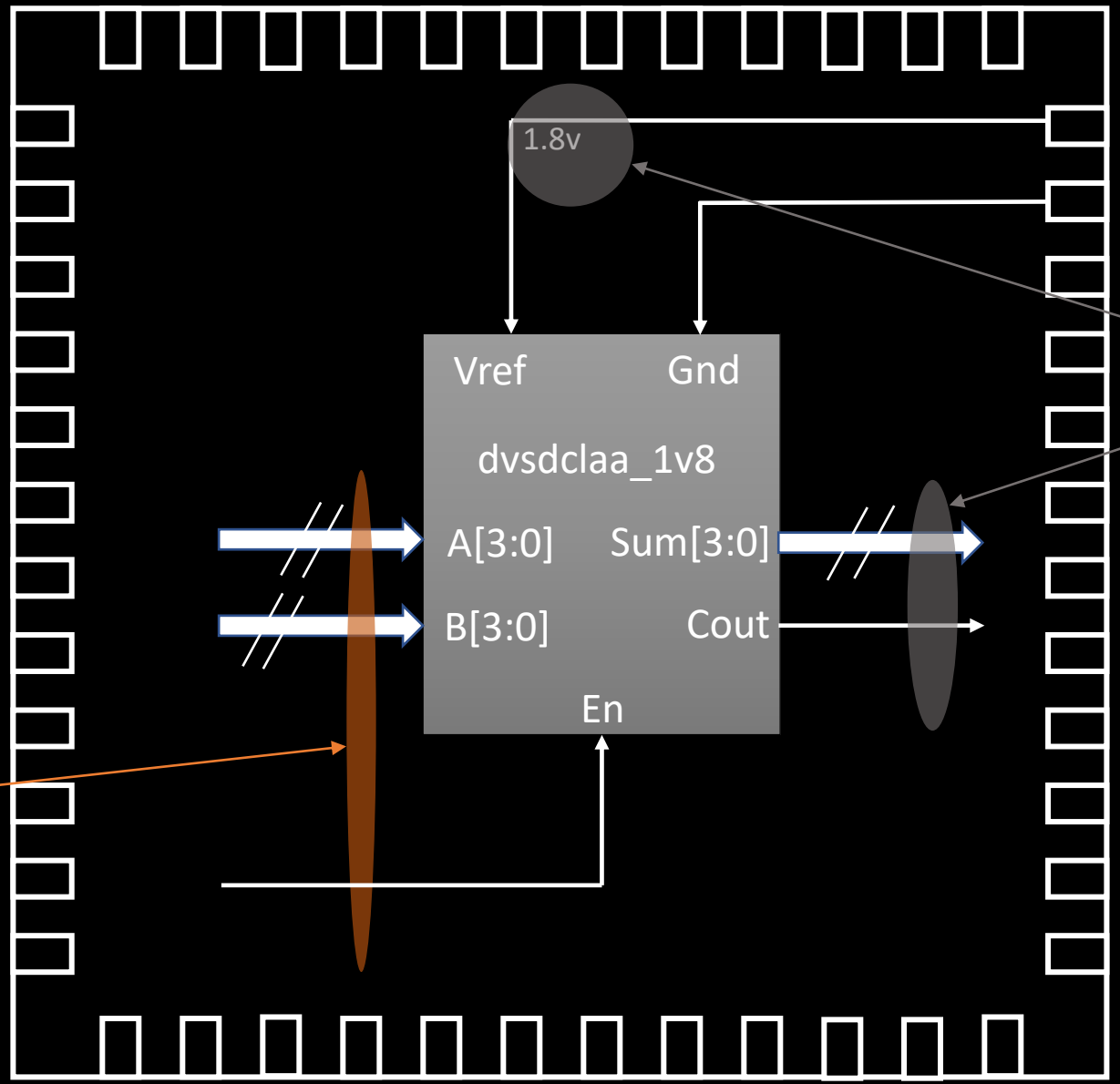
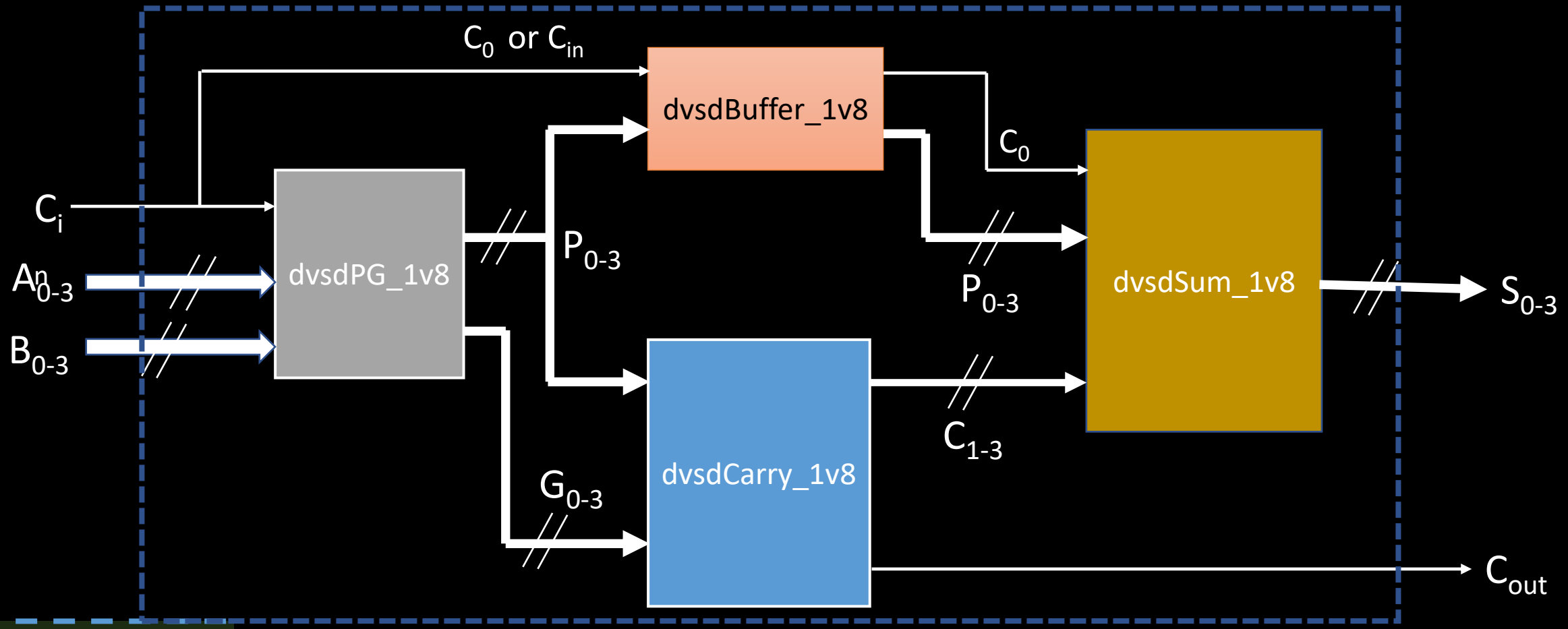
4-bit CLA adder spec sheet for 130nm tech node

- Specs Released under APACHE LICENSE 2.0
- For any query please contact aman4585lmp@gmail.com

Application note for 4-bit CLA Adder (dvsdclaa_1v8)

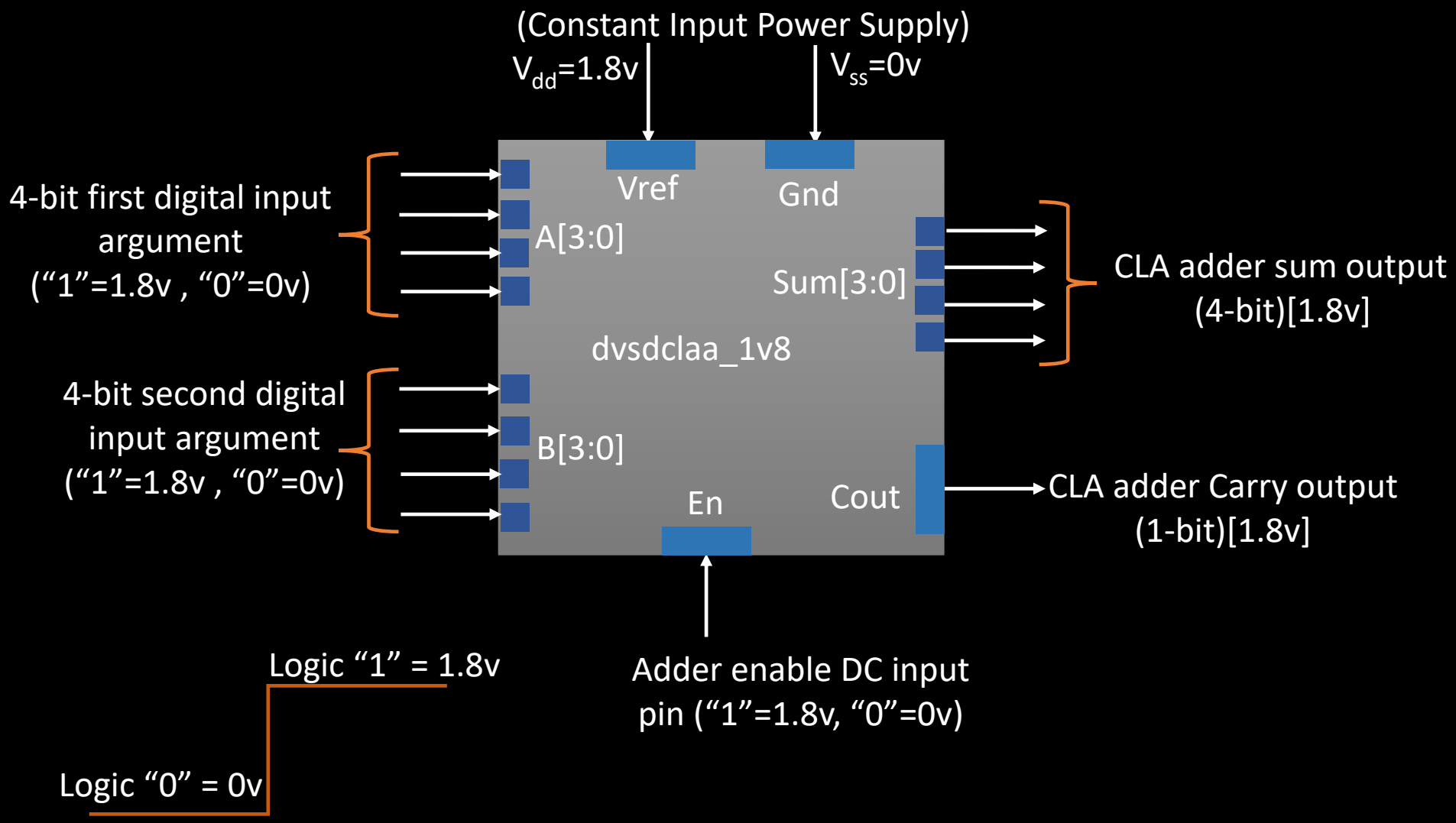


Inside view of dvsdclaa_1v8

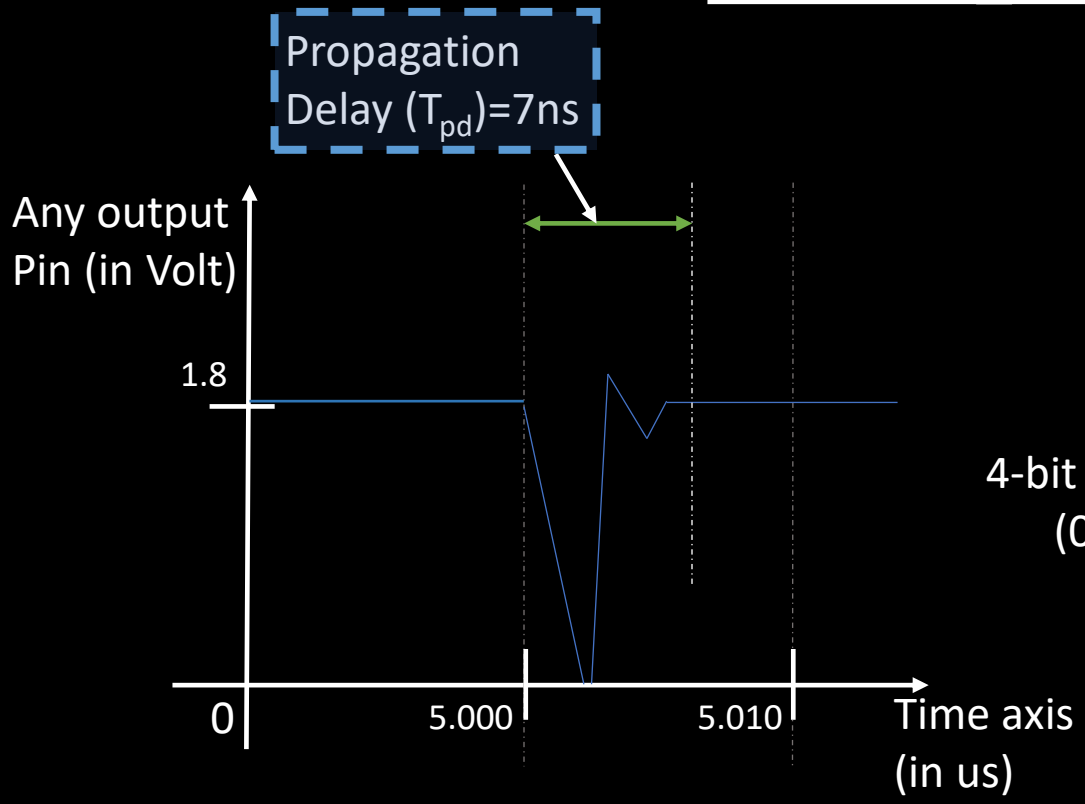


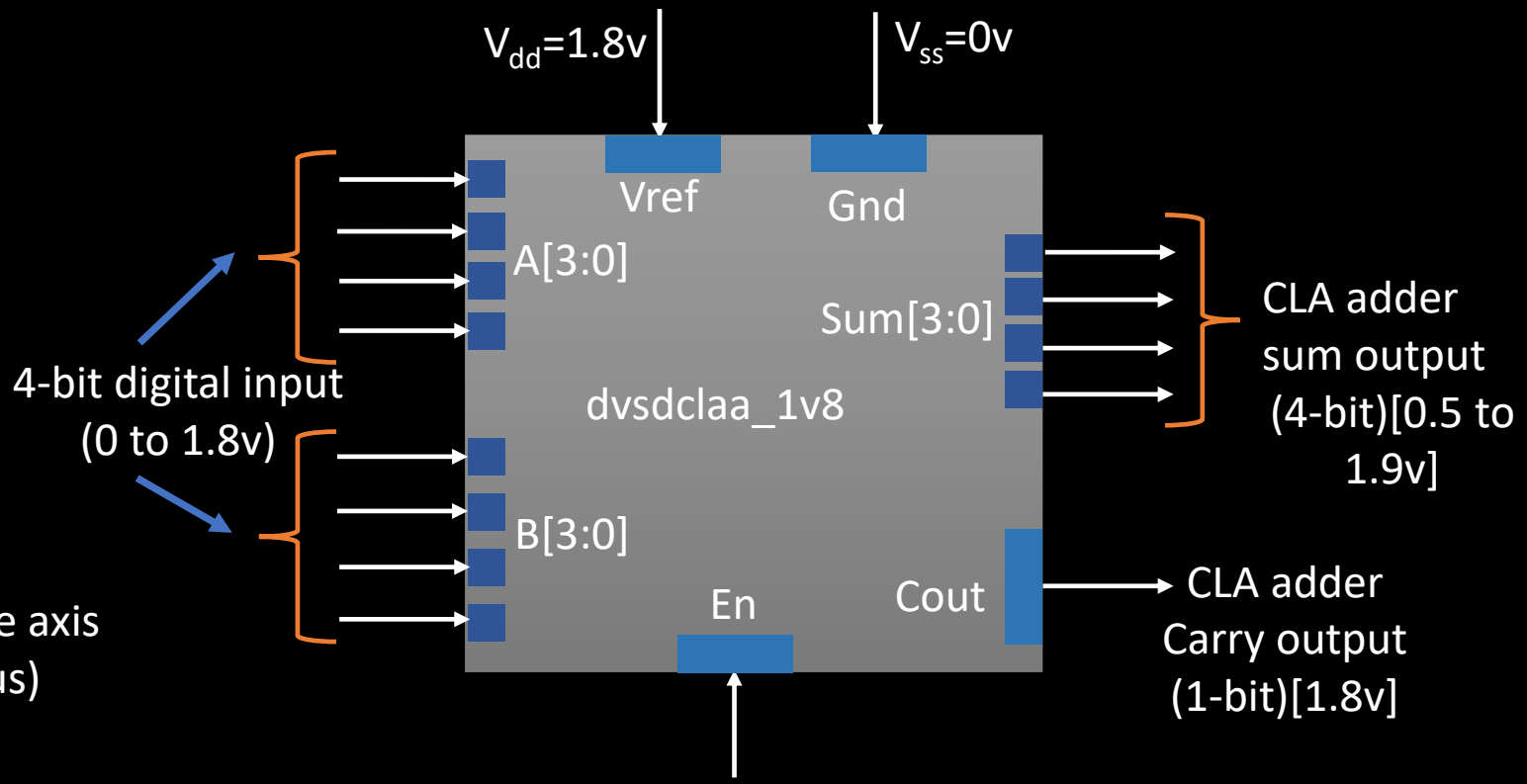
For each block
V_{dd}=1.8v
V_{ss}=0v

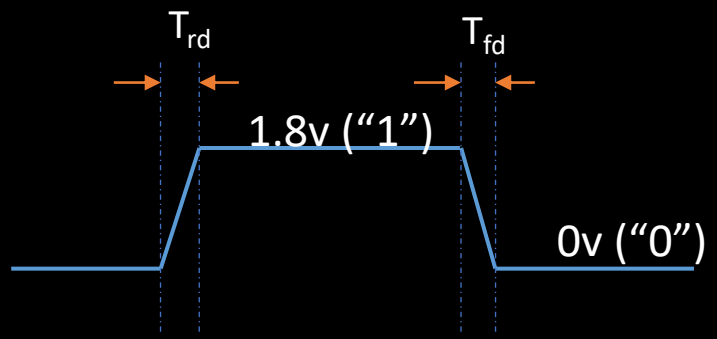
dvsdclaa_1v8 Operating Modes



dvsdclaa_1v8 Operating Modes







Propagation delay occurs due to Rising or Falling edges in input signals

For less distortion T_{rd} or T_{fd} = 1ps to 100ns

Operating

Free air Temperature range : O^o to 70^o C

Storage Temperature range : -65^o to 150^o C