ESE-2005 Lab 6 Single-Cycle Micro-architecture

Exercise

1. Review our slides carefully. Suppose that one of the following control signals in the single-cycle **ARM** processor has a stuck-at-0 fault, meaning that the signal is always 0, regardless of its intended value. What types of instruction would malfunction? Why?

   a) PCSrc

   b) $ALUControl_{1:0}$

   c) MemW

   d) ALUSrc