

Week-2 Experiments

- Installation of Icarus Verilog Compiler and GTKwave
- Getting familiar with compilation and execution procedure
- Getting familiar with verification of design
- Implementation of Half Adder
- Implementation of Full Adder
- Implementation of 4-bit Adder
- Implementation of 8-bit Adder
- Implementation of 16-bit Adder
- Implementation of 32-bit Adder – 10 Marks
- Implementation of 64-bit Adder – 10 Marks