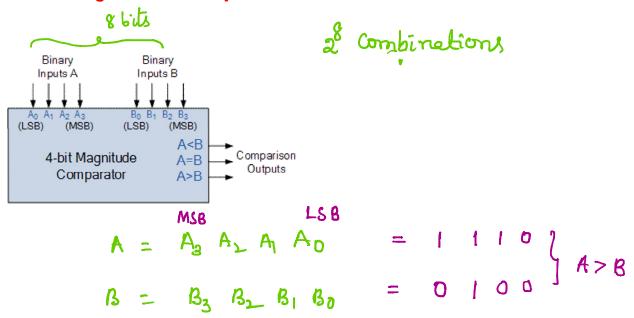
4-Bit Magnitude Comparator



The condition of A=B is possible only when all the individual bits of one number exactly coincide with corresponding bits of another number.

$$A = B \implies AB + \overline{AB} \implies ABB$$

$$\chi_{i} = A_{i}B_{i} + \overline{A}_{i}\overline{B}_{i} \implies A_{i}\overline{O}B_{i}$$
where $i = 0, 1, 2, 3$.

$$\chi_{0} = A_{0}B_{0} + \overline{A}_{0}\overline{B}_{0} \implies A_{0}\overline{O}B_{0}$$

$$\chi_{1} = A_{1}B_{1} + \overline{A}_{1}\overline{B}_{1} \implies A_{1}\overline{O}B_{1}$$

$$\chi_{2} = A_{2}B_{2} + \overline{A}_{2}\overline{B}_{2} \implies A_{2}\overline{O}B_{2}$$

$$\chi_{3} = A_{3}B_{3} + \overline{A}_{3}\overline{B}_{3} \implies A_{3}\overline{O}B_{3}$$

In a 4-bit comparator the condition of A>B can be possible in the following four cases:

III a 4-bit comparator the condition of A>D can be possible in

the following four cases:

1. If A3 = 1 and B3 = 0
$$\Rightarrow$$
 A₃ $\xrightarrow{\text{F}_2}$

2. If A3 = B3 and A2 = 1 and B2 = 0
$$\Rightarrow$$
 (A3 \circ B3) \cdot A2 \circ B3 \Rightarrow X3. A2 \circ B3

3. If A3 = B3, A2 = B2 and A1 = 1 and B1 =
$$0 \implies (A_3 \circ B_3) \cdot (A_1 \circ B_2) \cdot A_7 \cdot \overline{B}_1$$

$$\implies \mathcal{X}_3 \cdot \mathcal{X}_4 \cdot \overline{B}_1$$

4. If
$$A3 = B3$$
, $A2 = B2$, $A1 = B1$ and $A0 = 1$ and $B0 = 0$

$$A > B \Rightarrow A_3 \cdot \overline{B}_3 + \chi_3 \cdot A_2 \overline{B}_2 + \chi_3 \cdot \chi_2 \cdot A_1 \overline{B}_1 + \chi_3 \chi_2 \chi_1 \cdot A_0 \overline{B}_0$$

B) Similarly the condition for A<B can be possible in the following four cases:

1. If A3 = 0 and B3 =
$$\longrightarrow$$
 A_3 B_3

3. If
$$A3 = B3$$
, $A2 = B2$ and $A1 = 0$ and $B1 = 1$

$$(A_3 \circ B_3) (A_1 \circ B_1) \overline{A_1} B_1$$

 $(A_3) (A_2) \overline{A_1} B_1$

4. If A3 = B3, A2 = B2, A1 = B1 and A0 = 0 and B0 = 1

$$A < B \Rightarrow \overline{A}_3 B_3 + \chi_3 \cdot \overline{A}_2 B_2 + \chi_3 \cdot \chi_2 \cdot \overline{A}_1 B_1 + \chi_3 \chi_2 \chi_4 \cdot \overline{A}_0 B_0$$

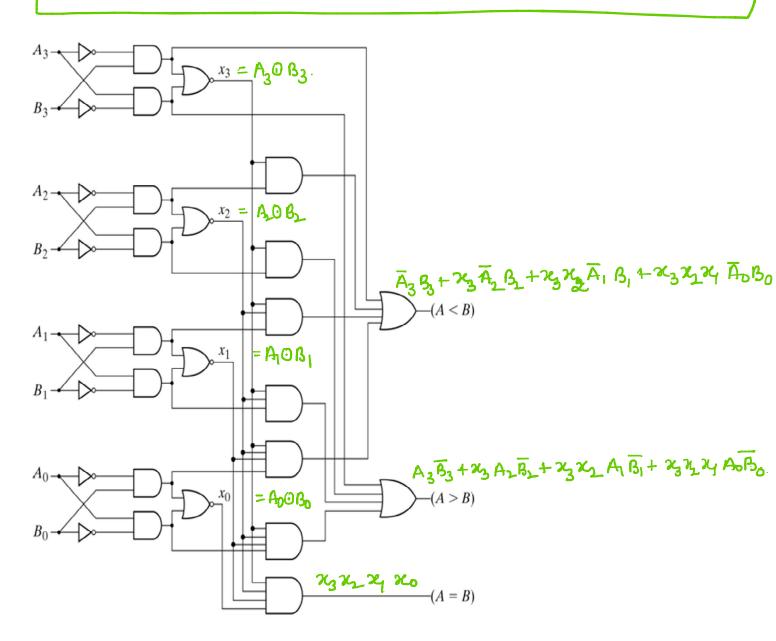


Fig. 4-17 4-Bit Magnitude Comparator