BCD adder (or) Decimal adder (or to 9)

Tuesday, October 13, 2020 8:40 AM

case : Sum equal to 9 (81 less with carry = 0 6 -> 0110 -> BCD for 6 +3 -> 0011 -> BCD for 3 +9 1001 -> BCD for 9 -> NO Collection normal addition

case 2 :- Sum greater than 9 with carry = 0 7 -> 0111 -> BCD FR 7 $\frac{+8}{15}$ $\xrightarrow{1000}$ BCD for 8 $\xrightarrow{15}$ $\xrightarrow{1111}$ $\xrightarrow{}$ Invalid BCD

(15) = 0001 0191 = BCD F& 15

Invalid BCD = 1111

to collect it add (+6) = 0110 000 1010 1 -> Callect BCD

case 3: Sum is less than or equal to 9 with carry = 1

8 -> 1000 +9 -> 1001 17 ,1)0001 -> Invalid BCD

Summary of BCD addition

- 1. Add two BCD numbers using bedinnery binary addition
- 2. If four bit seem is equal to or less than 9 no correction is needed. Sum is in BCD form
- 3. If four bit hum gleater than 9 (B)
 If carry generated from the four bit hum.
 Then sum is invalid BCD.
 To correct it add +6 (0110) to the
 Invalid BCD number.

To implement BCD addes

1. 4 bit pæallel adder for initial addition 2. Logic clut to detect sum greater 9
3. one onde 4 bit pæallel adder

BCD numbers (0 to 9)

(10) = (0001 0000)

(15)

(15)

(15)

(16)

(10)

(10)

(10)

(10)

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(10)

Cos	J Binary Sum					BCD Sum				Decimal					
	K	Z ₈	Z ₄	Z ₂	Z ₁		С	S ₈	S ₄	S ₂	S ₁				
	0	0	0	0	0	S	0	0	0	0	0		0		
	0	0	0	0	1	Α	0	0	0	0	1		1		
	0	0	0	1	0	М	0	0	0	1	0		2		
						E									
						_									
						C								1 1 H C	
						O D								\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \	
	0	1	0	0	0	E	0	1	0	0	0		8	10 00 01 11 10	
	0	0 1 0 0 1 E 0 1 0 0 1 9 10 6 19 Binary and BCD codes are not the same												16 00 01 11 10	
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	0	1	0	1	1)	1	0	0	0	1		11 /		
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	0	1	1	1	0	<u>ا</u> با	PHIC	0	1	0	0		14		H
	0	1	1	1	1	11 ~	111	0	1	0	1		15	1 11 10 10 10 11 1 1 1 1	M
	0 1	0	0	0	0	110	ZI.	0	1	1	0		16	8 / 9 11. 10.	}
1	1	0	0	0	1		1	0	1	1	1	1	17	10 [/ []] [[] []	/
Corry	1	0	0	1	0 \	V	1	1	0	0	0	1	18		
_	1	0	0	1	1		1	1	0	0	1		19		
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			Su	m <	(9										
					Z Z + Z Z	ク									
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		\mathcal{C}	_	k.	+	20	2 2	4.	+	~ K	, ,	1			
					,	8	Z	4			•				

- 1. It is obvious from the table that a correction is needed when the 'Binary Sum' has an output carry K=1.
- 2. The other six combinations from 10 to 15 need correction in which the bit on the Z8 position is 1.

- 3. In the Binary sum of 8 and 9, the bit on the Z_8 position is also 1. So, the second step fails, and we need to modify it.
- 4. To distinguish these two numbers, we specify that the bit on the Z_4 or Z_2 position also needs to be 1 with the bit of Z_8
- 5. The condition for a correction and an output carry can be expressed by the Boolean function:

