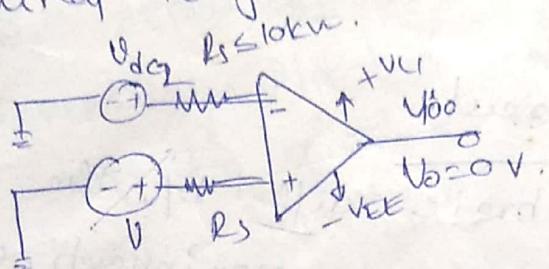


UNIT-丘

Specifications of Ic741.

1. Input offset Voltage:

Input offset Voltage:
The input offset voltage V_{IO} is the voltage that must be applied between the IIP terminal to get the zero output voltage.



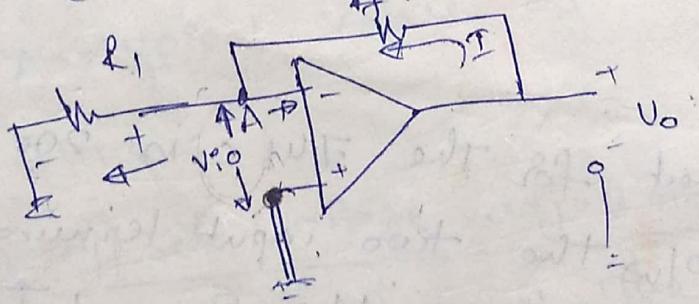
We denote IIP
Offset Voltage as
 V_{IO} it may be
positive or negative.

$$V_{o,0} = V_{dc1} - V_{dc2} \quad \text{For 741C the max value}$$

of V_{IO} is 6mV. This value will be matched if the input terminals are matched.

if the input terminals are
for 714C precision op-amp $V_{IO} = 150 \mu V$.

Q. Measurement of input off Voltage.



$$I = \frac{V_o - V_A}{R_f} = \frac{V_o - V_{io}}{R_f}$$

$$V_A = IR_1 = V_{i,0}$$

$$I = \frac{V_o}{R_1}$$

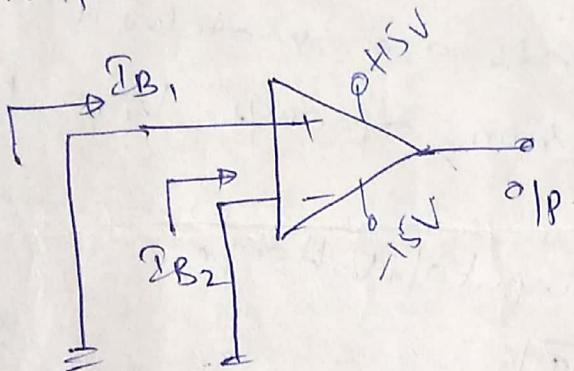
$$\frac{V_{io}}{R_f} = \frac{V_o - V_{io}}{R_f}$$

$$V_0 = N_{10} \left(1 + \frac{R_f}{R_i} \right)$$

Output resistance: Output resistance R_o is the equivalent resistance that can be measured b/w the op terminal of the op-amp and the ground (or common point). It is 75Ω for 741C op-amp.

② I_{Op} offset current

The algebraic diff b/w the currents into the inverting and noninverting terminal is referred to as I_{Op} offset current I_{io} .



$$I_{io} = (IB_1 - IB_2)$$

I_{Op} offset current for the 741C is 200 nA max. As matching b/w the two input terminal is improved, the difference b/w I_{B_1} and I_{B_2} becomes smaller i.e. I_{io} value decreases. 741C precision op-amp may $I_{io} = 6 \text{ nA}$.

③ Input bias current: Input bias current

I_B is the average of the two current that flow into the inverting and noninverting op terminal of the op-amp.

$$I_B = I_{B_1} + I_{B_2} / 2$$

$I_B = 500 \text{ nA}$ for 741C

$I_B = \pm 4 \text{ mA}$ for 7414C

- ④ Differential I/p resistance: R_E in the equivalent resistor can be measured at the either of the inverting or noninverting I/p terminal with other terminal connected to ground.

for 741C - $2 \text{ M}\Omega$

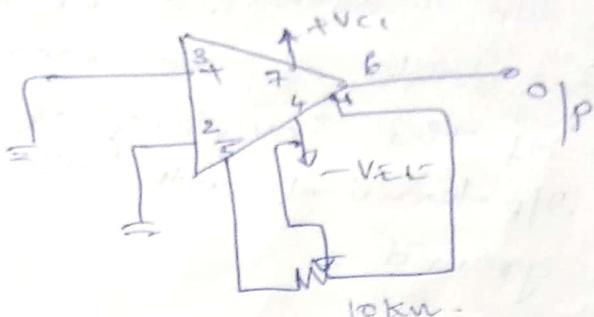
for MAF 771 PGT I/p op-amp - $1000 \text{ G}\Omega$

- ⑤ Input capacitance: C_i is measured at either of the input terminal of the op-amp. A typical value of C_i is 1.4 pF for 741C.

- ⑥ offset voltage adjustment range: one of the features of the 741 op-amp is an offset voltage null capability. The 741 op-amps have pins 1 and 5 marked as offset null for this purpose. A 10k Ω potentiometer can be connected b/w offset pins 1 and 5 and the wiper of the potentiometer can be connected to the $-V_{EE}$. By varying the potentiometer, the o/p

offset voltage can be reduced to zero.

No Hdg



The offset voltage adjustment range is the range through which the op offset voltage can be adjusted by varying the 10kΩ potentiometer.

For 741C offset adjustment range is

$\pm 15\text{mV}$.

(7) CMMR. The ratio of differential voltage gain to the common-mode noise

gain

$$\text{CMRR} = \frac{A_d}{A_{cm}}$$

$A_d \rightarrow$ same as large signal voltage gain

$$A_{cm} = \frac{V_{ocm}}{V_{cm}}$$

for 741C CMRR is 90 dB.

714C CMRR = 120 dB.

V_{cm} = output common-mode voltage

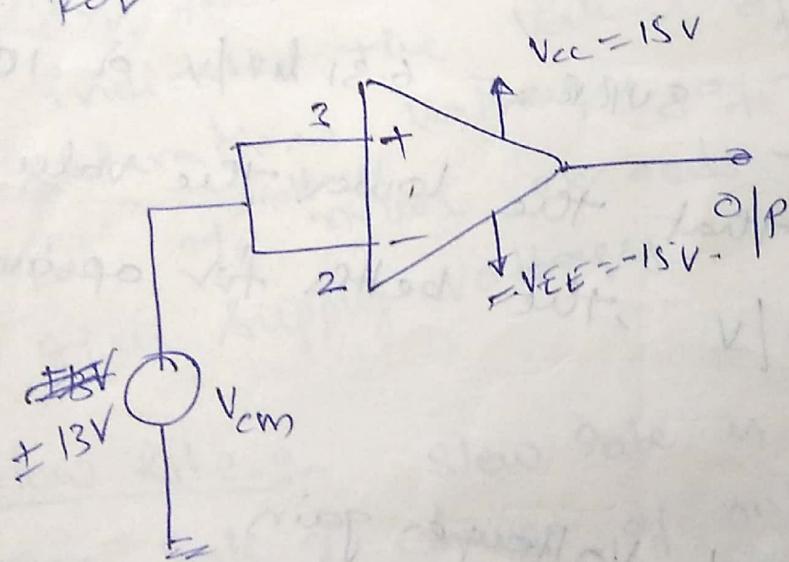
$V_{cm} = \frac{O/P}{A}$ common-mode voltage.

A_{cm} = Common mode voltage gain

Generally A_{cm} is very small and $A_d = A$ is very large. So CMRR is very large. Being a large value CMRR is most often expressed in decibels (dB). For the 741C, CMRR is 90dB typically. The higher the value of CMRR, the better is the matching between two O/P terminals. And smaller is the output common-mode voltage.

$$CMRR = 120 \text{ dB.}$$

For 714C precision opamp



⑧ SVRR (PSRR) :

The change in an op-amps I_{IP} offset voltage V_{IO} caused by variations in supply voltages is called the supply voltage rejection ratio or power supply sensitivity (PSS). These terms are expressed either in mV per V or in dB.

If we denote the change in supply voltages by ΔV and corresponding

change in input offset voltage by ΔV_{IO} , SVRR can be defined as

$$\text{SVRR} = \frac{\Delta V_{IO}}{\Delta V}$$

for 741C $\text{SVRR} = 150 \text{ mV/V}$.

for 714C $\text{SVRR} = 6.31 \text{ mV/V} \approx 104 \text{ dB}$

This means that the lower the value of SVRR in mV/V, the better for op-amp performance.

⑨

Large-Signal Voltage gain

The op-amp amplifies the differential voltage between two I/O terminals. The voltage gain of the amplifier is defined as

$$\text{Voltage gain} = \frac{\text{output voltage}}{\text{differential input voltage}}$$

Because output signal amp is much larger than the op signal the voltage gain is commonly called large-signal voltage gain. Under test conditions $R_L \geq 2k\Omega$ and $V_o = \pm 10V$ (or $20V$ P-P) the large signal voltage gain of 741C is 200000 or 2×10^5 .

(10) The output voltage swing :

The output voltage swing V_{max} of the 741C is guaranteed to be between -13 and $+13V$ for $R_L \geq 2k\Omega$ that is giving a $26V$ (P-P) undistorted sine wave for ac op signals. In fact the op voltage swing indicates the values of positive and -ve saturation voltages of the op-amp. The op never exceeds these limits of given supply voltages.

(11) Slew rate :- Slew rate is defined as the max. rate of change of output voltage per unit of time and is expressed in Volts per microsecond.

$$SR = \frac{dV_o}{dt} |_{\text{max}} \quad \text{V/microsec}$$

Slew Rate indicates how rapidly the output of an op-amp can change in response to changes in the input frequency. The slew rate changes with voltage gain and is normally specified at unity (+1) gain.

The slew rate of an op-amp is fixed if the slope requirements of the output signal are greater than the slew rate, then distortion occurs.

* Thus slew rate is one of the important factors in selecting the op-amp for ac applications, particularly at relatively high freq.

* One of the draw backs of the 741 is its low slew rate ($0.5V/\mu s$), which limits its use in high freq applications, especially in oscillators, comparators and filters.

The newer op-amps - LF 351, LF 771 and MC 34001 which are the direct replacements for 741 have a

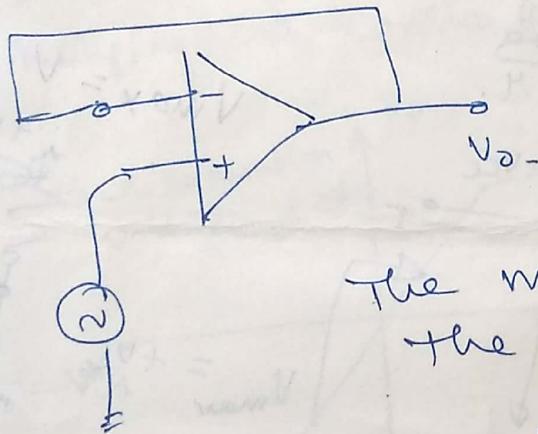
slew rate of $13V/\mu s$. In high speed op-amps especially, the slew rate is significantly improved. The LM 318 has slew rate of $70V/\mu s$.

Slew Rate equation: Generally the slew rate is specified for unity gain and hence let us consider the ~~for~~ voltage follower ~~for~~ op-amp. Let us assume that the op-amp is a large amp and high freq sine wave.

The equation for o/p signal is the

$$V_o = V_m \sin \omega t$$

With no slew rate limitation $V_o = V_m \sin \omega t$



$$\frac{dV_o}{dt} = \omega V_m \cos \omega t$$

The maximum rate of change of the o/p occurs
 $\cos \omega t = 1$ i.e.

$$\left. \frac{dV_o}{dt} \right|_{\text{max}} = \omega V_m$$

$$= 2\pi f V_m \text{ V/sec.}$$

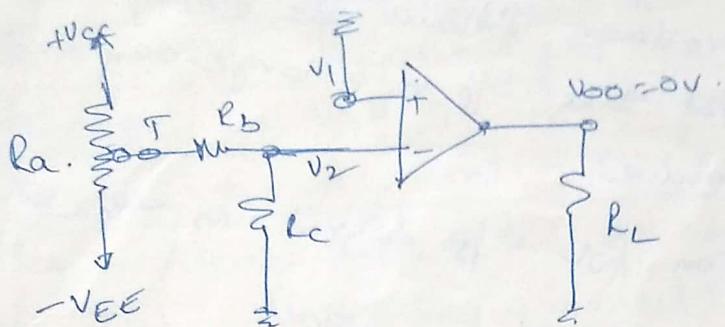
$$S = \frac{2\pi f V_m}{10^6} \text{ v/msec.}$$

O/p voltage voltage swing: The op-amp's voltage swing is finite and is decided by the supply voltages. It never exceeds the limits $+V_{cc}$ and $-V_{ee}$. Above these values op-amp

op-amp gets saturated.

~~op-amp~~ op-amp gets saturated at $+V_{cc}$ and $-V_{ee}$ and cannot produce o/p more than $+V_{cc}$ and $-V_{ee}$. Practically saturation voltage V_{sat} and $-V_{sat}$ are slightly less than $+V_{cc}$ and

offset - voltage compensating network:



if V_{oo} is +ve

$$V_1 > V_2$$

towards +ve.

V_{oo} is -ve

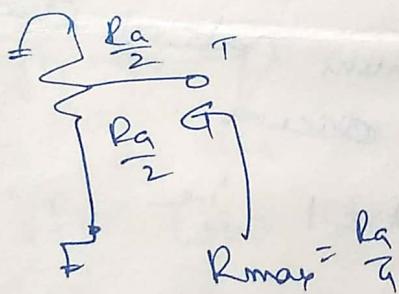
$$V_1 < V_2$$

towards -ve

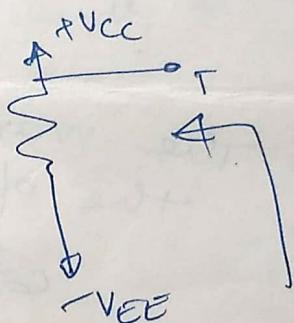
Max. thresholding is V_2 when

$$R_{max} = \frac{R_a}{2} \parallel \frac{R_a}{2} = \frac{R_a}{4}$$

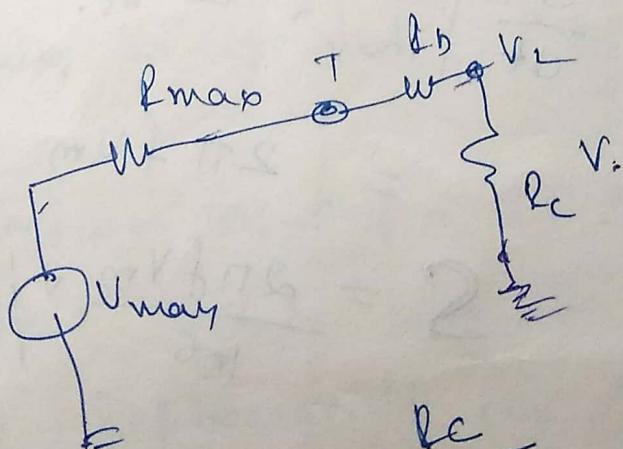
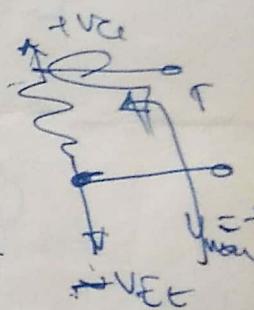
$$V_{max} = V_1$$



$$R_{max} = \frac{R_a}{4}$$



$$V_{max} = +V_{cc}$$



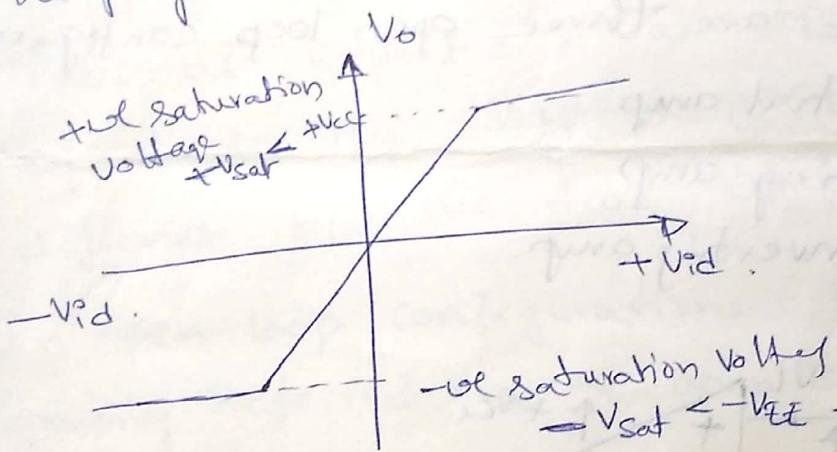
$$V_1 = \frac{R_c}{R_{max} + R_b + R_c} V_{max}$$

$$V_{oo} = \frac{R_b}{R_{max} + R_b + R_c} V_{max}$$

$$R_b > R_{max} > R_c$$

Ideal Voltage Transfer curve

$V_o = A(V_{id}) = A(V_1 - V_2)$ is the basic op-amp equation, in which the output offset voltage is assumed to be zero. This equation is useful in studying the op-amp's characteristics and in analyzing different ckt configurations that employ feedback. The graphic representation of this equation is shown in fig. where the output voltage V_o is plotted against input difference voltage V_{id} . V_o is plotted against V_{id} keeping A constant.



The output voltage cannot exceed the positive and negative saturation voltages. These saturation voltages are specified by an op-amp for given supply voltages.

Values of output voltage are directly proportional to the input difference voltage only until it reaches the saturation voltages and thereafter output voltage remains constant.

Open-loop op-amp configurations

In the case of amps often term open loop indicates that no connection, either direct or via another network, exists between the output and input terminals.

If the output signal is fed back in any form as part of the input signal and the loop that would have been formed with feedback is open,

When connected in open-loop configuration, the op-amp simply functions as a high-gain amp. There are three open-loop configurations

1. Differential amp.
2. Inverting amp
3. Non-inverting amp.

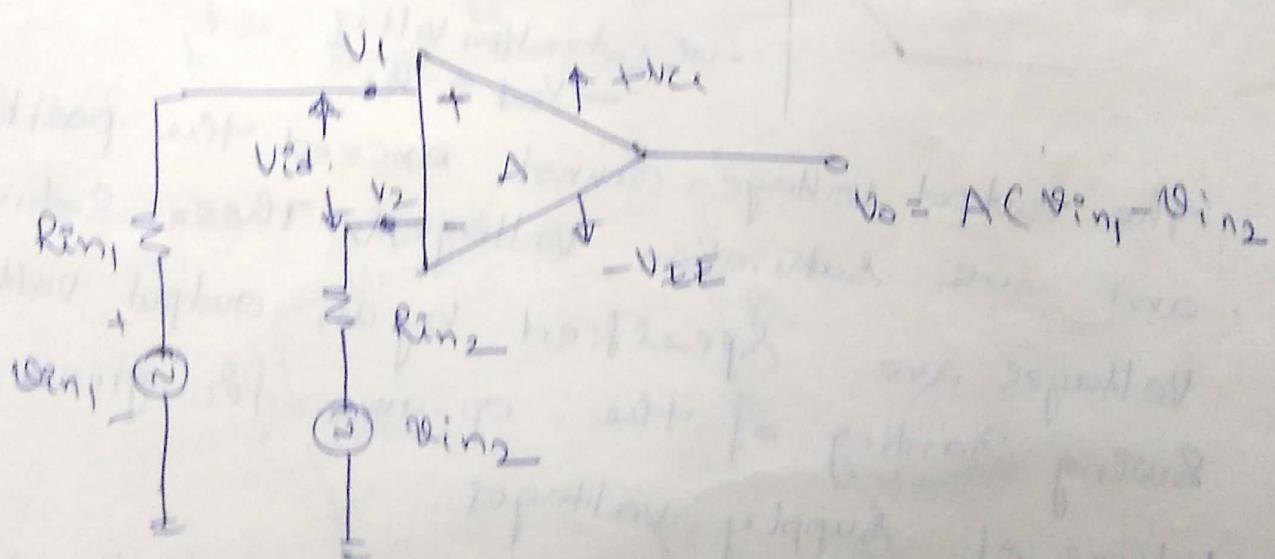


Fig shows the open-loop differential amp in which input signals V_{in_1} and V_{in_2} are applied to the +ve and -ve input terminals.

Since the op-amp amplifies the difference between the two input signals, this configuration is called the differential amp.

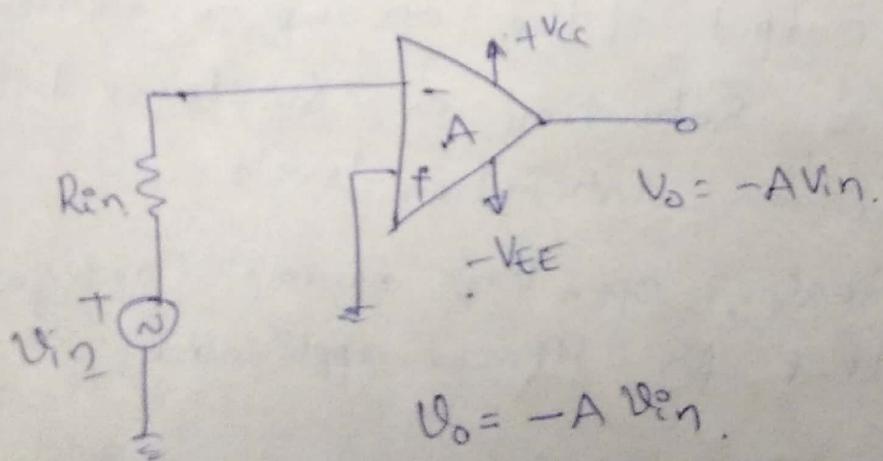
The op-amp can amplify both ac and dc voltages. Source resistances R_{in1} and R_{in2} are normally negligible compared to the input resistance R_i . Therefore the voltage drop across these resistors can be assumed to be zero, which then implies that $V_1 = V_{in1}$ and $V_2 = V_{in2}$.

$$\text{So } V_o = A (V_{in1} - V_{in2})$$

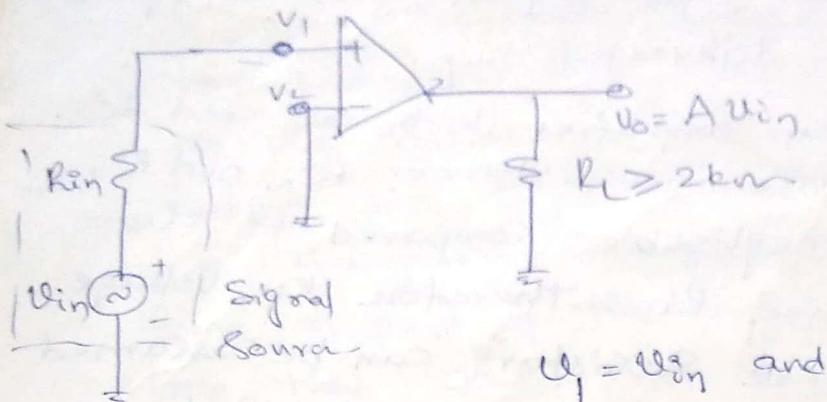
Thus as expected, the output voltage is equal to the voltage gain A times the difference b/w the two input voltages.

In open loop configurations, gain A is commonly referred to as open-loop gain.

Inverting amp.



The non-inverting amp.



$$U_o = A U_{in}$$

This means that the output voltage is larger than the I/p voltage by gain A and is in phase with the I/p signal.

- + In all three open-loop configurations any I/p signal (differential or single) i.e. is only slightly greater than zero drives the output to saturation level.

This results from the very high gain (A) of the op-amp. Thus when operated open loop, the output of the op-amp is either -ve or positive saturation or switches between positive and -ve saturation levels.

For this reason, open loop op-amp configurations are not used for linear applications.

Op-Amp with -ve feedback

- * Clipping occurs in open-loop configurations when the output attempts to exceed the saturation levels of the op-amp.

Because the open-loop gain of the op-amp is very high, only the smaller signals (of the order of 10^4 or less) having very low freq may be amplified accurately without distortion. However, Signals this small are very susceptible to noise and almost impossible to obtain in the laboratory.

- * Besides being large, the open-loop voltage gain of the op-amp is not constant. The voltage gain varies with changes in temp and power supply as well as manufacturing techniques.

- * The variations in voltage gain are relatively large in open-loop op-amps in particular, which makes the open-loop op-amp unsuitable for any linear applications. In most linear applications the op is proportional to the I/P and is of the same type.

- * The B.W (band of freqs for which the gain remains constant) of most open-loop op-amp is impractical in a.c applications.

open-loop bandwidth of the FET is approximately 5Hz.

- * In certain applications the open-loop op-amp is purposely used as a nonlinear device.

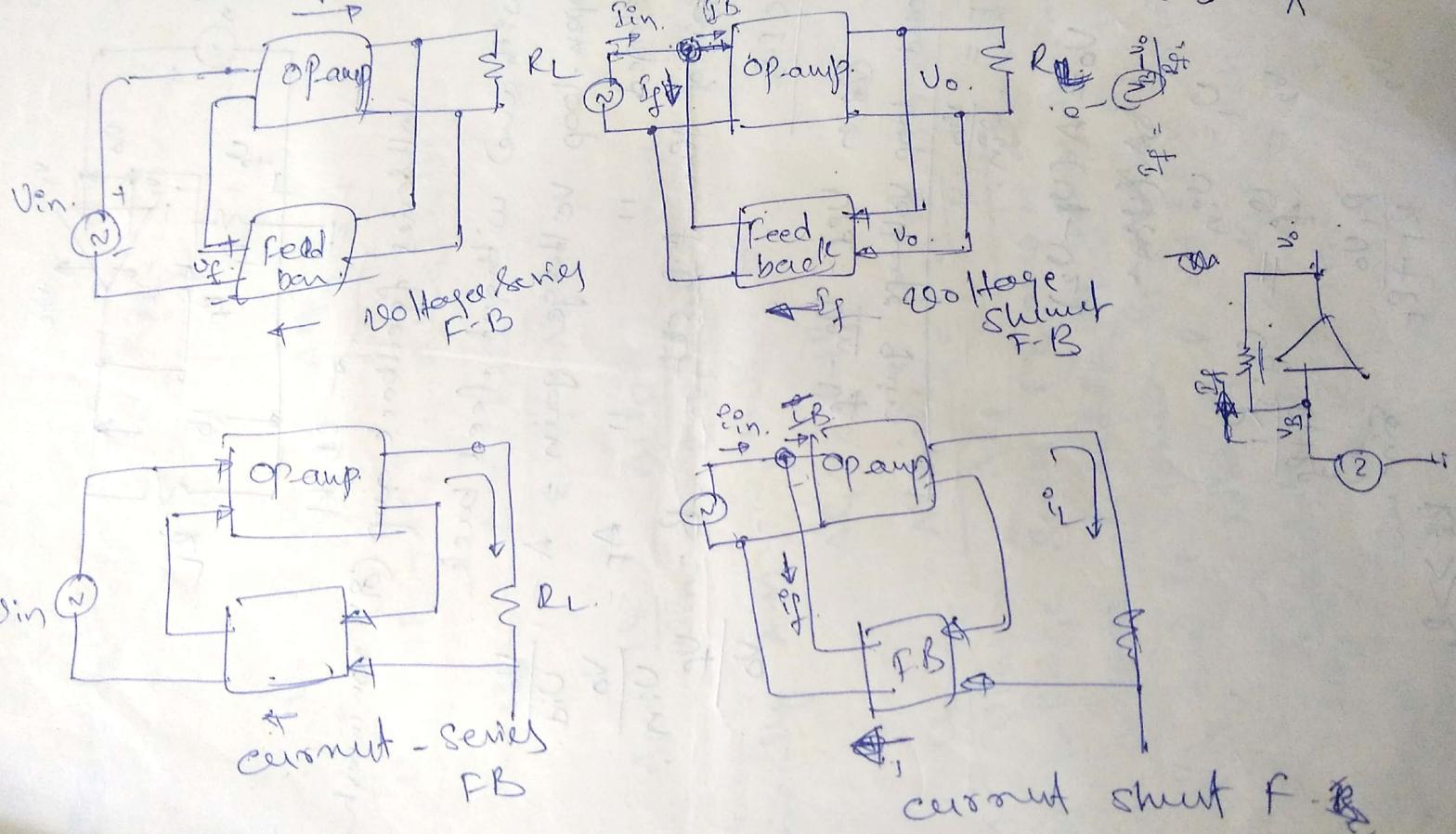
An Op-Amp with -ve Feedback

We can select as well as control the gain of open-loop opamp if we introduce a modification in the basic circuit. This modification involves the use of feed back, i.e. o/p signal is fed back to the I/p either directly or via another n/w.

If the signal fed back is of opposite polarity or out of phase by 180° (or odd multiples of 180°) with respect to I/p signal, this feed back is called -ve feed back.

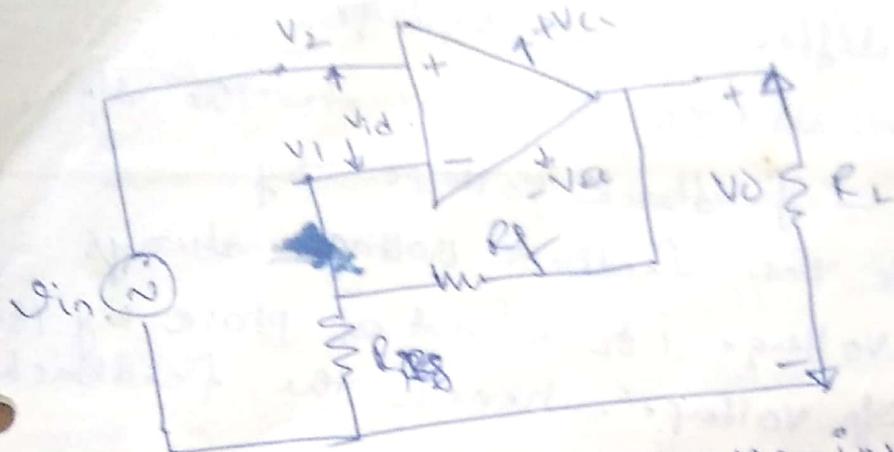
An amp with -ve feed back has a self correcting ability against any change in o/p voltage caused by changes in environmental conditions. -ve feedback is also known as degenerative feed back because when used it reduces the I/p voltage amp and in turn reduces the voltage gain. When used in amplifiers, -ve feed back stabilizes the gain, increases the B.W and changes the I/p and o/p resistances.

Block diagrams for different op-amp config.



Voltage Series feedback amp

The schematic diagram of the Voltage Series feedback amp is shown in fig.



The ckt is known as noninverting amp with feedback. (or closed-loop noninverting amplifier) because it uses feedback, and the input signal is applied to the noninverting terminal of the op-amp.

open-loop voltage gain (or gain without feedback)

$$A = \frac{V_o}{V_{id}}$$

closed loop voltage gain (or gain with feedback)

$$A_F = \frac{V_o}{V_{in}}$$

gain of the feedback ckt = $B = \frac{V_f}{V_o}$

-ve feedback

$$V_{id} = V_{in} - V_f$$

V_{in} = I_p voltage V_f = feedback voltage.

V_{id} = difference I_p voltage.

This difference voltage is equal to the I_p.

voltage V_{in} - feedback voltage V_f .

- * In other words the feedback voltage always opposes the I_p voltage. (or is out of phase by 180° w.r.t. to the I_p voltage) hence the feedback is said to be -ve.

Closed loop voltage gain

$$\text{The closed loop voltage gain } A_F = \frac{V_o}{V_{in}}$$

$$V_o = A(V_{in} - V_f)$$

$$V_1 = V_{in}$$

$$V_2 = V_f = \left(\frac{R_f}{R_1 + R_f} \right) V_o$$

$$V_o = A \left(V_{in} - \frac{R_f}{R_1 + R_f} V_o \right)$$

$$V_o \left[1 + \frac{R_f A}{R_1 + R_f} \right] = A V_{in}$$

$$V_o = \frac{A \cdot V_{in} (R_1 + R_f)}{R_1 + R_f + A R_1}$$

$$A_F = \frac{V_o}{V_{in}} = \frac{A(R_1 + R_f)}{R_1 + R_f + A R_1} \text{ exact.}$$

$$AB = A R_1 \Rightarrow R_{\text{f}} = R_1$$

$$A_F = \frac{A(R_1+R_F)}{A R_1} = 1 + \frac{R_F}{R_1} \quad (\text{Ideal})$$

$$A_F = 1 + \frac{R_F}{R_1}$$

①

Gain of the Voltage Series-feedback amp is determined by the ratio of two resistors, R_1 and R_F .
If the desired gain is 11, we can choose $R_1 = 100\text{k}\Omega$ and $R_F = 10\text{k}\Omega$ or $R_1 = 100\text{m}\Omega$ and $R_F = 1\text{k}\Omega$.
In setting the gain—the ratio of R_1 and R_F is important not the absolute values of these resistors.

$$B = \frac{V_F}{V_O} = \frac{R_1}{R_1+R_F}$$

$$A_F = \frac{1}{B}$$

This means that the gain of the feedback ckt is the reciprocal of the closed loop voltage gain for given values of R_1 and R_F —the values of A_F and B are fixed.

The closed loop voltage gain A_F in terms of open loop gain A and feedback ckt gain B being fixed.

$$A_F = \frac{A(R_1+R_F)}{R_1+R_F+A R_1}$$

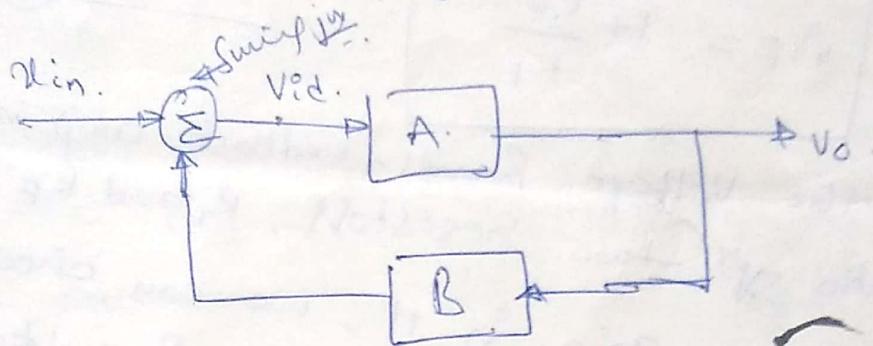
$$A_F = \frac{A(R_1+R_F)/R_1 R_F}{1 + A R_1 R_F} = \frac{A}{1+AB}$$

when $A_F = \text{closed loop gain}$

$A = \text{open loop gain}$

$B = \text{gain of the feedback ch.}$

$AB = \text{loop gain.}$



Difference Input Voltage ideally zero.

$$V_{id} = \frac{V_o}{A}$$

$$V_{id} \approx 0$$

$$V_1 - V_2 \approx 0$$

$$V_1 \approx V_2$$

Say that the voltage at the noninverting terminal of an op-amp is approximately equal to that at the inverting input terminal provided that A is very large.

* This concept is useful in analysis of closed-loop opamp circuits.

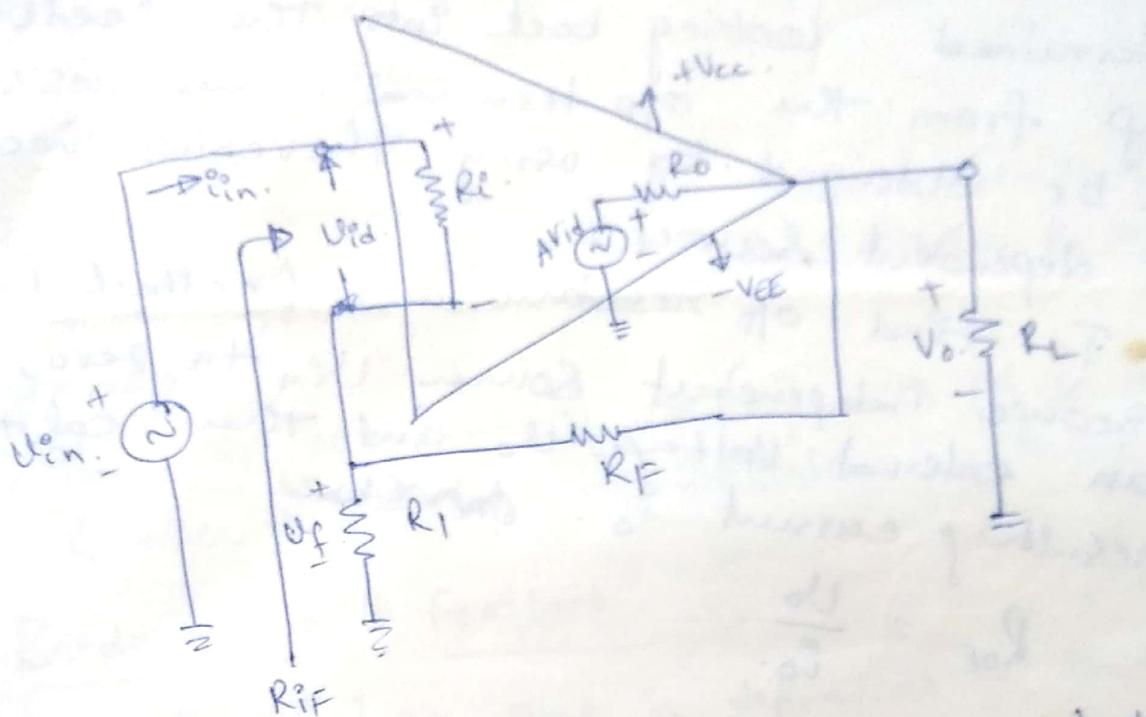
$$V_1 = V_{in}$$

$$V_2 = V_f = \frac{R_1 V_o}{R_1 + R_F}$$

$$V_{in} = \frac{R_1 V_o}{R_1 + R_F}$$

$$A_F = \frac{V_o}{V_{in}} = 1 + \frac{R_F}{R_1}$$

I_p resistance with feed back



R_i is the I_p resistance (open loop) of the op-amp, and R_{IF} is the I_p resistance of the amp with feedback. The I_p resistance with feed back is defined as

$$R_{IF} = \frac{V_{in}}{I_{in.}} = \frac{V_{in.}}{V_{id}/R_i}$$

$$V_{id} = \frac{V_o}{A} \text{ and } V_o = \frac{A}{1+AB} V_{in.}$$

$$R_{IF} = \frac{V_{in} R_i \cdot A}{V_o} = \frac{V_{in} R_i \cdot A}{\frac{A V_{in.}}{1+AB}}$$

$$R_{IF} = R_i(1+AB)$$

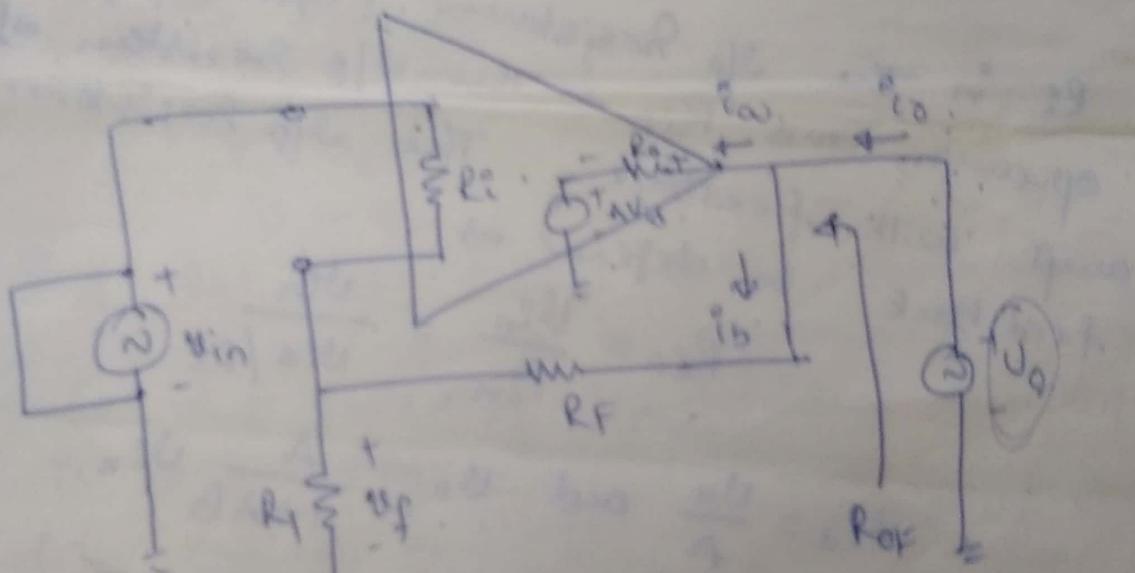
This means that the I_p resistance of the op-amp with feedback is $(1+AB)$ times that without feedback.

O/p Resistance with feedback

O/p resistance in the feedback determined looking back into the feedback amp from the o/p terminal. This resistance can be obtained by using Thevenin's theorem for dependent source.

- To find o/p resistance with feedback for, reduce independent source V_{in} to zero, apply an external voltage U_o and then calculate resulting current i_o .

$$R_{op} = \frac{U_o}{i_o}$$



$$(R_i || R_f) + R_F \gg R_o$$

$$i_o = i_a + i_B$$

$$(R_i || R_f) \gg R_o \text{ and } i_a \gg i_B$$

$$(R_F + R_i) || R_L \quad i_o = i_a$$

$$-U_o + R_o i_o + A V_{id} = 0$$

$$i_o = \frac{U_o - A V_{id}}{R_o}$$

$$\begin{aligned} V_{id} &= -U_f \\ &= -\frac{R_o U_o}{R_i + R_o} \\ &= -B U_o \end{aligned}$$

$$E_o = \frac{U_o + ABU_o}{R_o} = \frac{U_o(1+AB)}{R_o}$$

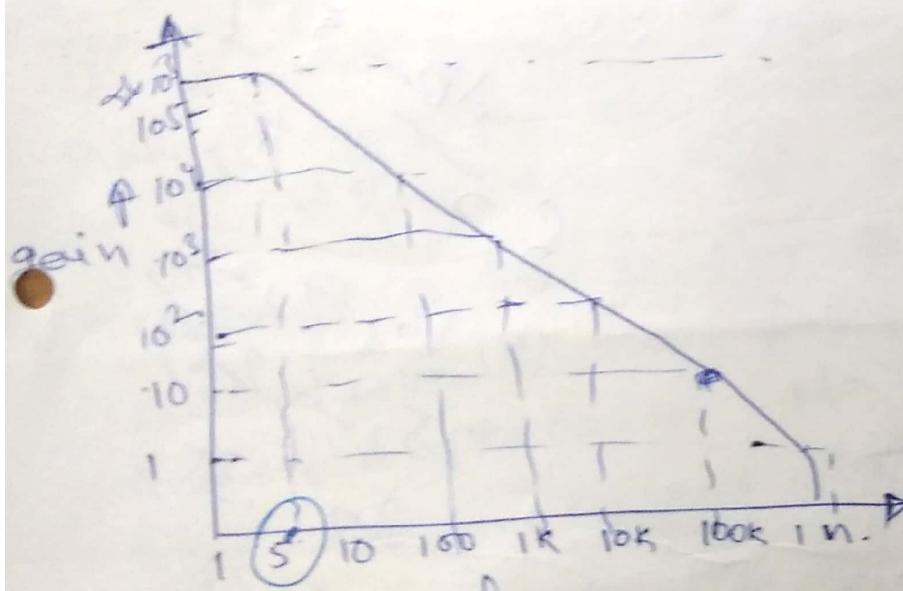
$$\frac{U_o}{E_o} = \frac{R_o}{1+AB}$$

The op-amp resistance of the Voltage-series feedback amp is $\frac{1}{1+AB}$ times the op-amp resistance R_o of the op-amp.

- * The op-amp resistance of op-amp with FB is much smaller than the op-amp resistance without feedback.

Bandwidth with feedback.

The B.W. of an amp is defined as the band of freq's for which the gain remains constn.



open loop gain Vs

freq. curve
of the 741C

DGB p

Break freq.

when gain is 1, B.W. 1MHz.

gain band width product is constant.

741, 5Hz is the break freq.

$$VGB = A f_0$$

$A = \text{open}$

$f_0 = \text{break freq.}$
• Single break.

$$VGB = (AP) f_F$$

$$A f_0 = A F f_F$$

$$f_F = \frac{A(f_0)}{\textcircled{AF}}$$

$$\textcircled{f_F} = f_0(1 + AB)$$

Total o/p offset voltage with feedback:
 with feed back the gain of the noninverting amp changes from A to $A/(1+AB)$ the total o/p offset voltage with feedback must also be $1/(1+AB)$ times the voltage without feedback.

$$\text{Total output offset voltage with feedback} = \frac{\text{total o/p offset voltage without feedback}}{1+AB}$$

$$V_{\text{oof}} = \frac{\pm V_{\text{sat}}}{1+AB}$$

Voltage Follower

The lowest gain that can be obtained from a noninverting amp is ^{one} when noninverting amp is configured for unity gain, it is called a voltage follower. because the o/p voltage equal to and in phase with the i/p. In the voltage follower the o/p follows the i/p.

* Although it is similar to the discrete emitter follower, the voltage follower is preferred because it has much high input resistance, and the o/p amp is exactly equal to the i/p.

* To obtain the voltage follower from the noninverting amp simply open R_f and short

R_f .

In this Rig all the o/p voltage is fed back into the inverting terminal of the op-amp

consequently the gain of the feedback ckt is

$$1. \quad (B = A_F = 1)$$

$$A_F = 1$$

$$1 + AB < A$$

$$R_{EF} = (1 + A) R_i$$

$$R_{OF} = A R_o$$

$$R_{OF} = \frac{R_o}{A}$$

$$f_F = A f_o$$

$$V_{OOT} = \frac{\pm V_{sat}}{A}$$

- * The voltage follower is also called noninverting buffer because, when placed b/w two M/W, it removes the loading on the first M/W.

The 741C op-amp having the following parameters is connected as a noninverting amp with $R_i = 1\text{k}\Omega$.

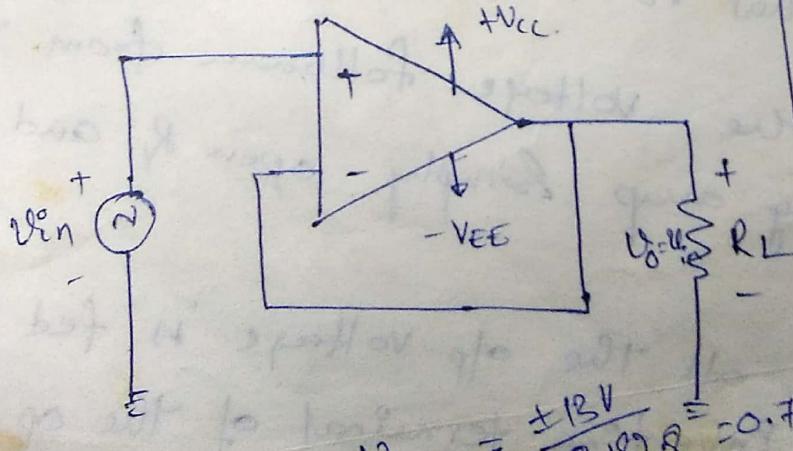
$$R_F = 10\text{k}\Omega$$

$$A = 200,000, \quad R_i = 2\text{M}\Omega, \quad R_o = 75\Omega$$

$$f_o = 5\text{Hz}, \quad \text{Supply voltages } \pm 15\text{V}$$

$$\text{Op voltage swing} = \pm 13\text{V}$$

Compute the values of A_F , R_{EF} , R_{OF} , f_F and V_{OOT} .



$$V_{OOT} = \frac{\pm 13\text{V}}{(18,182)^2} = 0.715\text{mV}$$

$$B = \frac{R_1}{R_1 + R_F} = \frac{1}{11}$$

$$1 + AB = 1 + \frac{200,000}{11} = 18,182.8$$

$$A_F = \frac{200,000}{18,182.8} = 10.99$$

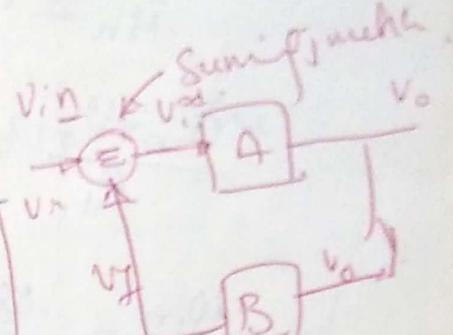
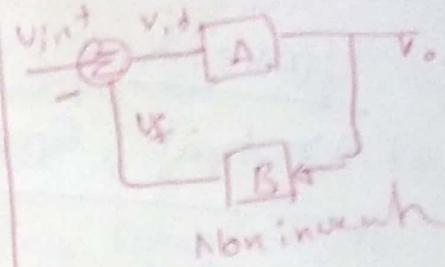
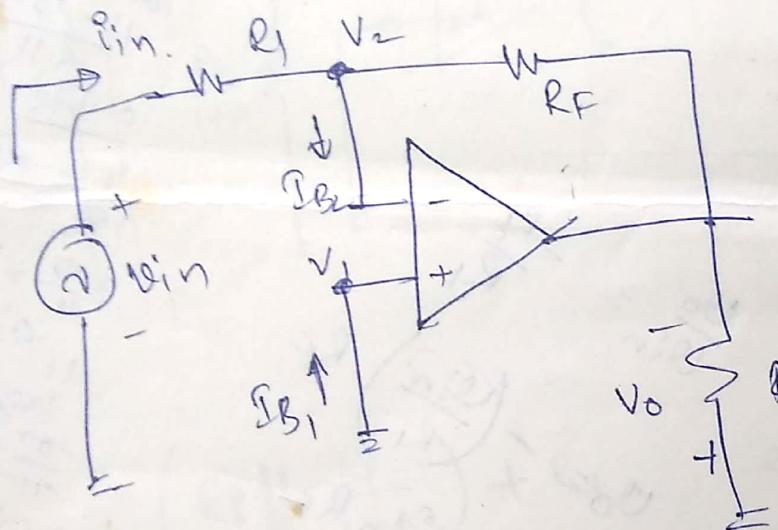
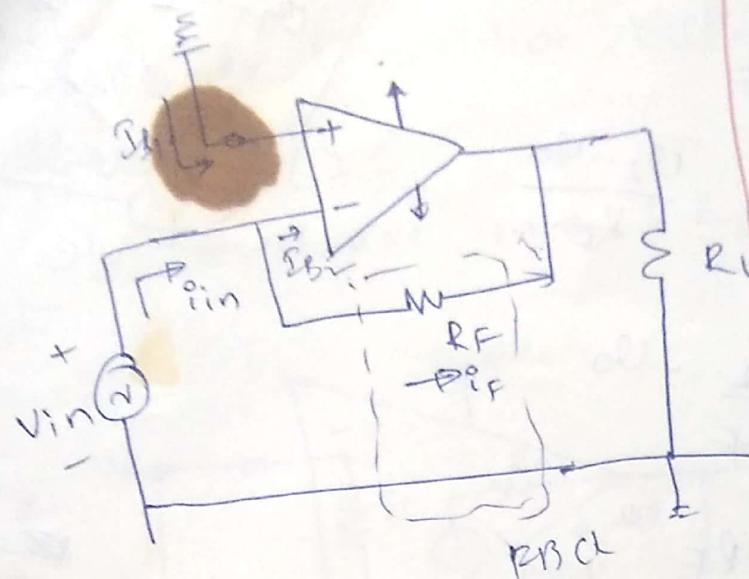
$$R_{EF} = 36.4\text{G}\Omega$$

$$R_{OF} = \frac{75\Omega}{18,182.8} = 4.12\text{m}\Omega$$

$$f_F = (5\text{Hz})(18,182)$$

$$= 90.9\text{kHz}$$

Voltage shunt FB amp.



$$AF = \frac{A \cdot R_F}{R_F + R_L}$$

$$1 + \frac{A \cdot R_F}{R_F + R_L}$$

$$AF = \frac{A \cdot K}{1 + AB}$$

$$K = \frac{R_F}{R_F + R_L} = \text{Voltage attenu.}$$

$$B = \frac{R_L}{R_F + R_L}$$

$$I_{in} = I_F + I_B$$

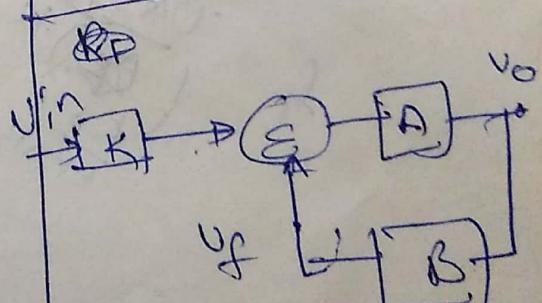
$$I_{in} \approx I_F$$

$$\frac{V_{in} - V_2}{R_1} = \frac{V_2 - V_o}{R_F}$$

$$V_1 - V_2 = \frac{V_o}{A}$$

$$V_1 = 0; V_2 = -\frac{V_o}{A}$$

$$\frac{V_{in} + \frac{V_o}{A}}{R_1} = -\frac{V_o}{A} - \frac{V_o}{R_F}$$



Block dia of
Inverting amp.
with FB

Inverting Op Terminal at
Virtual Ground.

$$i_{in} \approx i_F$$

$$\frac{v_{in} - v_2}{R_1} = \frac{v_2 - v_o}{R_F}$$

$$\frac{v_{in}}{R_1} = \frac{v_2}{R_F} - v_o$$

$$\frac{v_o}{v_{in}} = -\frac{R_F}{R_1}$$

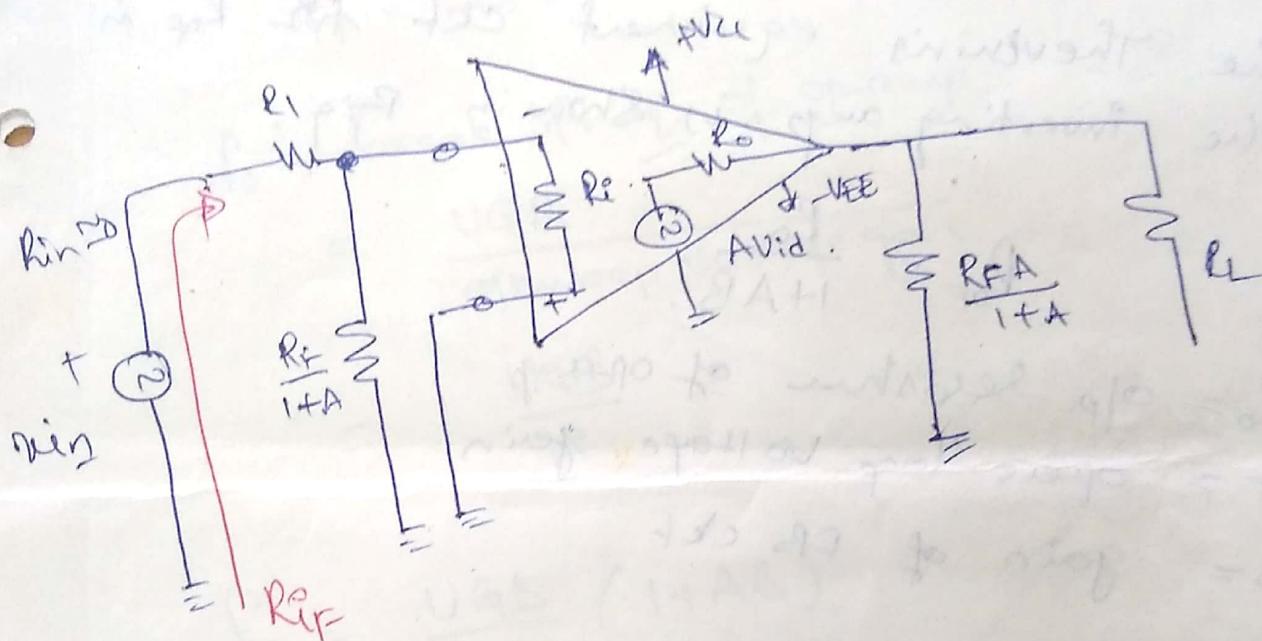
$$v_1 - v_2 = \frac{v_o}{R_F}$$

v_1, v_2 @ v_o

Input resistance with F.B.

The easiest method of finding the SIp resistance is to Millerize the F.B. feedback resistance into two Miller components i.e. split R_F into two Miller components as shown in fig.

The SIp resistance with F.B. R_{IF} is the



$$R_{IF} = \left(R_E \parallel \frac{R_F}{1+A} \right) + R_I \quad \text{exact}$$

$$\therefore \frac{R_F}{1+A} \parallel R_E \approx 0$$

$$R_{IF} = R_I$$

Output resistance with FB.

88

R_{OF} is the resistance measured at the o/p terminal of the FB amp. The o/p resistance of the noninverting amp was obtained by using Thevenin's theorem and we can do the same for the inverting amp.

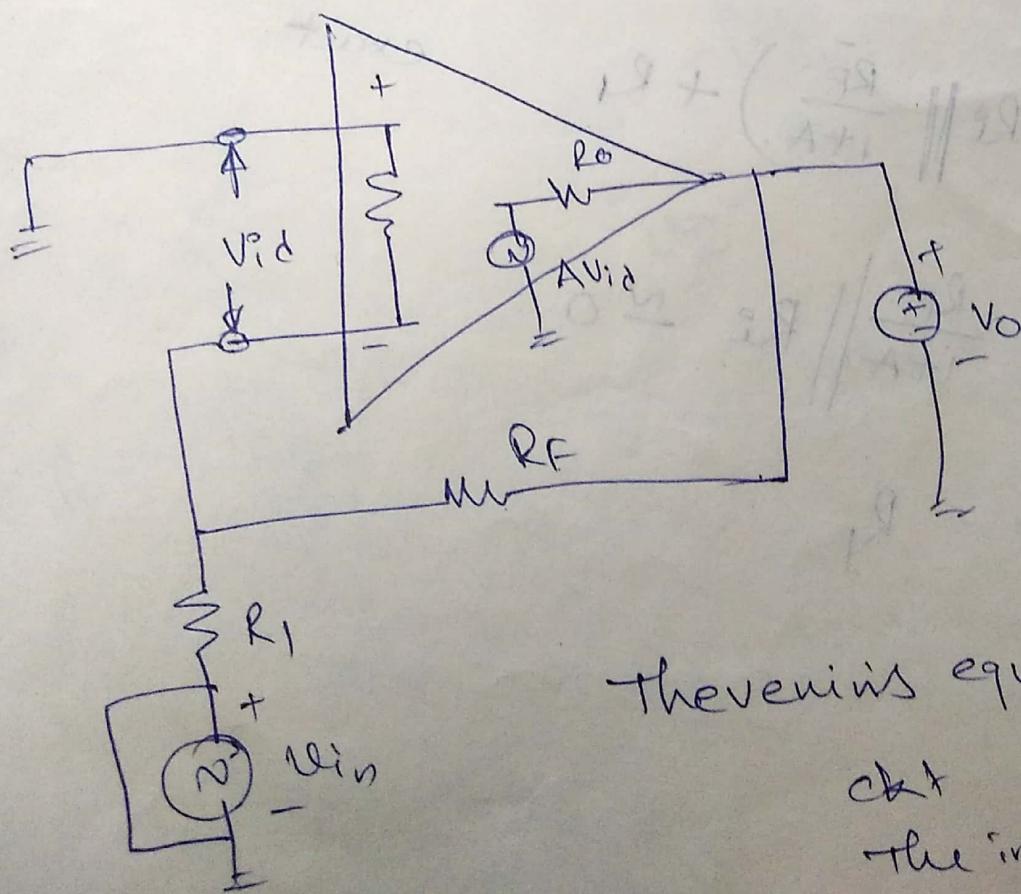
The Thevenin's equivalent ckt for R_{OF} in the inverting amp is shown in Fig.

$$R_{OF} = \frac{R_o}{1+AB}$$

R_o = o/p resistance of opamp

A = open-loop voltage gain

B = gain of FB ckt



Thevenin equivalent
ckt for R_{OF} of
the inverting amp.

Band width with FB.

The gain Bandwidth product of a single break freq opamp is always constant. we also saw that the gain of the amp with feed back is always less than the gain with out feed back.

$$f_p = f_0(1+AB)$$

f_0 = break freq of opamp.

$$= \frac{U_{GB}}{\text{openloop voltage gain}}$$

$$= \frac{U_{GB}}{A}$$

$$f_p = \frac{U_{GB}}{A} (1+AB)$$

$$= \frac{U_{GB}(K)}{A.F.}$$

$$K = \frac{R_F}{R_1 + R_F}$$

$$A.F. = \frac{A K}{1+AB}$$

$$= 1MHz (1 + 2 \times 10^5 \times \frac{1}{3.4})$$

$$\Delta f_p = \frac{K A F}{P F}$$

$$A.F. = \frac{P_F}{1+AB}$$

$$\Delta f_p'' = \frac{U_{GB}(1+AB)}{P_F}$$

$$\frac{U_{GB} K}{P F}$$

$-V_{EE}$ | $V_A = \frac{V_{EE}}{R_1 + R_2} = 18.195 \text{ mV}$ | $A_F = \frac{V_{out}}{V_{in}} = 10.99$

Total output offset voltage with FB.

$$V_{o,FB} = \frac{\text{total output offset voltage with FB.}}{1+AB}$$

$$V_{o,FB} = \frac{\pm V_{sat}}{1+AB}$$

$\pm V_{sat} \rightarrow$
A \rightarrow openloop gain

B \rightarrow

$$B = \frac{R_F}{R_1 + R_F}$$

Current -to -voltage converter.

Let us consider ideal voltage gain
of inverting amp.

$$\frac{V_o}{V_{in}} = -\frac{R_F}{R_1}$$

$$V_o = -\left(\frac{V_{in}}{R_1}\right) R_F$$

However since $V_{in} = 0$ and $V_1 = V_2$

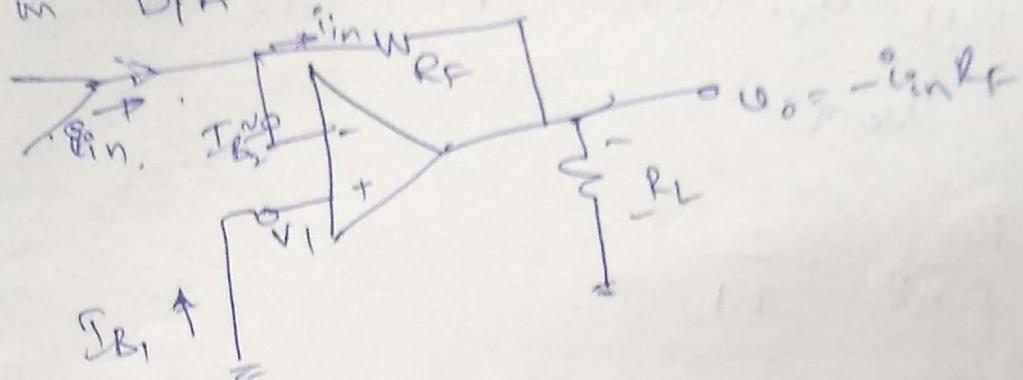
$$\frac{V_{in}}{R_1} = i_{in}$$

$$V_o = -(i_{in}) R_F$$

This means that if we replace the V_{in} and R_1 combination by a current source i_{in} as shown in fig the op voltage V_o

becomes proportional to the input current i_{in} . In other words, the circuit converts the input current into proportional output voltage.

Apps
Sensing current from photo-detectors and in D/A converter applications.



Inverter

If we need an output voltage signal equal in amplitude but opposite in phase to that of the I_{IP} signal, we can use the inverter. The inverting amplifier works as an inverter if $R_F = R_I$. Since the inverter is a special case of inverting amp all the equations developed for the inverting amp are also applicable here.

The eqs can be approx by substituting $A/2$ for $(1+AB)$

$$\text{Since } B = \frac{R_F}{R_I + R_F}$$

$$B = \frac{R_F}{R_I + R_F}$$

$$B = \frac{1}{2}$$

$$1+AB$$

$$1+A\frac{R_F}{R_I} \approx A/2$$

Prob.

For the inverting amp AF big $R_1 = 470\text{m}\Omega$
 and $R_F = 4.7\text{k}\Omega$. Assume that the
 op-amp is the 741 having same
 specification as those given in Ex-

cal the values A_F , R_{IF} , R_{OF} , f_F and

V_{OOT}

$$k = \frac{R_F}{R_1 + R_F} = \frac{1}{1.1}$$

$$B = \frac{1}{1.1}$$

$$1 + AB = 18,182.8$$

$$A_F = -10 \\ R_{IF} = 470 + \left[\frac{470}{2,000,000} \right] 2 \times 10^6 = 470.2$$

$$R_{OF} = \frac{25}{18,182.8} = 4.12 \text{ m}\Omega$$

$$f_F = 100 \text{ kHz} = \frac{542 (18,182.8)}{1.1}$$

$$V_{OOT} = \frac{\pm 13V}{18,182.8} = \pm 0.715 \text{ mV}$$