



A

B

C

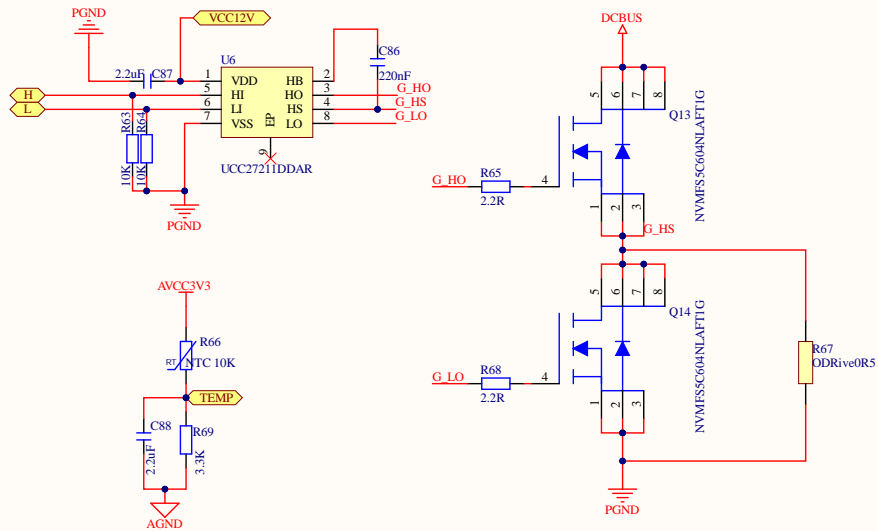
D

A

B

C

D





A

B

C

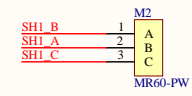
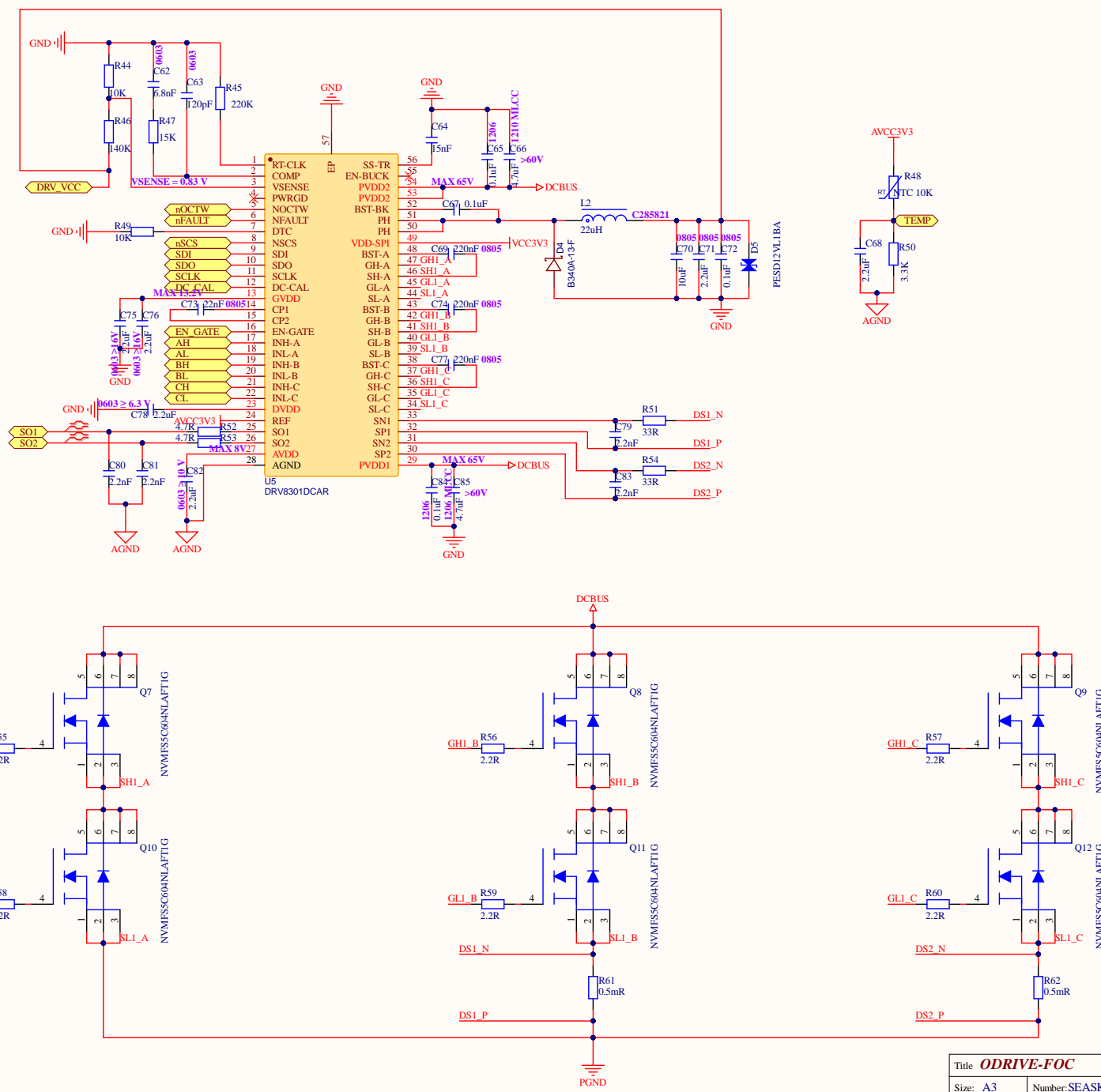
D

A

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D



输入端接线并电解电容

