## MACSAD An Exemplar Realization of Multi-Architecture P4 Pipelines



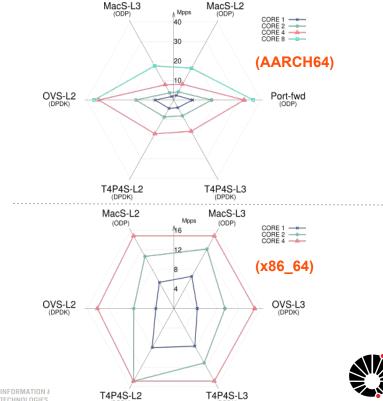
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## **High-level MACSAD Architecture**

## Select Use-Case Select Dataplane Target **Auxiliary Backend Auxiliary Frontend** SYSTEM DPDK MACSAD COMPILER DATAPATH LOGIC 1001101 1000001 CORE 1000011 COMPILER

**COMPILER** 

## Performance comparison on different platforms





**TRANSPILER** 



SoC(ARM)