## TAPAS - A P4 compiler for a variety of targets

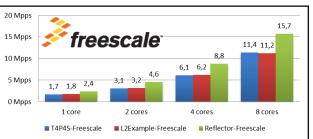
Sándor Laki, Dániel Horpácsi, Péter Vörös, Róbert Kitlei, Dániel Leskó, Máté Tejfel, Gergely Pongrácz

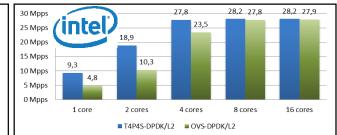
## A multi-target P4 compiler framework Table descriptions Headers Control P4 program Network HW Abstraction Library NPU Freescale DPDK Intel OpenWRT OpenWRT OpenWRT OpenWRT

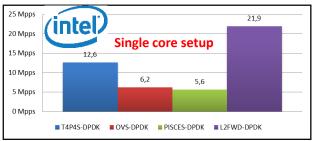
## **T4P4S – Translator for P4 Switches**

- Open source (on GitHub)
- P4-14 language support (P4-16 is coming soon)
- Supporting multiple targets
   (Hardware Independent Core +
   Network Hardware Abstraction Library)
- NetHALs for Intel (DPDK), Freescale (ODP SDK),
   OpenWRT (Native), ...

## L2 forwarding results for 2 targets (mesurements carried out in 3 testbeds w packet size 64B)







Web: <a href="http://p4.elte.hu">http://p4.elte.hu</a>

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