create\_clock -period 31.000 -name virtual\_clock -waveform {0.000 15.500}

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[0]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[0]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[0]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[0]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[1]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[1]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[2]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[2]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[3]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[3]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[4]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[4]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[5]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[5]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[6]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[6]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[7]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[7]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[8]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[8]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[9]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[9]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[10]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[10]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[11]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[11]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[12]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[12]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[13]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[13]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[14]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[14]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[15]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[15]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[16]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[16]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[17]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[17]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[18]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[18]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {Out[19]}]

set\_output\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {Out[19]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[1]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[1]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[2]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[2]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[3]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[3]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[4]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[4]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[5]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[5]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[6]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[6]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[7]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[7]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[8]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[8]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[9]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[9]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[10]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[10]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[11]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[11]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[12]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[12]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[13]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[13]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[14]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[14]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[15]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[15]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[16]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[16]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[17]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[17]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[18]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[18]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports {In[19]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports {In[19]}]

set\_input\_delay -clock [get\_clocks virtual\_clock] -min -add\_delay 0.010 [get\_ports Sign]

set\_input\_delay -clock [get\_clocks virtual\_clock] -max -add\_delay 0.010 [get\_ports Sign]