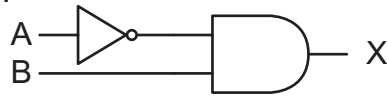


Combinational Logic

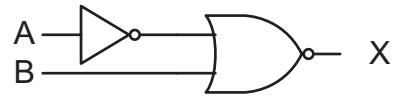
Determine the output of each gate circuit.

1.



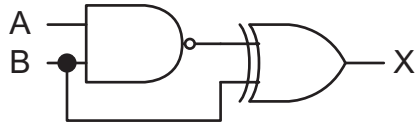
A=0, B=1

2.



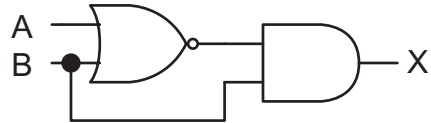
A=0, B=1

3.



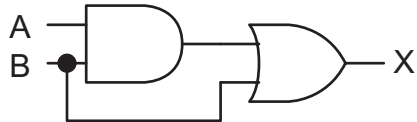
A=1, B=1

4.



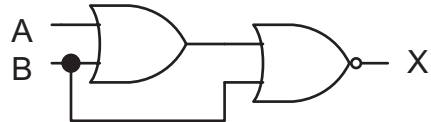
A=1, B=0

5.



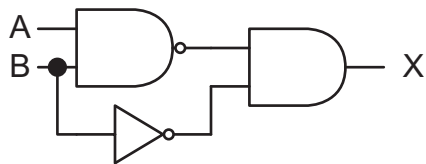
A=1, B=0

6.



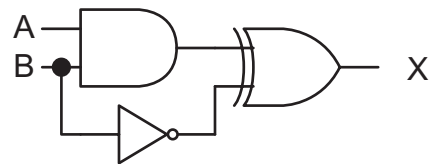
A=1, B=0

7.



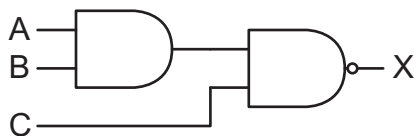
A=0, B=1

8.



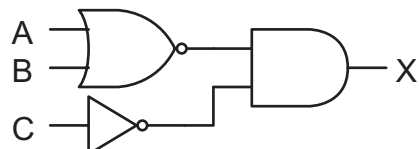
A=1, B=0

9.



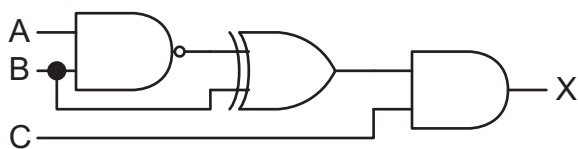
A=1, B=0, C=0

10.



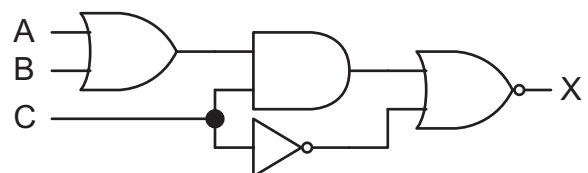
A=1, B=0, C=0

11.



A=1, B=0, C=1

12.



A=0, B=1, C=1