

AMESH BABU

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CAREER OBJECTIVE

A highly organized and hard-working individual looking for a responsible position to gain practical experience. A motivated individual capable of time management and working under pressure in all environments.

EDUCATION

Qualification	University/Board	Institution	Year	Percentage (%)
B. Tech (Electronics and Communication)	Jawaharlal Nehru Technological University, Kakinada	Sri Mittapalli College of Engineering, Tummalapalem	2020	64
Intermediate (Science)	Board of Intermediate Education, Andhra Pradesh	Vignana Bharati Junior College, Chirala	2018	76
Matriculation	Board of Secondary Education, Andhra Pradesh	Ashoka High School, Pedanandipadu		85

PROJECTS

DESIGN AND IMPLEMENTATION OF REVERSABLE FIR FILTER USING VERILOG HDL

- The aim of designing a Reversible FIR (Finite Impulse Response) filter using Verilog HDL (Hardware Description Language) involves creating a digital filter that can perform signal processing tasks while maintaining reversibility.
- Reversibility in this context typically refers to the ability to recover the original input signal from the filtered output, ensuring lossless information transformation.

SKILLS

Technical Skills: C Language, HTML, Java, Java Script, Css, Arduino, Embedded Systems .

Soft Skills: Team work, Analytical skills, Critical thinking, Patience. **Languages:** English, Telugu.

CERTIFICATIONS/WORKSHOPS

ELECTRONIC WORKSHOP

- This workshop is all about the basic electronic components. I learnt about each component working which makes ease while doing practical.

EMBEDDED SYSTEM WORKSHOP

- This is an one week workshop where I learnt the Embedded system. And also, I learnt how the android works and what are the operating systems used in it.

ACHIVEMENTS

- Participated in interschool competitions held by Sri Srinivasa High School, pedanandipadu.

DECLARATION

I hereby declare that the above furnished information is true to the best of my knowledge and belief.

(AMESHBABU DOPPALAPUDI)