## T.E. Sem- D (CBSGS), computer Engineering, may-2016 microprotessor

(05)

QP Code: 31091

(80 Marks) (3 Hours)

•	Questi	on no. 1 is compulsory.	
	Answe	r any three questions from question no. 2 – 6.	
	Assum	e suitable data, if necessary.	1
Q.1	. An	swer following questions in brief.	0
	a.	Explain programming model of 8086.	(05)
	b.	Explain V86 mode of 80386DX.	(05)
	c.	Explain, In brief, pipeline stages on Pentium processor.	(05)
	d.	Explain, in brief, data format supported by SuperSparc processor.	(05)
Q.2	. a.	Explain memory segmentation with pros and cons.	(80)
32	b.	Draw and explain the block diagram of 8255. Also, explain different operating	(12)
		modes of 8255.	Output Market
Q.3	a.	Design 8086 based minimum mode system for following requirements:	(12)
		I. 256 KB of RAM using 64 KB x 8-bit device	
		II. 128 KB of RAM using 64 KB x 8-bit device	
		III. Three 8-bit parallel ports using 8255	
		IV. Support for 8 interrupts	(00)
02173	b.	Explain, in brief, cache organization of Pentium processor.	(08)
Q.4		Draw and explain architecture of SuperSparc processor.	(12)
	b.	Discuss, in brief, protection mechanism of 80386DX.	(08)
Q.5		Draw and explain architecture of Pentium processor	(10)
	b.	Draw timing diagram of read operation on 8086 based system.	(10)
Q.E		rite short notes on	(05)
	a.	8089 I/O Processor	(05)
	b.	Comparison between i5 and i7	(05)
	C.	SuperSparc registers ( )	(05)

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d. 8259 - PIC

7	E (	Sem-Y)-CBSG3-16	QP Code:	31049	11
	(on	sem-V)-CBSGS-16 puler engg. (3 Hours)	[ Total Ma	arks: 80	
I	N.B.	<ol> <li>Question No. 1 is compulsory</li> <li>Attempt any three questions from remaining question</li> <li>Assume Suitable data if required</li> </ol>	ons.	100	,6
1.	(a) (b) (c) (d)	Discuss various scheduling Criteria.	Š	P. Coll	5 5 5
	3			dondlook	
2		What is deadlock? Explain the necessary and sufficient consuggest techniques to avoid deadlock.  Differentiate the following:  i) Process vs Thread ii) Preemptive vsNon-Preemptive Schools			10
3	. (a)	Explain the following in brief: i) Process Synchronisation ii) Inter-Process Communica	tion (IPC)		10
	(b)	Discuss partition selection algorithm in brief. Given mem 200k, 300k & 550k (in order), how would each of the fir algorithm place the processes of 220k, 430k, 110k & 425k (makes the most efficient use of memory?	oy partition of 15 st fit, best fit and	l worst fit	10
4	. (a)	Find AWT, ATAT, ART and AWTAT for the following s burst time in ms. Assume that all processes arrive at time (P1-19), (P2-7), (P3-3)  i) FCFS with order P2, P3, P1  ii) Round Robin (Quantum = 2ms)		with CPU	10
)	(b)	Explain paging hardware with TLB along - with protection	bits in page table	e.	10
5	. (a) (b)	6 1 6 11 1	ng using page rep	olacement	10 10
6	(a) (b) (c)	te short note on the following: (Any Two) File management in Linux Belady's anomaly Case study of windows operating system Virtual memory	anka, m		20

1	TE sem II, (CBGS) Computer Enga.  QP Code: 311  Structured and object oriented Analysis 4 Demign  [Total Marks:	75
	and about mounted Analysis A Denign	13
	Amentaged and object offers They as 4 story	80]
	5-20   6 (3 HOURS)	41
_( ~		4/1
	N.B.: (1) Question no. 1 is compulsory.	2
	(2) Attempt any three questions from remaining.	
	(3) Assume suitable data wherever necessary.	0
	Q1. (a) What is UML? Explain role of different UML diagrams in system design.	(10)
	(b) Explain DFD (up to two levels) with suitable example.	(10)
	(b) Explain DFD (up to two levels)	
	Q2. (a) Explain different requirement gathering techniques used in system analysis.	(10)
	(b) Explain software development life cycle used for system analysis:	(10)
-	(b) Explain software do large	
	Q3. (a) What is cost benefit analysis? Illustrate any one model of cost benefit analysis.	(10)
	(b) Explain Zachman's framework for software system design.	(10)
	Q4.(a) Draw the use case diagram of a library management system with extend, include	e and
	generalize relations between use cases.	(10)
	(b) What are characteristics of good graphical user interface? Draw GUI for o	nline
	course registration system.	(10)
	Q5.(a) Assume that the hospital management system is deployed using 3 tier archite	cture,
	explain its various components and its deployment with the help of diagrams.	(10)
	(b) Draw the sequence diagram for login procedure to a system. Include all po	ssible
0	scenarios and draw its activity diagram also.	(10)
	Q6. (a) Define cohesion and coupling in system design? High cohesion and low coup	ling is
	recommended for good system design. Justify.	(10)
	(b) Your college wishes to prepare and maintain the database system to track prog	ress of
	the students who were recruited through college training and placement	cen.
	prepare the proposal to design above system.	(10)

T.E. Sem-I (CBSGS), computer Engineering; computer MW's

QP Code: 31134

(3 Hours)

Total marks 80

## Note:

- Question No. 1 is compulsory.
- Attempt any Three questions out of remaining questions.
- Make suitable assumptions whenever necessary.

Q1:

- a) Compare connection oriented and connectionless services.
  - b) Explain in short Subnetting.
  - c) Explain in short different framing Methods.
  - d) Explain in short TCP/IP Model.
- e) What is the use of SSH?
- Q 2: a) Explain any four functions of Data Link layer with example.
- b) What is IPv4 protocol? Explain the IPv4 Header format with diagram. [10] Q 3:
  - a) Explain Classless Inter Domain Routing (CDR). [10]
    - b) Discuss the quality of service parameters in computer network. [10]

04:

- a) What are the steps involved in link state routing. Explain the contents and requirements of link state packets. [10]
- b) Compare Open Loop congestion control, Closed Loop congestion control.[10]

Q 5:

- a) Write a Program for client-server application using Socket Programming(TCP).
- b) An ISP is granted a block of addresses starting with 150.80.0.0/16.
  - The ISP wants to distribute these blocks to 2600 customers as follows. a. The first group has 200 medium-size businesses; each needs 128
  - b. The second group has 400 small businesses; each needs 16
- c. The third group has 2000 households; each needs 4 addresses. Design the subblocks and give the slash notation for each subblock. Find out how many addresses are still available after these allocations. [10]

Q 6: Write short notes on the following.

[5 X 4]

- a) Virtual LAN
- b) FDDI
- c) BGP
- d) SNMP

FW-Con. 10937-16.