

MCQ_TE-B_Subject-SPCC_R16_Block2

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MCQ Section

Q1 *

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|-----------|---|
| 1. | Which language processor bridges an execution gap but not translator? |
| Option A: | Pre-processor |
| Option B: | Assembler |
| Option C: | Compiler |
| Option D: | Loader |

☒ A

☐ B

☐ C

☐ D

Q2 *

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| 2. | What are the fields present in MOT used in two pass assembler design? 1. Mnemonic opcode 2. Binary opcode 3. Instruction length 4. Instruction format |
| Option A: | 1 & 2 only |
| Option B: | 2 & 3 only |
| Option C: | 1,2,3 & 4 |
| Option D: | 3 & 4 only |

☐ A

☐ B

☒ C

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Q3 *

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| 3. | Which of the following types of statements are present in assembly program? |
| Option A: | Imperative statements and assembler directives |
| Option B: | imperative and declarative statement |
| Option C: | imperative and declarative statement as well as assembler directive |
| Option D: | declarative statements and assembler directive |

☐ A

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Q4 *

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| 4. | In a two-pass assembler, which of the following process is/are done under first pass? 1. Adding literals to literal table 2. Address resolution of local symbol |
| Option A: | 1 only |
| Option B: | 2 only |
| Option C: | 1 & 2 both |
| Option D: | Neither 1 nor 2 |

☒ A

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Q5 *

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| 5. | Consider following code. What will be the entry come under <u>MNT</u> ? The current values for MDTC and MNTC are MDTC=1 and MNTC=1 MACRO INCR4 &AR1, &AR2, &AR3 A 1, &AR1 A 2, &AR2 A 3, &AR3 MEND |
| Option A: | Index-4, Macro name- INCR4, MDT index- 1 |
| Option B: | Index-1, Macro name- MACRO, MDT index- 1 |
| Option C: | Index-1, Macro name- INCR4, MDT index- 1 |
| Option D: | Index-4, Macro name- MACRO, MDT index- 4 |

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Q6 *

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| 6. | What is the process of replacing macro name by the statements and instructions included in macro definition is called? |
| Option A: | Expanding Macro |
| Option B: | Inserting a Macro |
| Option C: | Initializing a Macro |
| Option D: | Installing a Macro |

☒ A

☐ B

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Q7 *

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| 7. | Which of the following is not a data structure used during Macro Processor design? |
| Option A: | Symbol table |
| Option B: | MNT |
| Option C: | MDT |
| Option D: | ALA |

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Q8 *

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| 8. | In case of Direct Linking Loader, which are the fields are present in Relocation and Linkage Directory (RLD) card? |
| Option A: | source card reference number, ESD ID, Length, Flag, Relative Address |
| Option B: | source card reference number, ESD ID, Flag |
| Option C: | source card reference number, ESD ID, Relative Address |
| Option D: | source card reference number, Relative Address, Length |

☒ A

☐ B

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Q9 *

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| 9. | In case of absolute loading scheme, which function is performed by loader? |
| Option A: | Loading and Allocation |
| Option B: | Loading |
| Option C: | Relocation |
| Option D: | Allocation |

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☒ B

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☐ D

Q10 *

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| 10. | In which of the following scheme the loading and linking of external references is done at execution time? |
| Option A: | Absolute Loading |
| Option B: | Dynamic Linking |
| Option C: | General Loading |
| Option D: | Compile and go loading |

☐ A

☒ B

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Q11 *

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| 11. | Consider following Expression. $-(p * q) + (r + s) - (p + q + r + s)$ How many numbers of temporary variables are required to construct 3 address code? |
| Option A: | 8 |
| Option B: | 7 |
| Option C: | 9 |
| Option D: | 6 |

☐ A

☒ B

☐ C

☐ D



Q12 *

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| 12. | In which Code Optimization technique, variables are replaced with constants that have been assigned to them? |
| Option A: | loop optimization |
| Option B: | constant folding |
| Option C: | local optimization |
| Option D: | Constant propagation |

- ☐ A
- ☐ B
- ☐ C
- ☒ D

Q13 *

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| 13. | Which technique is applicable to optimize the given code? a=10; for (j=0 ; j< a*2; j++) { x= j+2; } |
| Option A: | Code Motion |
| Option B: | Copy Propagation |
| Option C: | Induction Variable Reduction |
| Option D: | Common Sub-expression Elimination |

- ☐ A
- ☐ B
- ☒ C
- ☐ D

Q14 *

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| 14. | Which of the following cannot be used as intermediate code form? |
| Option A: | Post fix notation |
| Option B: | Three address code |
| Option C: | Abstract Syntax tree |
| Option D: | Token |

- ☐ A
- ☐ B
- ☐ C
- ☒ D

Q15 *

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| 15. | What of the following graph represents flow of control among the set of basic blocks? |
| Option A: | Hamiltonian graph |
| Option B: | Control graph |
| Option C: | Flow graph |
| Option D: | DAG |

- ☐ A
- ☒ B
- ☐ C
- ☐ D

Q16 *

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| 16. | What will be the FOLLOW(A) for following grammar? $S \rightarrow AaAb$ $S \rightarrow BaBb$ $A \rightarrow \epsilon$ $B \rightarrow \epsilon$ |
| Option A: | Only a |
| Option B: | a, b |
| Option C: | Only b |
| Option D: | Only ϵ |

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☒ B
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☐ D

Q17 *

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| 17. | Which of the following grammar is appropriate for operator precedence grammar? |
| Option A: | $S \rightarrow EF$ |
| Option B: | $S \rightarrow E * F \mid \epsilon$ |
| Option C: | $S \rightarrow E + F$ |
| Option D: | $S \rightarrow +EF$ |

- ☐ A
☐ B
☒ C
☐ D

Q18 *

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| 18. | Which of the following statement are correct for Syntax Directed Definition? i. The terminals do not have inherited attributes. ii. The non-terminal can have both inherited and synthesized attributes. iii. Each grammar symbol is associated with a set of attributes. |
| Option A: | i only |
| Option B: | i, ii and iii |
| Option C: | ii and iii |
| Option D: | iii only |

- ☐ A
- ☒ B
- ☐ C
- ☐ D

Q19 *

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| 19. | Which of the following approach is used to evaluate the attributes in L-attributed SDTs? |
| Option A: | DFS with left-to-right Parsing |
| Option B: | BFS with left-to-right Parsing |
| Option C: | DFS with right-to-left Parsing |
| Option D: | BFS with right-to-left Parsing |

- ☒ A
- ☐ B
- ☐ C
- ☐ D

Q20 *

| | |
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| 20. | Which sentence/s is correct with <u>respesct</u> to lexical analyzer? 1. Recognizing the tokens 2. To organize the variables in a lexical order 3. Building a literal and identifier table |
| Option A: | 1 only |
| Option B: | 2 & 3 only |
| Option C: | 1, 2 & 3 |
| Option D: | 1 & 3 only |

☒ A

☐ B

☐ C

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