**COMPUTER ENGINEERING DEPARTMENT**

**ASSIGNMENT NO-02**

**SUB: SPCC**

**COURSE: T.E. Year: 2020-2021 Semester: VI**

**DEPT: Computer Engineering**

**SUBJECT CODE: CSC602 SUBMISSION DATE: 20/04/2021**

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**Assignment No 2**

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| **Sr. No.** | **Question** |
| 1 | Explain the forward reference problem and how it is handled in assembler design. |
| 2 | For the following code what will be the output generated by Pass-I and Pass-II of the assembler. Explain with databases.   |  |  |  |  | | --- | --- | --- | --- | | **Stmt no** | **Symbol** | **Op-code** | **Operands** | | 1 | ABC | START | 0 | | 2 |  | USING | \*, 15 | | 3 |  | L | 1, FIVE | | 4 |  | A | 1, FOUR | | 5 |  | ST | 1, TEMP | | 6 | FOUR | DC | F ‘4’ | | 7 | FIVE | DC | F ‘5’ | | 8 | TEMP | DS | 1F | | 9 |  | END |  | |
| 3 | Explain different types of loaders in details. |
| 4 | Explain the working of the direct linking loader with an example, showing entries in different databases built by DLL. |
| 5 | Explain the working of the two-pass macro processor with neat flowcharts and databases. |
| 6 | Explain different features of macro with example. |

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