Online_Test(All Subject)

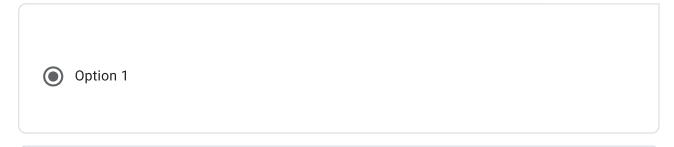
Total points 40/40



SH2020

Email address * ameythakur@ternaengg.ac.in

0 of 0 points





★ Roll No * 1.1 2. 2 3.3 4. 4 5. 5 6.6 7. 7 8.8 9.9 10.10 11.11 12. 12 13. 13 14. 14 15. 15 16. 16 17. 17 18. 18 19. 19 20.20 21. 21 22. 22 23. 23 24. 24 25. 25

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26. 26

27. 27 28. 28 29. 29 30.30 31.31 32.32 33.33 34. 34 35.35 36.36 37. 37 38.38 39.39 40.40 41.41 42.42 43.43 44. 44 45.45 46.46 47. 47 48.48 49.49 50.50 X 51.51 52. 52 53.53

- 54. 54
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- 73. 73
- 74. 74
- 75. 75
- 76.76
- 77. 77
- 78. 78
- 79.79
- 80.80

20	Online_Test(All Subject)
	81. 81
	Correct answer
	1
	Student Name *
	Amey Thakur
	DBMS 8 of 8 points
	1. the level of data abstraction which describes how the data is actually 1/1 stored is: *
	onceptual level
	physical level
	ofile level
	none of the above
	Feedback
	physical level

✓ 2. A data model is: *	1/1
set of basic operations on the database	
used to describe structure of a database.	✓
both of the above	
onone of the above	
Feedback describes structure of the database	
3. Which of the following is an advantage of using a database system.	stem * 1/1
a)all othe below	~
b)Security can be enforced	
c) Redundancy can be avoided	
d)Reduced inconsistency	
4.Users who query and update a database by using pre-written application programs are called: *	1/1
Special users	
O Sophisticated users	
Naive users	✓
System analysts	

5. The person who has the central control over data and application 1/1 programs is: *
System analyst
● DBA
O Database designer
None
 6. Which of the following schemas defines the stored data structures for 1/1 the database model used *
Conceptual
External
Internal
None
7. Which component of the DBMS architecture handles the authorization 1/1 of users *
Query processor
Storage manager
O User interface

~	8. Which of the following statement is correct? Student (Roll_no,Name, Phone number)Retrive name and Phone number from student table. *	1/1
•	Select name, phone number from student;	✓
0	Select * from student;	
0	Select name from student;	
0	Select phone number ,rollno from student;	
CN	8 of 8	ooints
✓	1. The created a model called the Open Systems Interconnection, which allows diverse systems to communicate. *	1/1
0	IEEE	
•	ISO	✓
0	OSI	
0	None of the above	
~	2. The layer changes bits into electromagnetic signals. *	1/1
•	A) Physical	✓
0	B) Transport	
0	C) Data link	
0	D) None of the above	

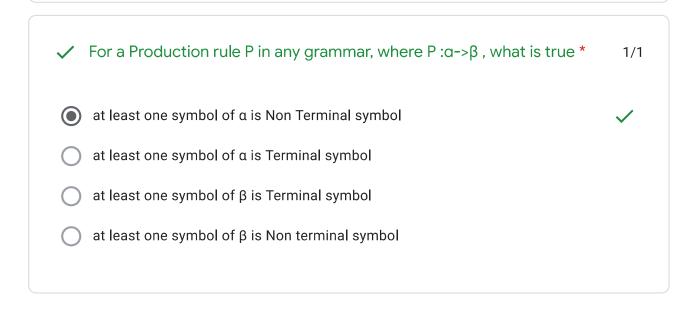
3. The is the physical path over which a message travels. *	1/1
A) Protocol	
B) Medium	✓
C) Signal	
O D) All of the above	
4. The layer is responsible for moving frames from one hop (node to the next. *	e) 1/1
A) transport	
B) data link	✓
C) physical	
D) none of the above	
5. An unauthorised user is a network Issue. *	1/1
A) Performance	
B) Reliability	
C) Security	✓
D) All of the above	

✓	6. Before Data can be transmitted, they must be transformed to *	1/1
0	A) periodic signals	
•	B) electromagnetic signals	✓
0	C) aperiodic signals	
0	D) low frequency sine waves.	
/	7. A periodic signal completes one cycle in 0.001 s. What is the frequency? *	1/1
0	A) 1 Hz	
0	B) 100 Hz	
•	C) 1 KHz	✓
0	D) 1 MHz	
/	8. In a Time-domain plot, the horizontal axis is a measure of	* 1/1
0	A) signal amplitude	
0	B) frequency	
0	C) phase	
•	D) time	✓

7 of 7 points TCS

For given language $L(G)=\{a^mb^n:n,m=1,2,\}$, state which strings can not be generated *
a
o aaab
aaabbbb
ab

✓	For given language $L(G)=\{a^nb^n: n=0, 1, 2,\}$, state which strings can not be generated *	1/1
0	empty	
0	ab	
0	aabb	
•	bbbaaa	~



✓ A grammar G can be formally written as a 4-tuple *	1/1
● V,T, S,P	~
NT, P,S,Q	
○ R,P,Q,S	
○ S,P,R,T	
✓ Select what is true among following *	1/1
Non Terminal and variable i.e. NT and V means same	✓
P is starting symbol of a grammar	
Terminal symbols are written in capital letters	
We can derive productions from terminal symbol	
Given a NFA with N states, the maximum number of states in an equivalent minimized DFA is at least.	1/1
○ N^2	
② 2^N	✓
2N	
O N!	

~	Languages of a automata is	1/1
•	If it is accepted by automata	✓
0	If it halts	
0	If automata touch final state in its life time	
0	All language are language of automata	
✓	Number of states require to simulate a computer with memory capable of storing '3' words each of length '8'.	1/1
0	3 * 28	
•	2(3*8)	✓
0	2(3+8)	
0	None of the mentioned	
WDL	. 8 of 8 p	points
~	1. What are structural components of WWW *	1/1
0	a) Internet	
0	b) Servers	
0	c) Client/browsers, servers, internet	
•	d) Client/browsers, servers, internet, catches	✓

2. HTTP stands for *	1/1
a) Hypertext transfer port	
b) Hypertext transfer protocol	✓
C) Hypertext transfer presentation	
d) Hypertext terminal port	
3. There are how many heading tags are there in HTML *	1/1
a) H1,h2,h3,h4	
b) H1,h2,h3,h4,h5,h6	~
c) H1,h2,h3,h4,h5,h6,h7,h8	
(a) H1,h2,h3	
✓ 4. HTML document contains *	1/1
a) Tags	
b) Plain text	
c) Plaintext and tags	~
d) None of the above	
d) None of the above	

5. Who is making the Web standards? *	1/1
a) Microsoft	
b) Google	
c) World wide web consortium	✓
d) Mozilla	
✓ 6. Choose the correct HTML element for the largest heading: *	1/1
a) <h1></h1>	✓
b) <head></head>	
c) <heading></heading>	
(d) <h6></h6>	
✓ 7. What is the correct HTML element for inserting a line break? *	1/1
(a) <lb></lb>	
b) break>	
(a) br>	✓

✓	8. What is the correct HTML for creating a hyperlink? *	1/1
0	a) <a><u>http://www.google.com</u>	
0	b) http://www.google.com	
•	c) <a href="<u>http://www.google.com</u>"> google	/
0	d) google	
MP	9 of 9 pc	oints
✓	1. The clock rate of microprocessor 8086 is*	1/1
0	5 MHZ	
0	8 MHZ	
0	10 MHZ	
•	All of the mentioned	✓
~	2.Stack Pointer is decremented by 2 , for following operation in 8086 *	1/1
0	DIV	
0	POP	
•	PUSH	~
0	None of above	

3. The size of each segment in 8086 is: *	1/1
	✓
50 kb	
24 kb	
4. The address of a memory is a 20-bit address for the 8086 microprocessor: *	1/1
Logical	
Physical	✓
Both	
None of these	
5. Which register contains the 8086/8088 flag? *	1/1
Status register	✓
Stack register	
Flag register	
Stand register	

6. If MN/MX is low the 8086 operates in mode. *	1/1
Minimum	
Maximum	✓
O Both a and b	
Medium	
✓ 7. BHE of 8086 microprocessor signal is used to interface the*	1/1
O 1/0	
O DMA	
Even bank memory	
Odd bank memory	✓
✓ 8. INTR pin of a microprocessor 8086 is used **	2/2
To indicate that processor is ready to receive i/o cammands	
To indicate that processor is ready to receive interrupts	✓
To provide direct memory access.	
To introduce wait states	

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