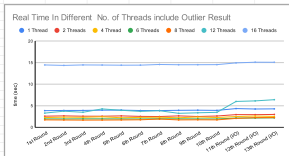
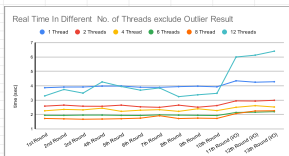


Physical Hardware	
CPU	Intel(R) Core(TM) i7-10750H CPU @ 2.60GHz BOOST @ 4.80GHz Base speed: 2.59 GHz Sockets: 1 Cores: 6 Logical processors: 12 Virtualization: Enabled L1 cache: 384 KB L2 cache: 1.5 MB L3 cache: 12.0 MB
RAM	16 GB Dual-Channel DDR4 @2933MHz
SSD	WD BLUE SN550 1TB : PCIE Gen3x4
GPU	RTX 2060 Moblile
OS	Windows 10 2H22
Hyper-V Environments	
CPU	6C/12T
RAM	8 GB
SSD	30 GB
OS	UbuntuLTS Desktop 22.04

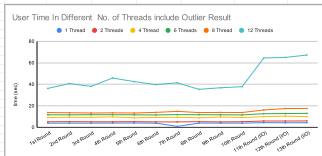
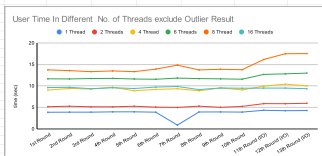
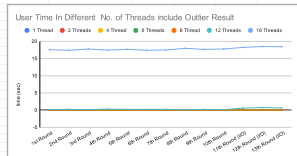
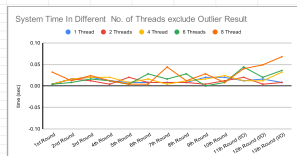
workload	10000 steps for find a convergent point
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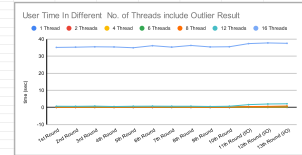
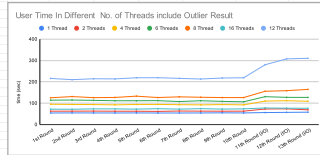
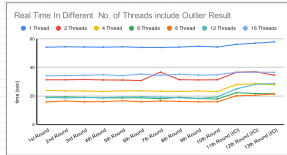
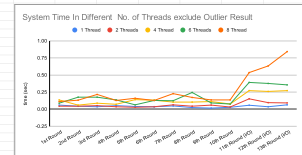
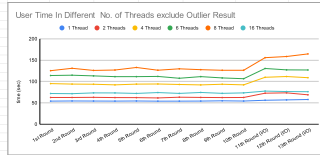
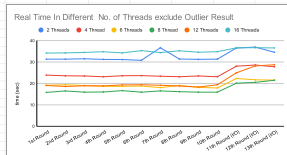
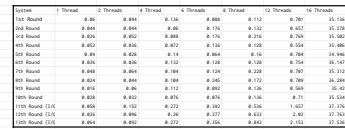
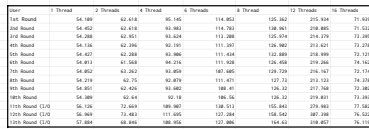
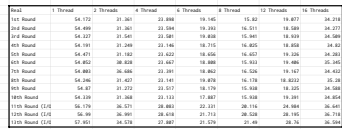
	1 Thread	2 Threads	4 Threads	8 Threads	16 Threads	32 Threads
1st Round	3.867	3.587	3.261	3.947	3.737	3.297
2nd Round	3.914	3.575	3.255	3.941	3.702	3.248
3rd Round	3.918	3.586	3.267	3.941	3.702	3.248
4th Round	3.96	3.575	3.242	3.963	3.673	3.232
5th Round	3.984	3.623	3.233	3.941	3.711	3.263
6th Round	3.988	3.528	3.260	3.941	3.702	3.248
7th Round	3.993	3.562	3.239	3.977	3.698	3.251
8th Round	3.944	3.654	3.221	3.958	3.722	3.246
9th Round	3.971	3.571	3.240	3.948	3.748	3.275
10th Round	3.921	3.624	3.275	3.948	3.733	3.245
11th Round (TLE)	4.348	3.951	3.544	2.146	2.869	3.680
12th Round (TLE)	4.253	3.939	3.619	2.146	2.460	3.122
13th Round (TLE)	4.253	3.939	3.619	2.146	2.460	3.122



	1 Thread	2 Threads	4 Threads	8 Threads	16 Threads	32 Threads	64 Threads
1st Round	3.871	5.148	6.932	11.45	13.733	36.186	8.368
2nd Round	3.897	5.237	6.908	11.423	13.505	49.718	8.371
3rd Round	3.927	5.362	6.927	11.717	13.718	43.818	8.371
4th Round	3.967	5.514	6.944	11.75	13.628	45.853	8.371
5th Round	3.975	5.572	6.976	11.487	13.514	45.523	8.368
6th Round	3.96	5.46	6.92	11.821	13.628	46.767	8.368
7th Round	3.884	4.905	5.34	11.82	14.832	49.487	8.368
8th Round	3.835	5.291	5.84	11.499	13.718	35.235	8.368
9th Round	3.885	5.505	6.522	11.680	14.834	36.887	8.368
10th Round	3.985	5.224	6.871	11.555	13.764	55.56	8.368
11th Round (C2)	4.136	5.878	5.942	12.872	17.146	64.5	8.368
12th Round (C2)	4.235	5.864	10.411	12.873	17.400	65.85	8.368
13th Round (C2)	4.235	5.864	10.411	12.873	17.400	65.85	8.368

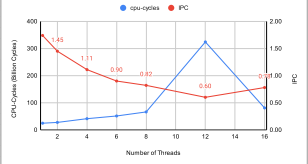
[illegible]

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66000 steps for find a convergent point : Possible Maximum
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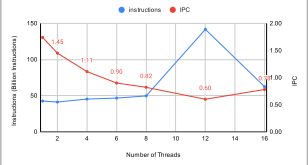
[illegible]

68800 steps for find a convergent point : 320x320 Square																						
Threads	cpu-cycles	Instructions	IPC	cache-references	cache-hit	cache-hit (%)	cache-misses (%)	dTLB-load	dTLB-load-misses	dTLB-load-misses (%)	dTLB-store	dTLB-store-misses	dTLB-store-misses (%)	ITLB-load	ITLB-load-misses	ITLB-load-misses (%)	branch-load	branch-load-misses	branch-load-misses (%)	time-elapsed (sec)		
1	24,494,232,796	42,528,423,239	1.74	2,387,063,104	2,272,852,841	95.22%	114,210,263	95.22%	4.78%	11,746,759,453	3,869,819	0.03%	3,256,471,154	1,841,469	0.03%	744,169	2,246,224	287.83%	8,776,929,882	24,789,947	0.28%	3.86795
2	27,415,789,298	41,184,251,852	1.45	2,226,558,812	2,117,829,481	95.08%	108,729,331	95.08%	4.92%	11,911,877,193	4,563,811	0.04%	3,447,644,887	1,889,454	0.03%	548,952	2,691,981	381.32%	8,641,974,655	24,546,958	0.28%	2.54802
4	41,164,141,022	40,382,938,489	1.11	2,329,421,844	2,216,811,132	95.14%	112,610,712	95.14%	4.78%	12,488,471,218	3,864,801	0.03%	3,365,279,194	1,856,234	0.03%	572,827	1,919,941	335.89%	9,137,648,887	24,789,489	0.27%	2.10910
8	51,136,396,045	46,616,149,542	0.90	2,486,822,274	2,313,884,793	93.81%	172,937,481	93.81%	3.86%	12,449,383,872	3,341,367	0.03%	3,283,166,344	1,889,215	0.03%	692,986	2,115,847	311.13%	9,852,888,545	24,613,116	0.25%	1.96385
16	68,882,641,088	49,645,132,287	0.82	2,355,866,744	2,232,447,316	94.76%	123,419,428	94.76%	5.24%	12,963,332,432	3,675,571	0.03%	3,432,742,717	1,427,858	0.04%	887,129	2,138,445	239.24%	10,616,743,485	26,439,831	0.25%	1.93890
32	124,128,627,127	145,647,786,319	0.98	2,384,176,281	2,873,522,883	77.19%	510,653,402	77.19%	5.16%	29,389,751,137	16,446,838	0.06%	3,849,888,125	4,251,587	0.11%	1,225,189	4,611,541	379.24%	75,394,887,883	37,964,555	0.13%	8.1
64	89,882,641,088	82,372,428,137	0.78	9,511,985,188	9,511,564,487	99.99%	420,420,701	99.99%	2.11%	16,455,882,137	183,799,528	1.12%	6,895,788,434	84,188,183	1.33%	18,889,884	222,913,689	2227.13%	13,816,325,957	124,334,849	0.94%	15.2162

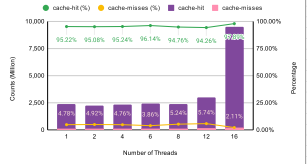
CPU Cycles &amp; IPC



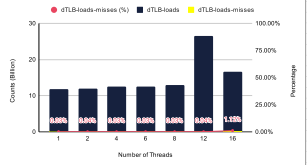
Instructions &amp; IPC



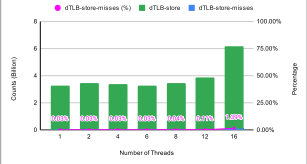
Cache Hit &amp; Cache Miss



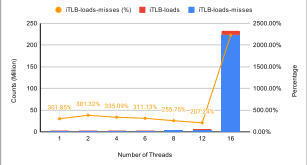
dTLB Load &amp; dTLB Load Miss



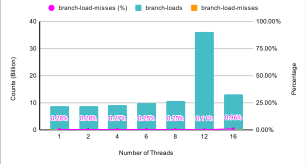
dTLB Store &amp; dTLB Store Miss



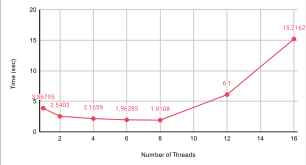
ITLB Load &amp; ITLB Load Miss



Branch Load &amp; Branch Load Miss



Time Elapsed



Performance counter stats for 'system wide' (10 runs):

24,494,232,796	cpu-cycles		( +- 0.58% ) (31.28%)
42,528,423,239	instructions	# 1.74 inn per cycle	( +- 0.18% ) (41.79%)
2,387,063,104	cache-references		( +- 0.87% ) (41.79%)
114,210,263	cache-misses	# 4.95% of all cache refs	( +- 1.03% ) (41.79%)
11,746,759,453	dTLB-load		( +- 0.15% ) (41.79%)
3,869,819	dTLB-load-misses	# 0.03% of all dTLB cache accesses	( +- 1.33% ) (41.79%)
3,256,471,154	dTLB-store		( +- 0.27% ) (33.38%)
1,841,469	dTLB-store-misses		( +- 1.54% ) (33.38%)
744,169	ITLB-load		( +- 2.78% ) (33.38%)
2,246,224	ITLB-load-misses	# 308.08% of all ITLB cache accesses	( +- 1.03% ) (33.38%)
8,776,929,882	branch-load	# 388.08% of all branch-load	( +- 0.28% ) (33.38%)
24,789,947	branch-load-misses		( +- 1.43% ) (33.38%)

3.86795 +- 0.00724 seconds time elapsed ( +- 0.19% )

Performance counter stats for 'system wide' (10 runs):

27,415,789,298	cpu-cycles		( +- 0.81% ) (31.21%)
41,184,251,852	instructions	# 1.45 inn per cycle	( +- 0.62% ) (41.68%)
2,226,558,812	cache-references		( +- 0.94% ) (41.79%)
108,729,331	cache-misses	# 4.78% of all cache refs	( +- 1.57% ) (41.94%)
11,911,877,193	dTLB-load		( +- 0.45% ) (42.88%)
4,563,811	dTLB-load-misses	# 0.04% of all dTLB cache accesses	( +- 2.58% ) (41.97%)
3,447,644,887	dTLB-store		( +- 0.67% ) (33.38%)
1,889,454	dTLB-store-misses		( +- 1.38% ) (33.38%)
548,952	ITLB-load		( +- 4.29% ) (33.11%)
2,691,981	ITLB-load-misses	# 508.58% of all ITLB cache accesses	( +- 1.82% ) (33.18%)
8,641,974,655	branch-load	# 388.08% of all branch-load	( +- 0.28% ) (33.38%)
24,546,958	branch-load-misses		( +- 1.58% ) (33.89%)

2.54802 +- 0.0103 seconds time elapsed ( +- 0.16% )

Performance counter stats for 'system wide' (10 runs):

41,164,141,022	cpu-cycles		( +- 0.79% ) (33.88%)
40,382,938,489	instructions	# 1.11 inn per cycle	( +- 0.48% ) (41.38%)
2,329,421,844	cache-references		( +- 0.48% ) (41.67%)
112,610,712	cache-misses	# 4.77% of all cache refs	( +- 1.51% ) (41.87%)
12,488,471,218	dTLB-load		( +- 0.53% ) (41.88%)
3,864,801	dTLB-load-misses	# 0.03% of all dTLB cache accesses	( +- 1.68% ) (42.16%)
3,365,279,194	dTLB-store		( +- 0.49% ) (33.38%)
1,856,234	dTLB-store-misses		( +- 1.68% ) (33.38%)
572,827	ITLB-load		( +- 1.58% ) (33.31%)
1,919,941	ITLB-load-misses	# 329.58% of all ITLB cache accesses	( +- 0.78% ) (33.83%)
9,137,648,887	branch-load		( +- 0.55% ) (32.99%)
24,789,489	branch-load-misses		( +- 1.89% ) (32.99%)

2.10910 +- 0.0146 seconds time elapsed ( +- 0.67% )

Performance counter stats for 'system wide' (10 runs):

51,136,396,045	cpu-cycles		( +- 0.51% ) (31.28%)
46,616,149,542	instructions	# 0.90 inn per cycle	( +- 0.28% ) (41.78%)
2,486,822,274	cache-references	# 0.90 inn per cycle	( +- 0.75% ) (41.98%)
189,523,981	cache-misses	# 7.62% of all cache refs	( +- 2.28% ) (41.91%)
11,911,877,193	dTLB-load		( +- 0.18% ) (41.91%)
4,563,811	dTLB-load-misses	# 0.03% of all dTLB cache accesses	( +- 1.68% ) (41.91%)
3,447,644,887	dTLB-store		( +- 0.67% ) (33.38%)
1,889,454	dTLB-store-misses		( +- 1.38% ) (33.38%)
548,952	ITLB-load		( +- 4.29% ) (33.11%)
2,691,981	ITLB-load-misses	# 508.58% of all ITLB cache accesses	( +- 1.82% ) (33.18%)
8,641,974,655	branch-load		( +- 0.28% ) (33.38%)
24,546,958	branch-load-misses		( +- 1.58% ) (33.18%)

1.96385 +- 0.0064 seconds time elapsed ( +- 0.48% )

Performance counter stats for 'system wide' (10 runs):

68,882,641,088	cpu-cycles		( +- 1.89% ) (32.88%)
49,645,132,287	instructions	# 0.82 inn per cycle	( +- 0.96% ) (41.28%)
2,355,866,744	cache-references		( +- 0.48% ) (41.38%)
123,419,428	cache-misses	# 5.11% of all cache refs	( +- 2.81% ) (41.58%)
12,963,332,432	dTLB-load		( +- 0.63% ) (41.78%)
3,675,571	dTLB-load-misses	# 0.03% of all dTLB cache accesses	( +- 4.72% ) (41.97%)
4,432,742,717	dTLB-store		( +- 0.93% ) (33.48%)
1,427,858	dTLB-store-misses		( +- 4.64% ) (33.52%)
887,129	ITLB-load		( +- 3.88% ) (33.54%)
2,138,445	ITLB-load-misses	# 388.14% of all ITLB cache accesses	( +- 2.68% ) (33.43%)
18,616,743,485	branch-load		( +- 1.85% ) (33.22%)
26,439,831	branch-load-misses		( +- 1.58% ) (33.82%)

1.93890 +- 0.0051 seconds time elapsed ( +- 2.98% )

Performance counter stats for 'system wide' (10 runs):

124,128,627,127	cpu-cycles		( +- 12.31% ) (33.31%)
145,647,786,319	instructions	# 0.68 inn per cycle	( +- 18.38% ) (41.68%)
2,384,176,281	cache-references		( +- 2.68% ) (41.78%)
171,195,688	cache-misses	# 5.62% of all cache refs	( +- 8.89% ) (41.71%)
16,465,828	dTLB-load		( +- 0.44% ) (41.71%)
16,465,828	dTLB-load-misses	# 0.03% of all dTLB cache accesses	( +- 11.95% ) (41.72%)
1,849,888,125	dTLB-store		( +- 2.74% ) (33.31%)
1,234,419	dTLB-store-misses		( +- 10.88% ) (33.31%)
2,225,189	ITLB-load		( +- 11.71% ) (33.31%)
4,611,541	ITLB-load-misses	# 266.94% of all ITLB cache accesses	( +- 11.58% ) (33.38%)
15,964,887,883	branch-load		( +- 11.25% ) (33.32%)
17,964,555	branch-load-misses		( +- 8.45% ) (33.38%)

8.1 +- 1.12 seconds time elapsed ( +- 18.36% )

Performance counter stats for 'system wide' (10 runs):

89,882,641,088	cpu-cycles		( +- 1.68% ) (33.31%)
82,372,428,137	instructions	# 0.94 inn per cycle	( +- 2.67% ) (41.65%)
9,511,985,188	cache-references		( +- 0.44% ) (41.68%)
188,488,193	cache-misses	# 0.19% of all cache refs	( +- 59.68% ) (41.67%)
16,455,882,137	dTLB-load		( +- 1.93% ) (41.68%)
162,769,528	dTLB-load-misses	# 1.12% of all dTLB cache accesses	( +- 0.25% ) (41.65%)
1,405,788,414	dTLB-store		( +- 1.88% ) (33.38%)
84,188,183	dTLB-store-misses		( +- 0.11% ) (33.38%)
18,889,884	ITLB-load		( +- 3.19% ) (33.34%)
222,913,689	ITLB-load-misses	# 213.95% of all ITLB cache accesses	( +- 0.34% ) (33.39%)
13,816,325,957	branch-load		( +- 1.13% ) (33.32%)
124,334,849	branch-load-misses		( +- 5.75% ) (33.31%)

15.2162 +- 0.88 seconds time el ( +- 0.85% )

Performance counter stats for 'system-wide' (10 runs): 5 Threads			
40,317,822,868	cpu-cycles		(+/- 0.34%) (33.126)
237,320,483,787	instructions	# 0.54 inst per cycle	(+/- 0.08%) (41.683)
40,379,440,838	cache-references	# 0.91% of all cache refs	(+/- 0.54%) (41.693)
43,492,437	cache-misses		(+/- 0.62%) (41.693)
43,394,401,837	ATLB-load-misses	# 0.016% of all ATLB accesses	(+/- 0.80%) (41.693)
23,284,547	ATLB-load-misses	# 0.03% of all ATLB cache accesses	(+/- 0.91%) (41.781)
15,388,401,871	ATLB-store-misses		(+/- 0.18%) (33.126)
9,051,408	ATLB-store-misses		(+/- 0.17%) (33.126)
2,274,766	ATLB-load-misses		(+/- 0.77%) (33.126)
8,866,759	ATLB-load-misses	# 361.45% of all ATLB cache accesses	(+/- 0.47%) (33.126)
9,732,688,152	branch-load-misses		(+/- 0.18%) (33.126)
33,749,784	branch-load-misses		(+/- 0.15%) (33.126)
14.805% ± 0.008% seconds time elapsed (+/- 0.78%)			
Performance counter stats for 'system-wide' (10 runs): 6 Threads			
105,585,826,131	cpu-cycles		(+/- 0.21%) (33.126)
228,595,588,080	instructions	# 0.57 inst per cycle	(+/- 0.18%) (41.693)
105,617,051,524	cache-references	# 0.58% of all cache refs	(+/- 0.48%) (41.693)
227,183,782	cache-misses		(+/- 1.04%) (41.693)
105,616,439,499	ATLB-load-misses	# 0.03% of all ATLB cache accesses	(+/- 0.15%) (33.126)
105,606,362	ATLB-load-misses		(+/- 0.83%) (41.693)
14,387,379,231	ATLB-store-misses	# 0.03% of all ATLB cache accesses	(+/- 0.11%) (33.146)
5,384,347	ATLB-store-misses		(+/- 0.97%) (33.146)
1,951,748	ATLB-load-misses		(+/- 1.54%) (33.146)
6,217,398	ATLB-load-misses	# 404.81% of all ATLB cache accesses	(+/- 1.17%) (33.146)
48,438,238,231	branch-load-misses		(+/- 0.11%) (33.146)
46,386,175	branch-load-misses		(+/- 0.37%) (33.146)
10.382% ± 0.003% seconds time elapsed (+/- 0.48%)			
Performance counter stats for 'system-wide' (10 runs): 1 Thread			
234,878,849,142	cpu-cycles		(+/- 0.91%) (33.126)
238,276,084,080	instructions	# 0.86 inst per cycle	(+/- 0.03%) (41.683)
67,203,406,151	cache-references		(+/- 0.27%) (41.683)
67,203,406,148	cache-misses	# 0.79% of all cache refs	(+/- 5.87%) (41.683)
69,438,67,522	ATLB-load-misses	# 0.79% of all ATLB cache accesses	(+/- 0.47%) (41.683)
16,687,634	ATLB-load-misses	# 0.84% of all ATLB cache accesses	(+/- 5.89%) (41.683)
15,388,756,184	ATLB-store-misses		(+/- 4.38%) (33.146)
6,059,379	ATLB-store-misses		(+/- 2.99%) (33.146)
1,362,376	ATLB-load-misses		(+/- 18.00%) (33.146)
6,070,376	ATLB-load-misses	# 414.79% of all ATLB cache accesses	(+/- 0.91%) (33.146)
49,387,399,929	branch-load-misses		(+/- 0.03%) (33.126)
182,787,731	branch-load-misses		(+/- 5.77%) (33.126)
32.462% ± 0.038% seconds time elapsed (+/- 0.18%)			
Performance counter stats for 'system-wide' (10 runs): 2 Threads			
286,487,243,985	cpu-cycles		(+/- 0.37%) (33.126)
238,198,396,080	instructions	# 0.75 inst per cycle	(+/- 0.68%) (41.683)
282,881,484,379	cache-references		(+/- 0.47%) (41.683)
514,771,371	cache-misses	# 0.84% of all cache refs	(+/- 8.72%) (41.683)
55,727,027,274	ATLB-load-misses	# 0.84% of all ATLB cache accesses	(+/- 0.48%) (41.683)
10,888,686,498	ATLB-load-misses		(+/- 0.84%) (41.683)
16,288,241,081	ATLB-store-misses	# 0.84% of all ATLB cache accesses	(+/- 1.28%) (33.185)
6,176,229	ATLB-store-misses		(+/- 1.03%) (33.185)
6,571,694	ATLB-load-misses		(+/- 3.88%) (33.146)
13,486,483	ATLB-load-misses	# 201.38% of all ATLB cache accesses	(+/- 1.14%) (33.126)
44,747,131,113	branch-load-misses		(+/- 0.49%) (33.126)
17,471,885	branch-load-misses		(+/- 0.78%) (33.185)
10.034% ± 0.196% seconds time elapsed (+/- 0.34%)			
Performance counter stats for 'system-wide' (10 runs): 4 Threads			
387,507,314,291	cpu-cycles		(+/- 0.47%) (33.126)
234,282,779,531	instructions	# 0.64 inst per cycle	(+/- 0.25%) (41.673)
155,754,362	cache-misses	# 0.75% of all cache refs	(+/- 0.88%) (41.673)
285,255,288	cache-references		(+/- 7.17%) (41.683)
69,387,538,890	ATLB-load-misses	# 0.83% of all ATLB cache accesses	(+/- 0.22%) (41.713)
23,138,882	ATLB-load-misses	# 0.03% of all ATLB cache accesses	(+/- 1.03%) (41.713)
6,397,152	ATLB-store-misses		(+/- 0.84%) (33.185)
6,364,056	ATLB-store-misses		(+/- 0.93%) (33.185)
1,469,887	ATLB-load-misses		(+/- 0.88%) (41.683)
1,561,255	ATLB-load-misses	# 108.42% of all ATLB cache accesses	(+/- 1.03%) (33.126)
99,289,497	branch-load-misses		(+/- 0.28%) (33.126)
11,008,078	branch-load-misses		(+/- 0.82%) (33.173)
21.775% ± 0.092% seconds time elapsed (+/- 0.42%)			
Performance counter stats for 'system-wide' (10 runs): 12 Threads			
619,309,539,438	cpu-cycles		(+/- 0.81%) (33.126)
502,682,844,254	instructions	# 0.44 inst per cycle	(+/- 0.48%) (41.683)
497,897,211,980	cache-references		(+/- 0.84%) (41.683)
439,627,838	cache-misses	# 0.59% of all cache refs	(+/- 1.08%) (41.683)
43,492,437	ATLB-load-misses		(+/- 0.48%) (41.693)
43,394,401,837	ATLB-load-misses	# 0.005% of all ATLB cache accesses	(+/- 0.83%) (41.693)
23,284,547	ATLB-store-misses		(+/- 0.83%) (33.185)
15,388,401,871	ATLB-store-misses		(+/- 1.18%) (33.126)
9,051,408	ATLB-store-misses		(+/- 0.17%) (33.126)
2,274,766	ATLB-load-misses	# 218.08% of all ATLB cache accesses	(+/- 2.23%) (33.185)
8,866,759,894	branch-load-misses		(+/- 0.58%) (33.185)
99,309,429	branch-load-misses		(+/- 1.25%) (33.185)
20.336% ± 0.074% seconds time elapsed (+/- 0.85%)			
Performance counter stats for 'system-wide' (10 runs): 8 Threads			
612,837,053,540	cpu-cycles		(+/- 1.08%) (33.126)
234,535,141,080	instructions	# 0.54 inst per cycle	(+/- 2.37%) (41.693)
188,484,782,938	cache-references		(+/- 0.34%) (41.683)
188,484,782,938	cache-misses	# 0.17% of all cache refs	(+/- 10.68%) (41.673)
5,867,423,784	ATLB-load-misses		(+/- 1.93%) (33.185)
789,814,088	ATLB-load-misses	# 1.05% of all ATLB cache accesses	(+/- 0.25%) (41.693)
10,388,401,871	ATLB-store-misses		(+/- 3.88%) (33.185)
48,438,238,231	ATLB-store-misses		(+/- 0.11%) (33.185)
17,437,718	ATLB-load-misses		(+/- 3.19%) (33.146)
42,432,816	ATLB-load-misses	# 101.05% of all ATLB cache accesses	(+/- 0.24%) (33.126)
48,438,238,231	branch-load-misses		(+/- 1.19%) (33.126)
212,957,527	branch-load-misses		(+/- 5.75%) (33.126)
30.871% ± 0.337% seconds el (+/- 0.86%)			