

قطعات و تراشه های استفاده شده:

```
# BELS           : 499
#   GND          : 1
#   INV          : 1
#   LUT1         : 60
#   LUT2         : 45
#   LUT3         : 2
#   LUT4         : 85
#   LUT5         : 3
#   LUT6         : 101
#   MUXCY        : 130
#   VCC          : 1
#   XORCY        : 70
# FlipFlops/Latches : 76
#   FDC          : 70
#   FDCE         : 6
# Clock Buffers   : 1
#   BUFGP        : 1
# IO Buffers      : 99
#   IBUF         : 97
#   OBUF         : 2
```

بررسی میزان بهره برداری:

#### Slice Logic Utilization:

```
Number of Slice Registers:    76 out of 18224   0%
Number of Slice LUTs:        297 out of 9112    3%
  Number used as Logic:      297 out of 9112    3%
```

#### Slice Logic Distribution:

```
Number of LUT Flip Flop pairs used: 299
  Number with an unused Flip Flop: 223 out of 299 74%
  Number with an unused LUT:       2 out of 299 0%
  Number of fully used LUT-FF pairs: 74 out of 299 24%
  Number of unique control sets:    3
```

#### IO Utilization:

```
Number of IOs:                100
Number of bonded IOBs:        100 out of 232 43%
```

#### Specific Feature Utilization:

```
Number of BUFG/BUFGCTRLs:     1 out of 16 6%
```

Speed Grade: -3

Minimum period: 3.254ns (Maximum Frequency: 307.281MHz)

Minimum input arrival time before clock: 11.776ns

Maximum output required time after clock: 3.668ns

Maximum combinational path delay: No path found

حداکثر فرکانس کلاک: 307.281MHz