

قطعات و تراشه های استفاده شده:

```
# BELS           : 76
#   GND          : 1
#   INV          : 1
#   LUT2         : 4
#   LUT3         : 10
#   LUT4         : 7
#   LUT5         : 19
#   LUT6         : 30
#   MUXF7        : 4
# FlipFlops/Latches : 38
#   FDC          : 38
# Clock Buffers   : 1
#   BUFGP        : 1
# IO Buffers      : 6
#   IBUF         : 2
#   OBUF         : 4
```

بررسی میزان بهره برداری:

#### Slice Logic Utilization:

```
Number of Slice Registers:    38 out of 18224    0%
Number of Slice LUTs:        71 out of  9112    0%
Number used as Logic:        71 out of  9112    0%
```

#### Slice Logic Distribution:

```
Number of LUT Flip Flop pairs used:  102
Number with an unused Flip Flop:    64 out of  102  62%
Number with an unused LUT:          31 out of  102  30%
Number of fully used LUT-FF pairs:   7 out of  102   6%
Number of unique control sets:       1
```

#### IO Utilization:

```
Number of IOs:                7
Number of bonded IOBs:         7 out of  232   3%
```

#### Specific Feature Utilization:

```
Number of BUFG/BUFGCTRLs:       1 out of   16   6%
```

گزارش تایمینگ:

Speed Grade: -3

Minimum period: 6.227ns (Maximum Frequency: 160.578MHz)

9826653

Minimum input arrival time before clock: 6.347ns

Maximum output required time after clock: 3.634ns

Maximum combinational path delay: No path found

حداکثر فرکانس کلاک: 160.578MHz