

قطعات و تراشه های استفاده شده:

```
# BELS           : 1235
#   LUT2         : 289
#   LUT3         : 51
#   LUT4         : 131
#   LUT5         : 133
#   LUT6         : 630
#   MUXF7        : 1
# FlipFlops/Latches : 4
#   FDC          : 4
# Clock Buffers   : 1
#   BUFGP        : 1
# IO Buffers      : 549
#   IBUF         : 545
#   OBUF         : 4
```

بررسی میزان بهره برداری:

#### Slice Logic Utilization:

```
Number of Slice LUTs:      1234 out of 9112  13%
Number used as Logic:      1234 out of 9112  13%
```

#### Slice Logic Distribution:

```
Number of LUT Flip Flop pairs used: 1234
Number with an unused Flip Flop: 1234 out of 1234  100%
Number with an unused LUT: 0 out of 1234  0%
Number of fully used LUT-FF pairs: 0 out of 1234  0%
Number of unique control sets: 1
```

#### IO Utilization:

```
Number of IOs: 550
Number of bonded IOBs: 550 out of 232  237% (*)
IOB Flip Flops/Latches: 4
```

#### Specific Feature Utilization:

```
Number of BUFG/BUFGCTRLs: 1 out of 16  6%
```

گزارش تایمینگ:

Speed Grade: -3

Minimum period: No path found

Minimum input arrival time before clock: 85.831ns

Maximum output required time after clock: 3.597ns

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Maximum combinational path delay: No path found