This README file has been written in order to clarify the testbench patterns:

CR=CanRestore CS=CanSave CWP=CurrentWindowPointer
SWP=SavedWindowPointer

CALL SUB0	CS=2	CR=1		CWP=0 SWP=0
CALL SUB1	CS=1	CR=2		CWP=1 SWP=0
CALL SUB2	CS=0->1	CR=3->2	SPILL	CWP=2 SWP=1
CALL SUB3	CS=0->1	CR=3->2	SPILL	CWP=0 SWP=2
RET SUB3	CS=2	CR=1		CWP=2 SWP=2
CALL SUB3	CS=1	CR=2		CWP=0 SWP=2
RET SUB3	CS=2	CR=1		CWP=2 SWP=2
RET SUB2	CS=3->2	CR=0->1	FILL	CWP=1 SWP=1
RET SUB1	CS=3->2	CR=0->1	FILL	CWP=0 SWP=0

Furthermore, all the signals related to the register-file's read/write cycles have not been defined, since the register-file module has been already tested in the previous section (3.1.1).