

Assignment no 2

Experiment No.1

OBJECTIVE:

Write a program to add two hexadecimal numbers.

Statement:

add two number 05H and 13H and display result in output port 18H.

Steps:

- The MVI command moves the 05H and 13H to their respective destination A and B.
- Store the added number (new number) and display in the output port 18H.
- Terminate the program.

Program: -

```
1  MVI A, 05H
2  MVI B, 13H
3  ADD B
4  OUT 18H
5  HLT
```

Register: -

Registers			Flag	
A	18		S	0
BC	13	00	Z	0
DE	00	00	AC	0
HL	00	00	P	1
PSW	00	00	C	0
PC	42	08		
SP	FF	FF		
Int-Reg	00			

Output port:-

Address (Hex)	Address	Data
00	0	0
01	1	0
02	2	0
03	3	0
04	4	0
05	5	0
06	6	0
07	7	0
08	8	0
09	9	0
0A	10	0

Line No	Assembler Message
0	Program assembled successfully

Experiment 2

Objectives

Write a program to add two 8-bit hexadecimal numbers.

Statement:

Add numbers from memory location 2050H & 2051H and store result in memory location 2055H.

Steps:

- Load the data from the memory location 2051H
- Transfer the data
- Load to accumulator
- Store in 2055H memory location

Program:

```
1  LDA 2051H
2  MOV B,A
3  LDA 2050H
4  ADD B
5  STA 2055H
6  HLT
```

Register:

Registers			Flag	
A	00		S	0
BC	00	00	Z	1
DE	00	00	AC	0
HL	00	00	P	1
PSW	00	00	C	0
PC	42	0C		
SP	FF	FF		
Int-Reg	00			

Output port:

Address (Hex)	Address	Data
00	0	0
01	1	0
02	2	0
03	3	0
04	4	0
05	5	0
06	6	0
07	7	0
08	8	0
09	9	0
0A	10	0

Line No	Assembler Message
0	Program assembled successfully

Experiment 3

Objectives

Subtract numbers 25H & 12H and display result in output port o01H.

Steps:

- Transfer the data from memory location 25H and 12H respectively in A and B.
- Subtract
- Store the subtracted number (new number) in 01H port.

Program:

```
1  MVI A, 25H
2  MVI B, 12H
3  SUB B
4  OUT 01H
5  HLT
```

Register:

Registers			Flag	
A	13		S	0
BC	12	00	Z	0
DE	00	00	AC	0
HL	00	00	P	0
PSW	00	00	C	0
PC	42	08		
SP	FF	FF		
Int-Reg	00			

Output port:

Address (Hex)	Address	Data
00	0	0
01	1	19
02	2	0
03	3	0
04	4	0
05	5	0
06	6	0
07	7	0
08	8	0
09	9	0
0A	10	0

Line No	Assembler Message
0	Program assembled successfully

Experiment 4

Objectives

Write a program to subtract two 8-bit numbers.

Statement:

Subtract numbers from memory location 2050H & 2051H and store result in memory location 2055H.

Steps:

- Same as Experiment 1 (only difference is the addition and subtraction).

Program:

```
1 LDA 2051H
2 MOV B,A
3 LDA 2050H
4 SUB B
5 STA 2055H
6 HLT
```

Register:

Registers			Flag
A	00		S 0
BC	00	00	Z 1
DE	00	00	
HL	00	00	AC 0
PSW	00	00	P 1
PC	42	0C	
SP	FF	FF	C 0
Int-Reg	00		

Output port:

Start		
Address (Hex)	Address	Data
00	0	0
01	1	0
02	2	0
03	3	0
04	4	0
05	5	0
06	6	0
07	7	0
08	8	0
09	9	0
0A	10	0
Line No	Assembler Message	
0	Program assembled successfully	

Experiment 5

Objectives

Write a program to find 1's complement of a number.

Statement:

Input number from memory location 2013H and store result in memory location 2055H.

Steps:

- Transfer data from memory location 2013H to Accumulator.
- Complement the contents in the accumulator.
- Store in 2052H memory location.

Program:

```
1  LDA 2013H
2  CMA
3  STA 2052H
4  HLT
```

Register:

Registers			Flag
A	FF		S 0
BC	00	00	Z 0
DE	00	00	AC 0
HL	00	00	P 0
PSW	00	00	C 0
PC	42	08	
SP	FF	FF	
Int-Reg	00		

Output port:

Start		23h
Address (Hex)	Address	Data
23	35	0
24	36	0
25	37	0
26	38	0
27	39	0
28	40	0
29	41	0
2A	42	0
2B	43	0
2C	44	0
2D	45	0
Line No	Assembler Message	
0	Program assembled successfully	