Microprocessor Assignment - 1

Obsepare a seport on 8085 microprocessor orchitecture & its functional units.

+ Microprocessor is a controlling unit of a micro-computer, tobricated of a small chip capable of performing ALU(Asithmetic logic other connected to it.

The architecture of 8085 microprocessor ove:

O ALU (Azithmetic logic unit)

2) Register array

@ Register array letter such as: B, C, D, E, H, L, IX & IY. These registers are used to store data & address temporarily during the execution.

3 control onit -) It provides the necessary timing & control signals to all the operations. It controls the flow of data between the microprocessor & memory & peripherals.

A I/o (Input & output)

-> The input section transfer data & instructions
in binary from the outside world to the
microprocessor. Eg -> Keyboard, scanner, etc.

The output section transfers data to output
devices such as LED, Printer, etc.

5) Memory

The stores binary information such as instruction

E data, & provides the information. When

executing, the data is read from memory

E performs in the ALU & either displays

it in the outer section or stores it.

6 System bus

→ It 9's a communication path between the microprocessor & peripherals.

The functional units of 8085 microprocessor

- 1) Accomolator
- -> It is an 8-bit register used to perform arithmetic, logical, I/o & load/store operation It is connected to internal data bus & ALU.
- 2) ALU

 -> It performs all the arithmetic e logical
 operation.

3 General purpose register -> There are 6 general purpose register i.e. B, C, D, E, H & L. Each register con hold 8-5:+ data. These register on work in pair to hold 16-bit data & their combination is like B-C, D-E E H-L. 4) Program counter -> A 16-6:+ register that stores the address
of the next executable instruction.

(5) stack pointer -> A 16-15+ register i.e. always incremented/

decremented by 2 dwring push & pop operation.

6) Flog register.

-> It is an. 8-15t register having five 1-15t flip-flop which holds either Oor I depending upon the result stored. The five 1-bit flip-flops are:

O egn(s)

2 zero(z) (3) Aux919ory comy (AC)

(a) Parity (P)

The Lits position les given below: D7 D6 D5 D4 D3 D2 DL D0

S Z Z AC AC CY

· Blown clies

- Temporary register

 > It is a 16-bit register which holds

 the temporary data of the ALU.
- 8) Instructions register & decoder > It is on 8-bit segister where instruction is fetched from memory & the decoder decodes the information present in the instruction register.
 - a Timing & control whit + It provides firing & control signal to the The fining & control signals are:
 - @ control signal: READY, ED', WR', ALT

 B status "1 : SO, SI, IO/M

 C DMA "1 : HOID, & HILDA

 - @ Reset " : RESET IN, RESET OUT
 - 1 Intersept control -> It controls the interrupt signals during the process. When an interrupt signal is detected, the microprocessor shifts the control from the main program to the process
 of Incoming request.

 There are 5 interupt signals: Trap INITE,

 RST 7.5, 257 6.5, 257 5.5. When microprocessor detects intropt signal it sends INTA to the Pempherals.

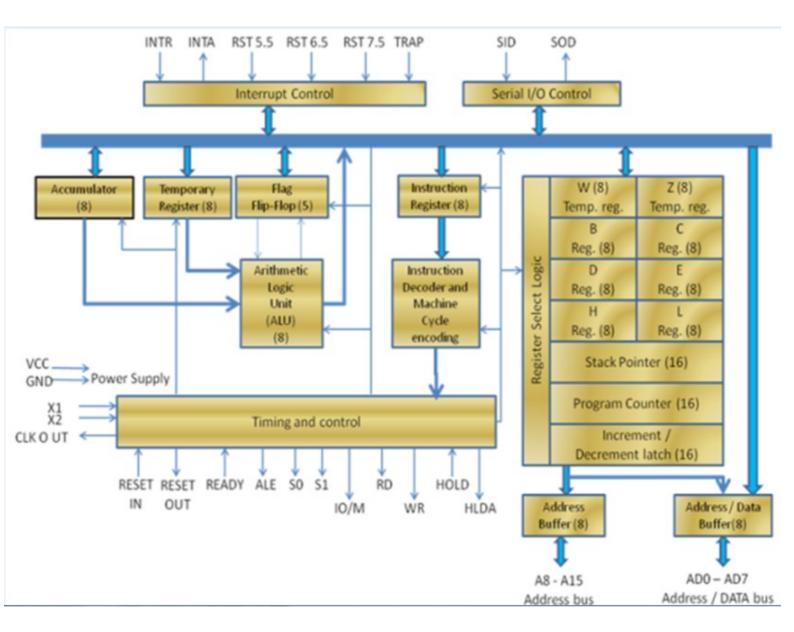


Fig: Block diagram of 8085 Microprocessor.