

Computer Assignment #3-Phase 1 – SystemC Simulation

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In this assignment, a circuit with assign statements in systemC is created and is tested with a testbench.

SC_FUNCTIONAL:

An SC_MODULE is created and calculations for 4 expressions are done with 4 SC_THREAD.

SC_FUNCTIONALTB:

SC_FUNCTIONAL is instantiated here. Inputs are passed to it. Inputs are valued with a SC_THREAD. Also output is printed in terminal with a SC_THREAD.

CA3:

SC_FUNCTIONALTB is instantiated here and signals are traced with sc_trace_file.

With gtkWave, waveforms can be seen:



```
SystemC 2.3.2-Accellera --- May  2 2021 23:04:08
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ALL RIGHTS RESERVED
o is X at 0 s
Info: (I702) default timescale unit used for tracing: 1 ps (Assignment.vcd)
o is 0 at 17 ns
o is 1 at 59 ns
```