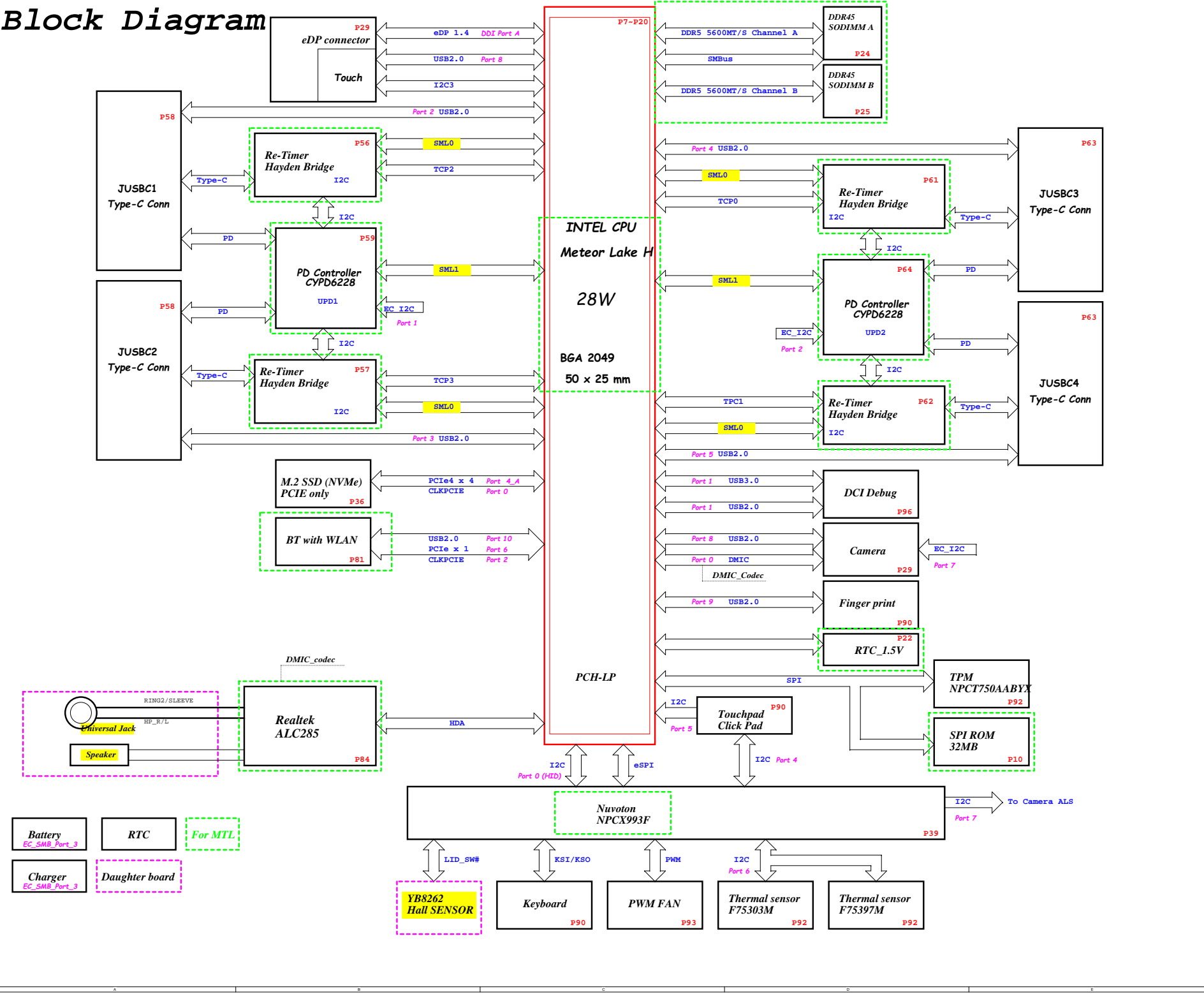
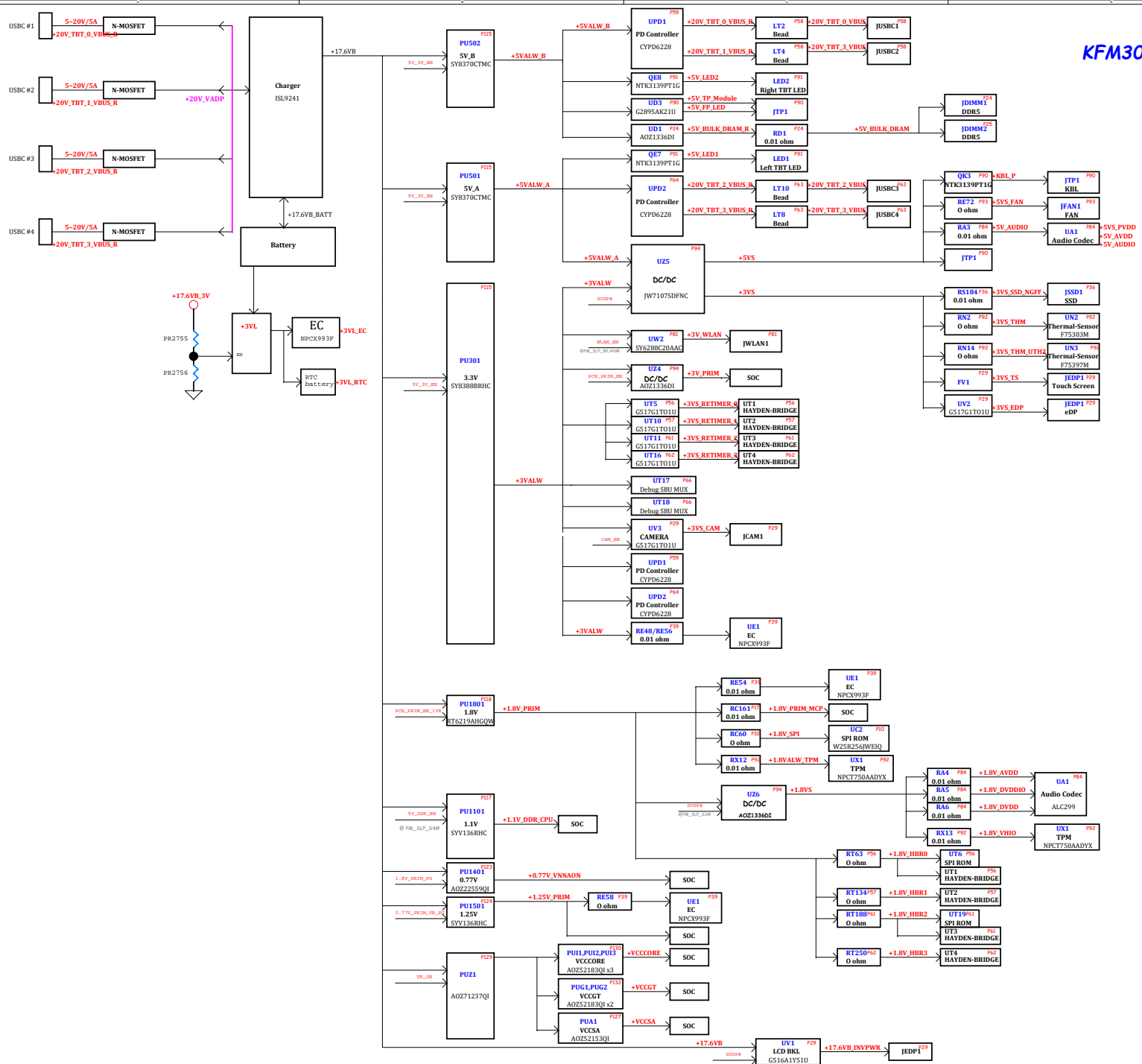
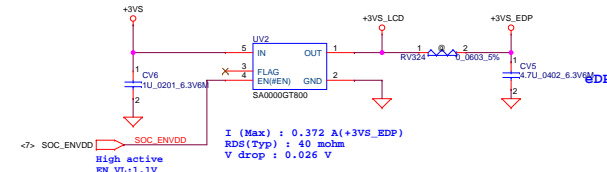
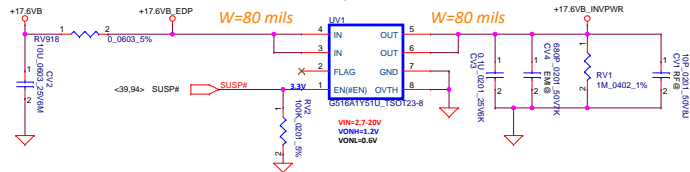


Block Diagram

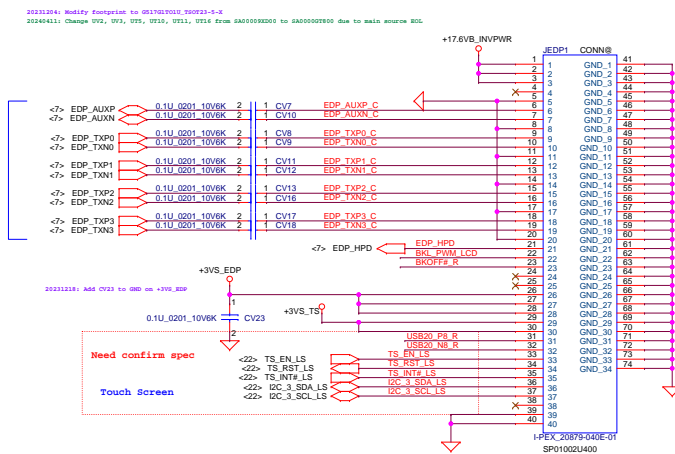




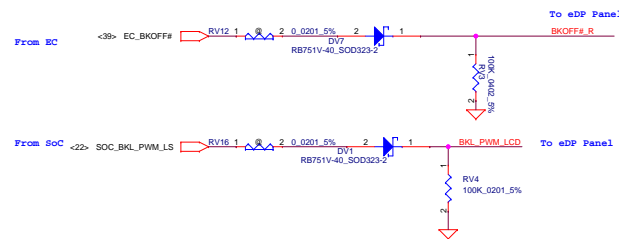
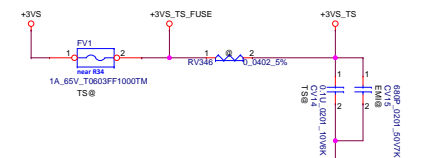
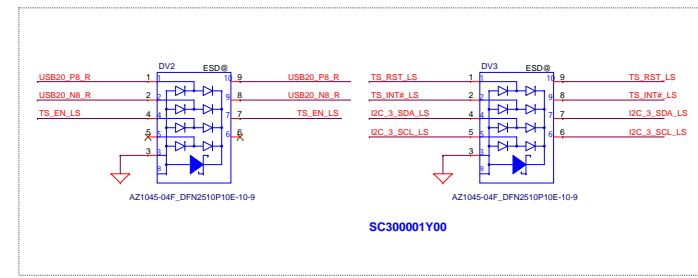
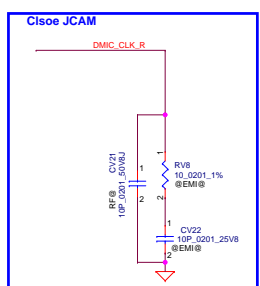
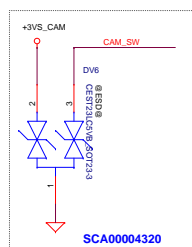
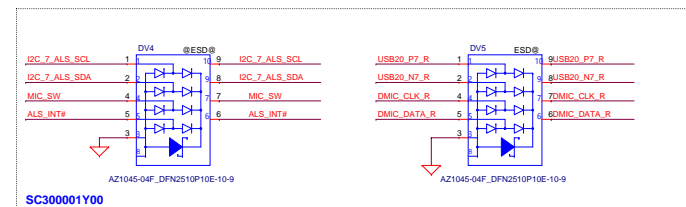
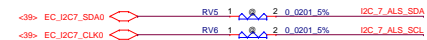
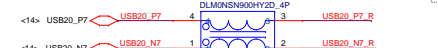
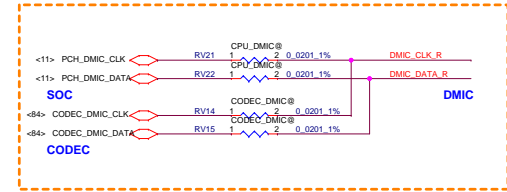
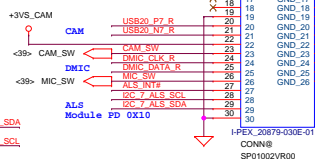
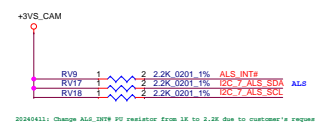
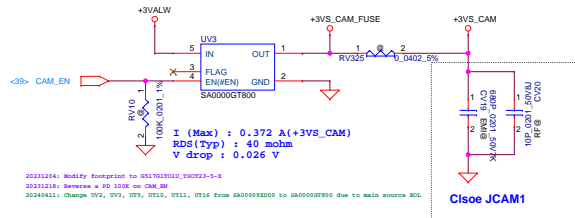
LCD backlight power control



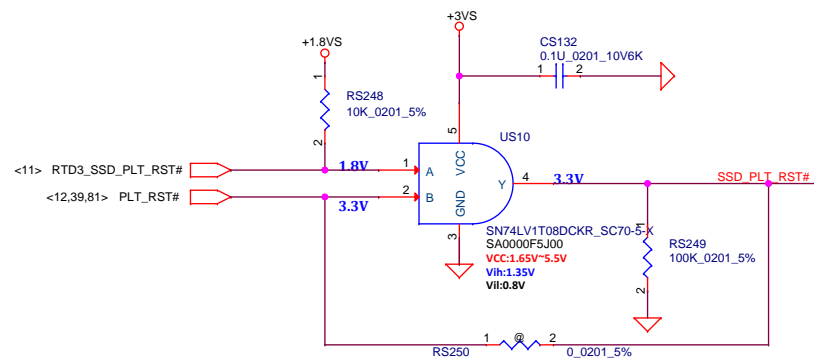
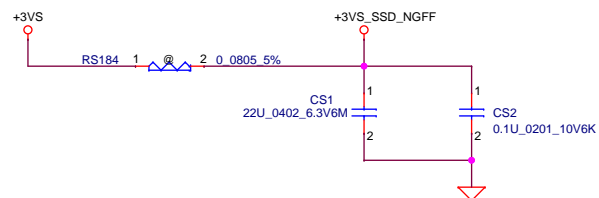
eDP Conn.



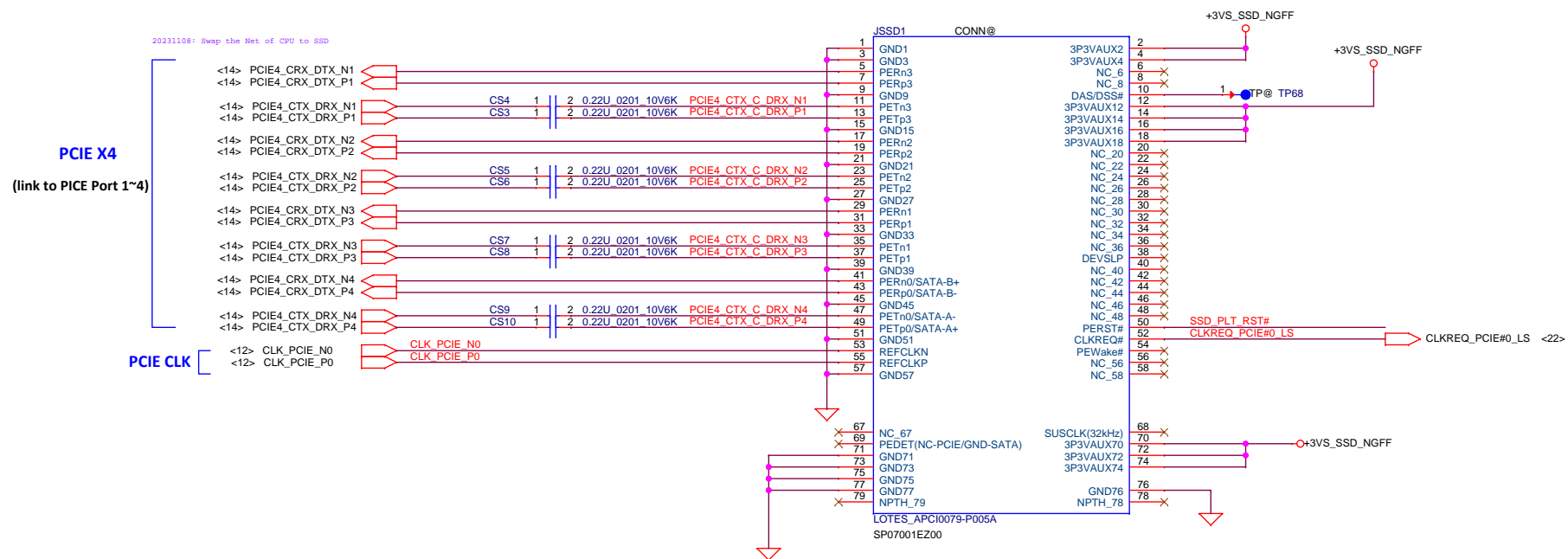
Touch Screen

**DMIC / CAM**

NGFF SSD (KEY M)
PCIE Gen4

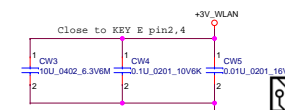
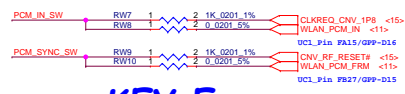
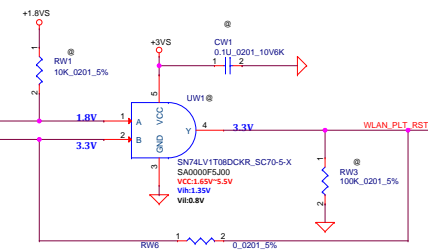
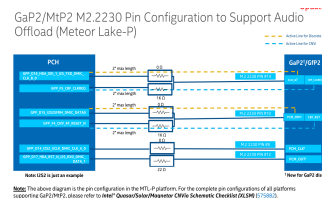
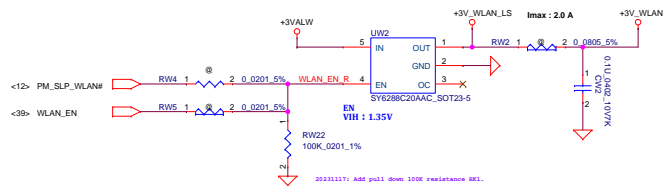


KEY M

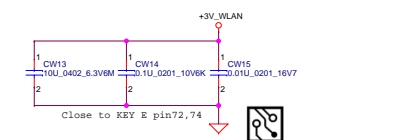
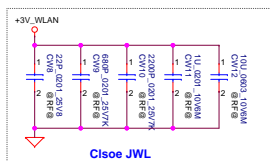
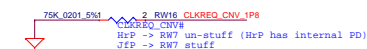
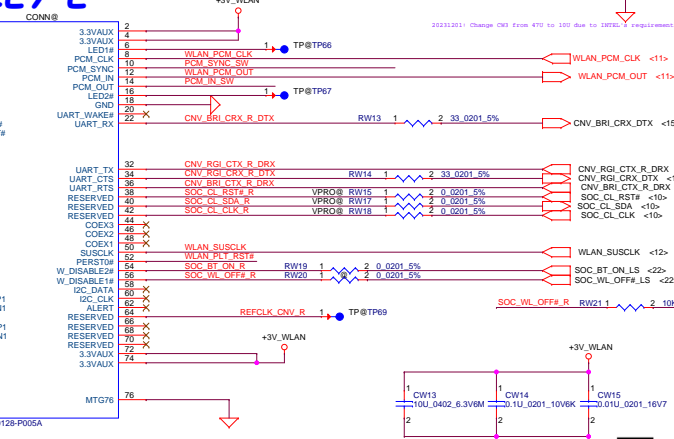
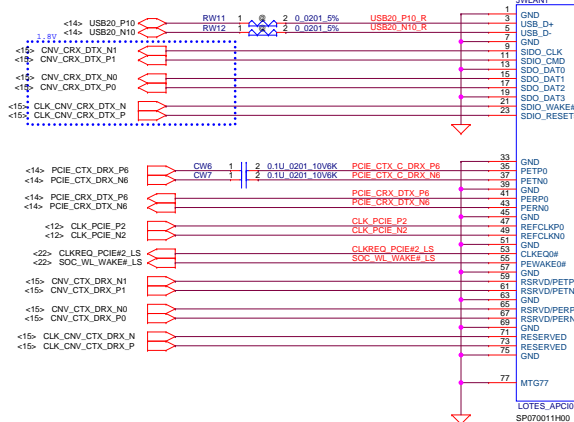
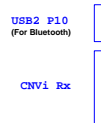


Main Func = NGFF WLAN / BT (Key E)

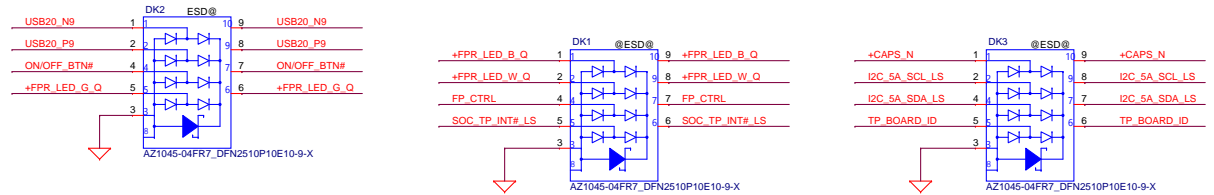
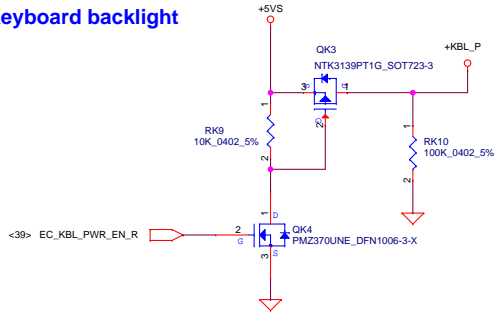
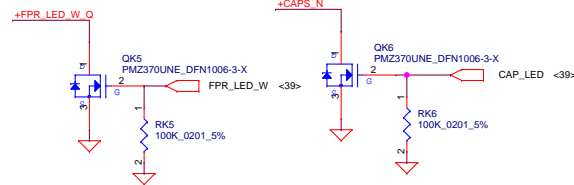
[PCIE+USB/CNVi]



Layout Note:
RC150,151 to be placed <=1000mils near CPU
RW13,14 to be placed <=1000mils near connector.



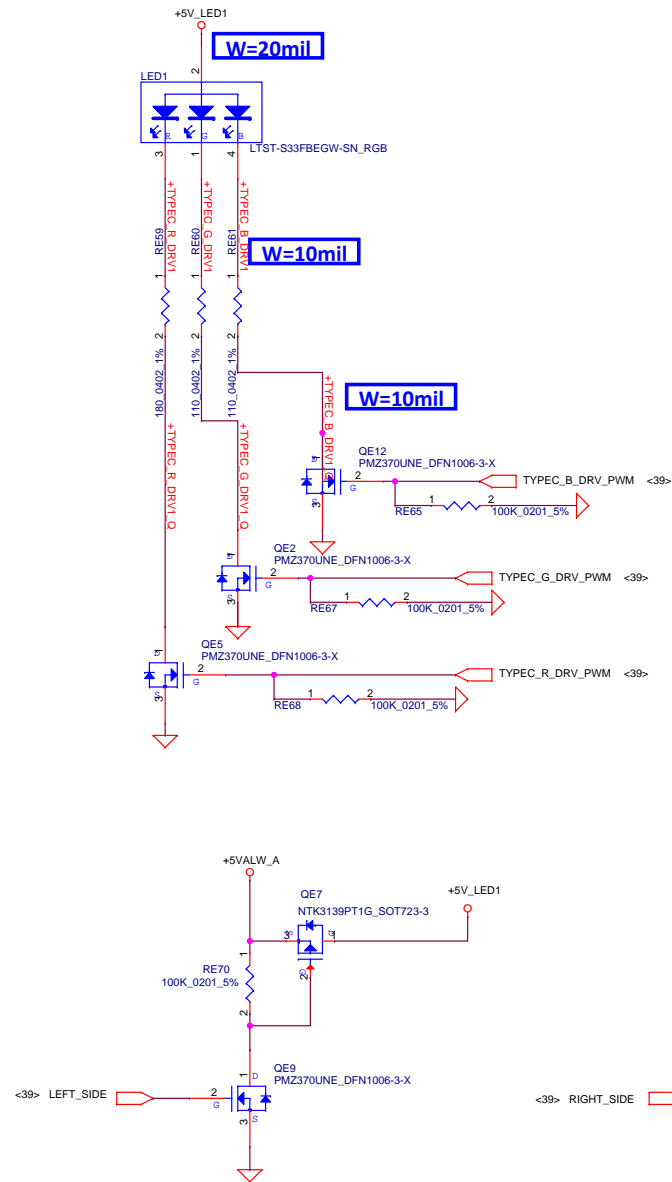
```
<39> KSI[0..7]    >>>
<39> KSO0[0..9]   <<<
<39> KSO[10..15] <<<
```



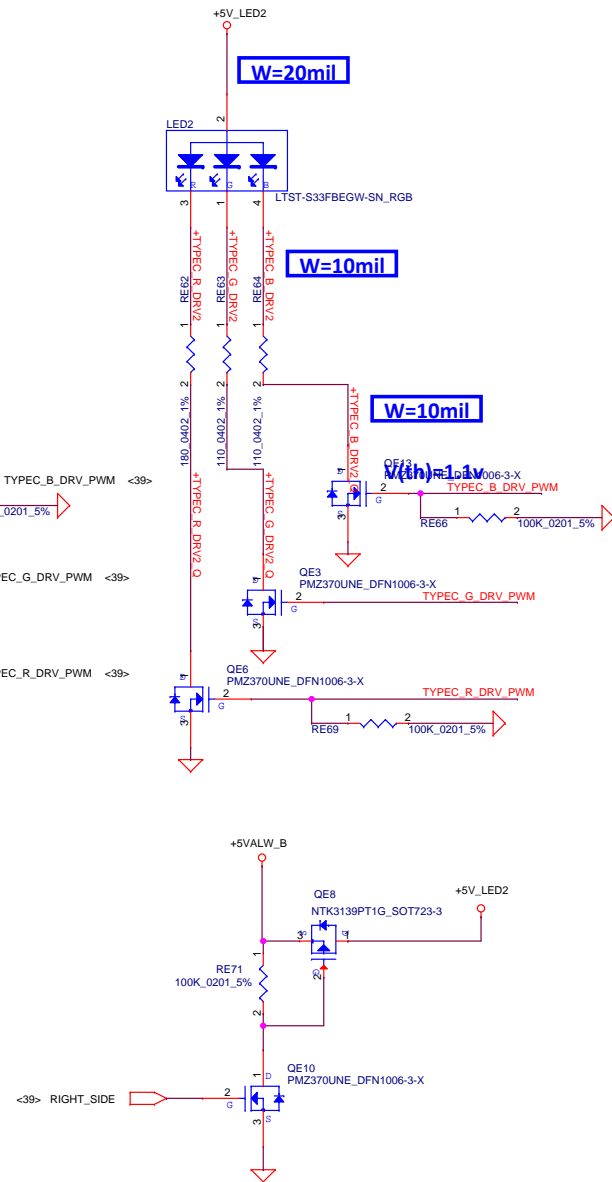
SC300001Y00

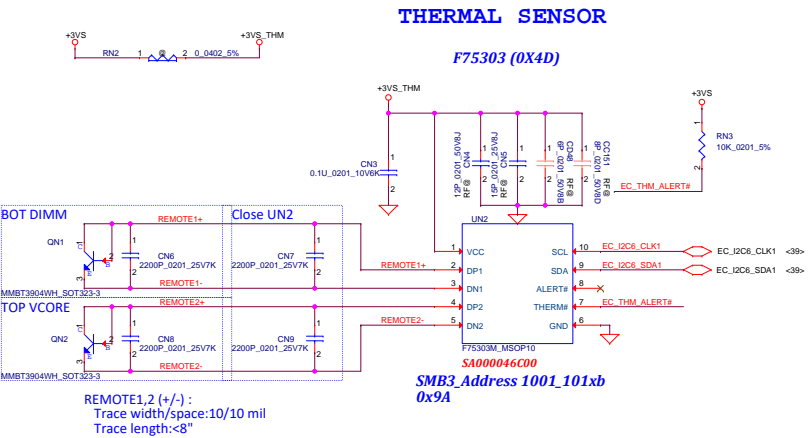
Main Func = LED

SC50000EI00
Left TBT LED

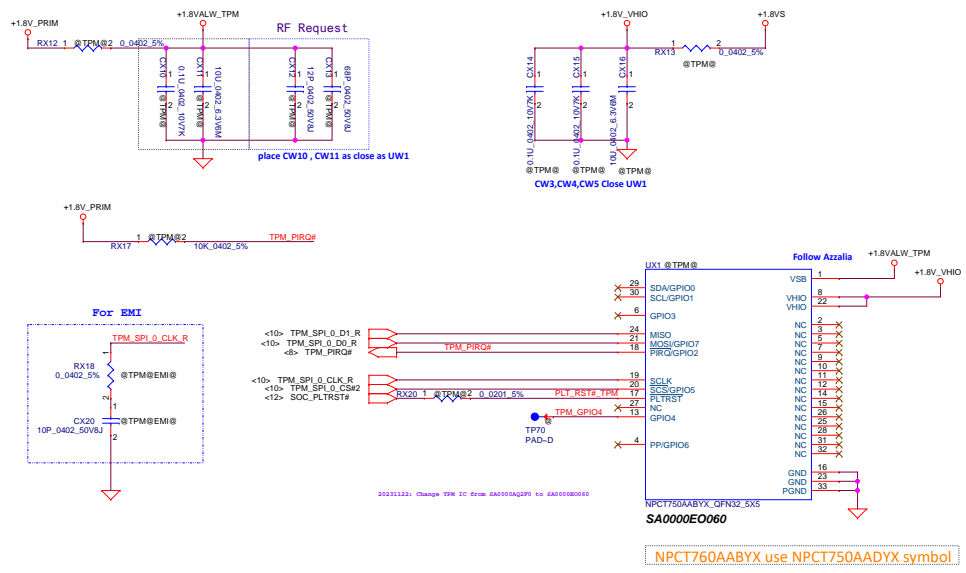


Right TBT LED
SC50000EI00

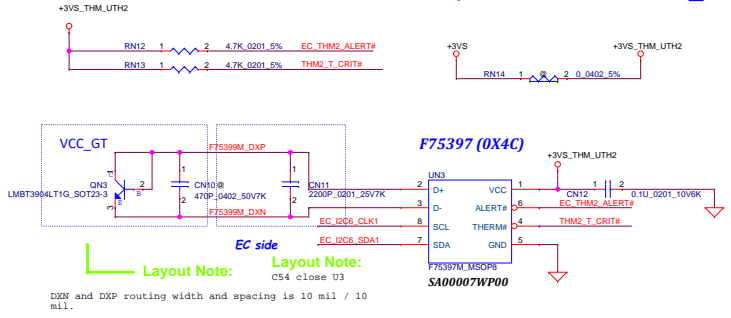




TPM will change to NPCT750AADYX SA0000AQ2F0

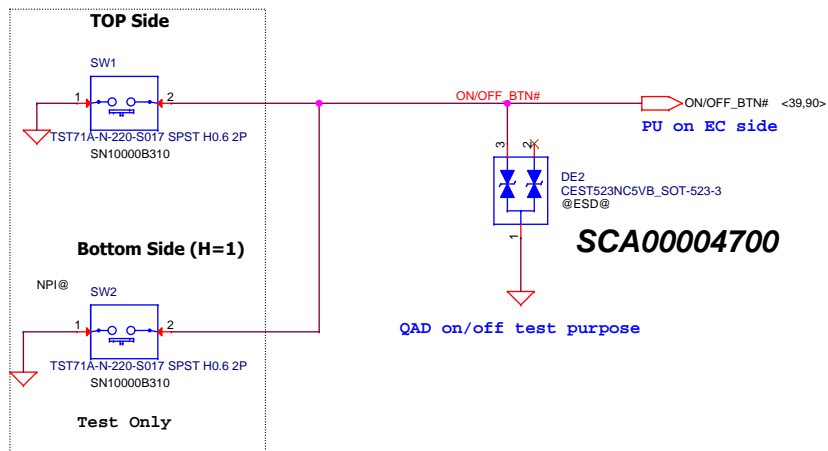


THERMAL SENSOR (Reserve for VCC_GT)

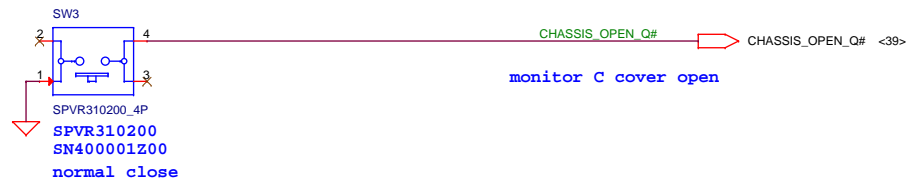
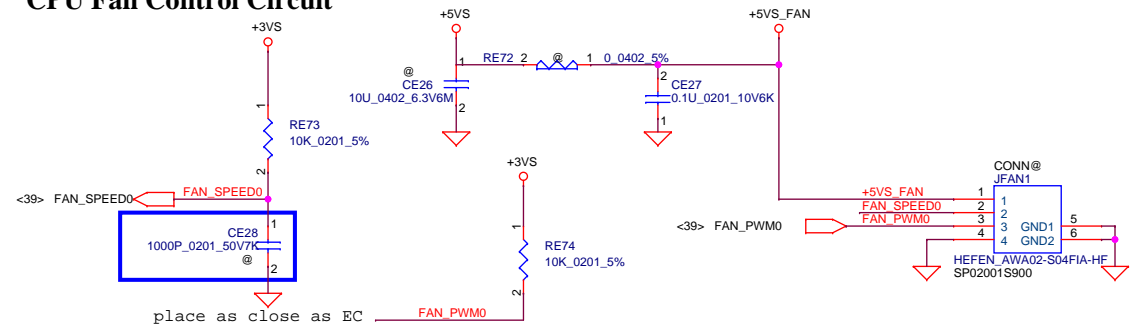


Power Button

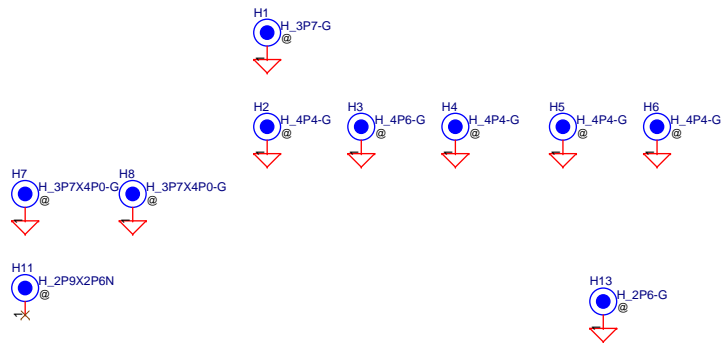
ON / OFF switch



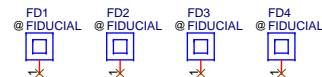
CPU Fan Control Circuit



SCREW



Fiducial Mark



Stand Off (Fan)

Stand Off (SSD)

