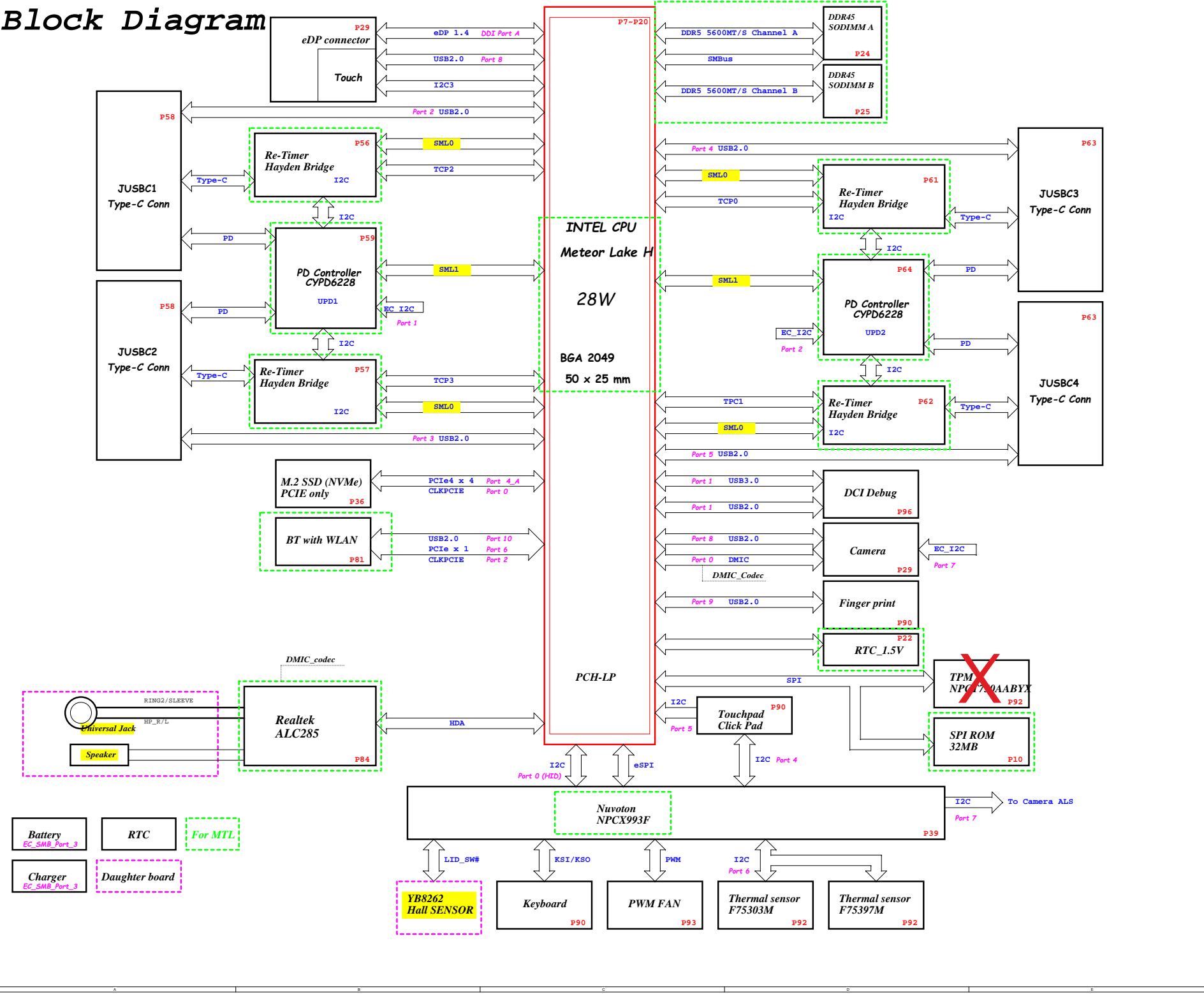
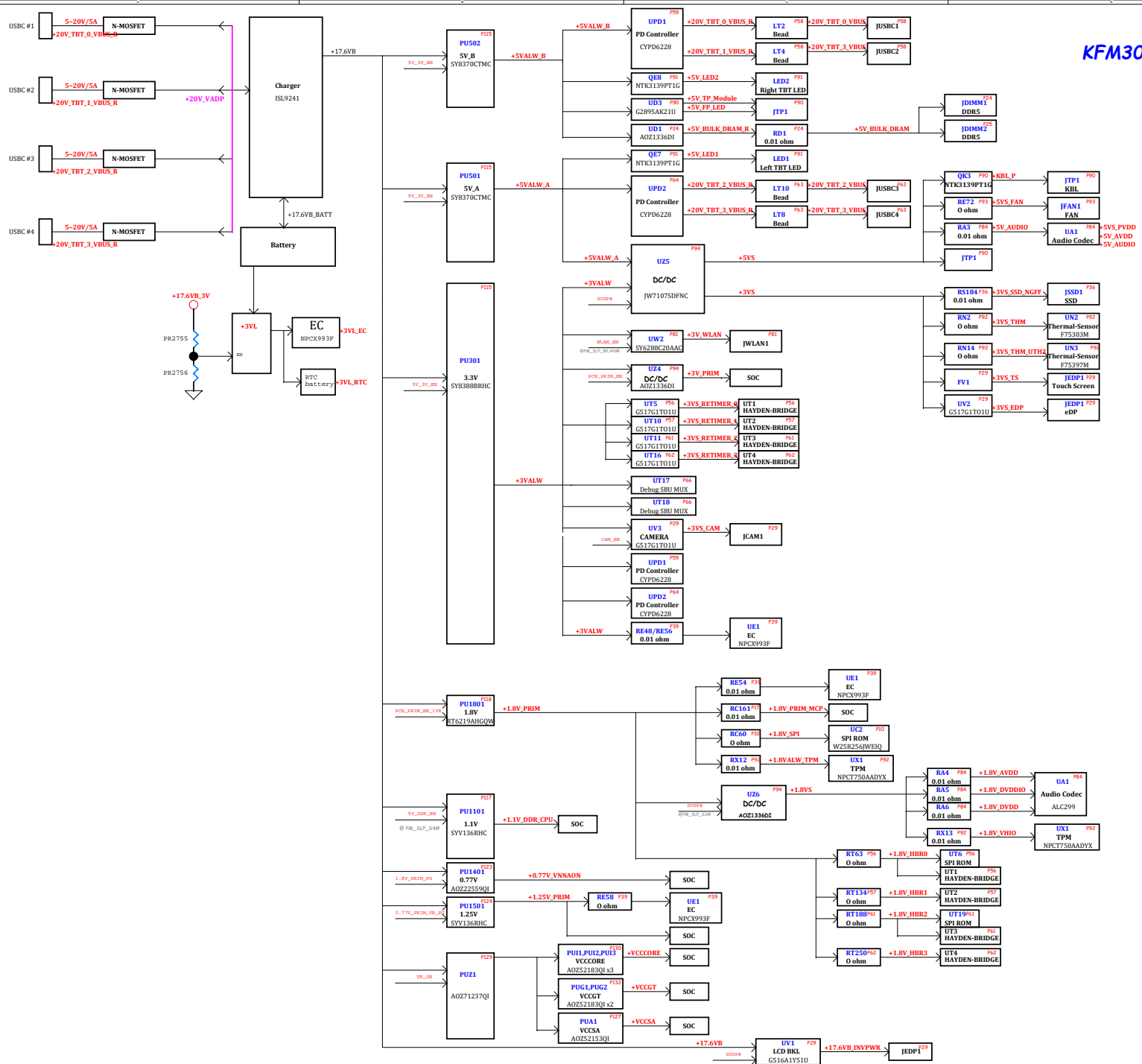


# Block Diagram

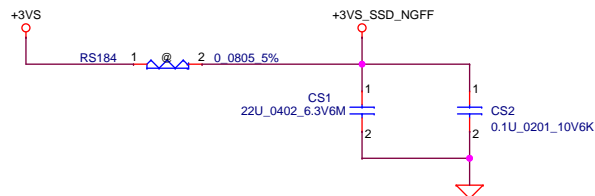




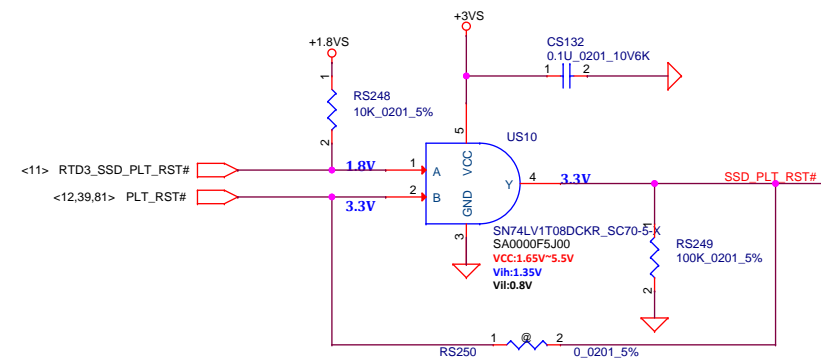
KFM30-Power Map MTL DDR5 (28W)



# NGFF SSD (KEY M) PCIe Gen4



## KEY M



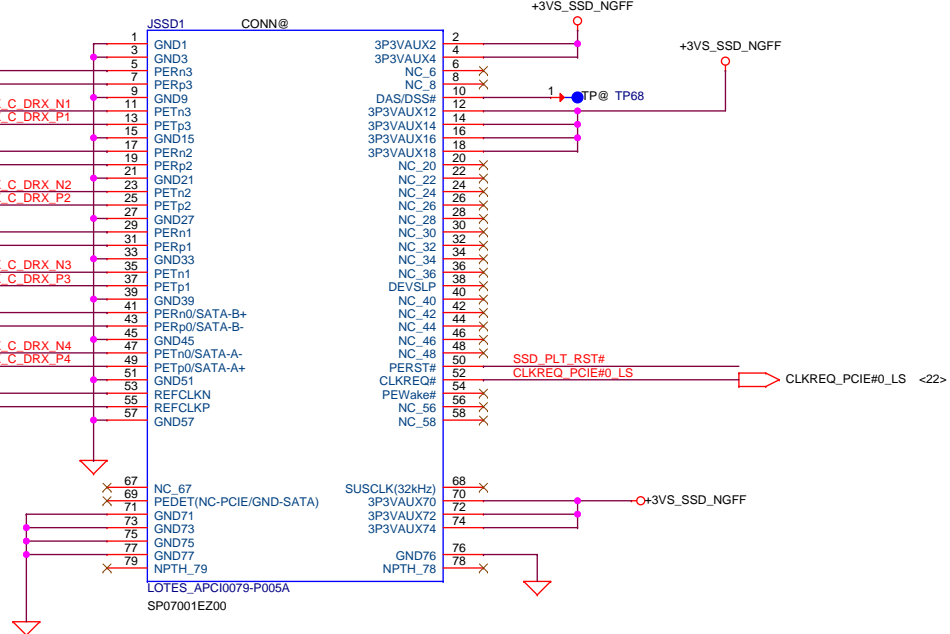
PCIe X4  
(link to PICE Port 1~4)

PCIe CLK

20231108: Swap the Net of CPU to SSD

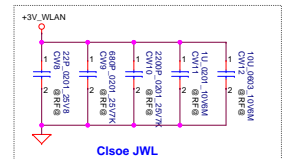
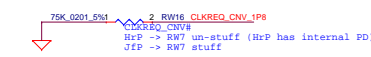
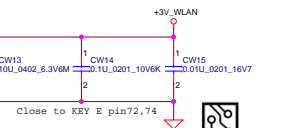
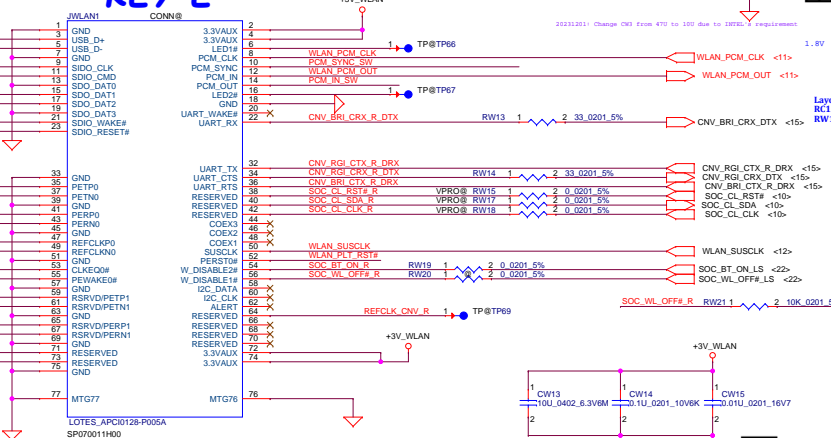
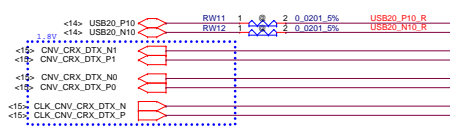
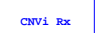
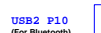
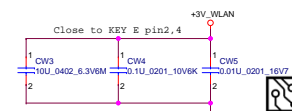
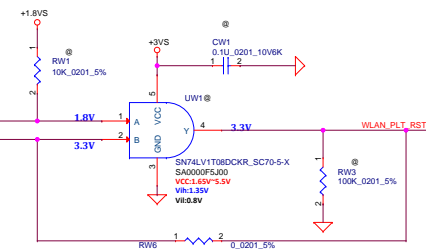
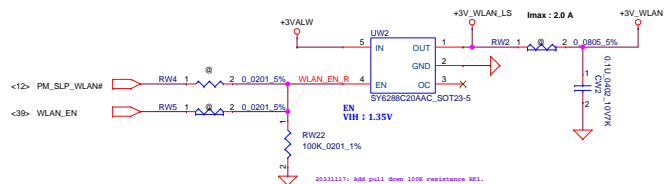
- <14> PCIe4\_CRX\_DTX\_N1
- <14> PCIe4\_CRX\_DTX\_P1
- <14> PCIe4\_CTX\_DRX\_N1
- <14> PCIe4\_CTX\_DRX\_P1
- <14> PCIe4\_CRX\_DTX\_N2
- <14> PCIe4\_CRX\_DTX\_P2
- <14> PCIe4\_CTX\_DRX\_N2
- <14> PCIe4\_CTX\_DRX\_P2
- <14> PCIe4\_CRX\_DTX\_N3
- <14> PCIe4\_CRX\_DTX\_P3
- <14> PCIe4\_CTX\_DRX\_N3
- <14> PCIe4\_CTX\_DRX\_P3
- <14> PCIe4\_CRX\_DTX\_N4
- <14> PCIe4\_CRX\_DTX\_P4
- <14> PCIe4\_CTX\_DRX\_N4
- <14> PCIe4\_CTX\_DRX\_P4

- <12> CLK\_PCIE\_N0
- <12> CLK\_PCIE\_P0

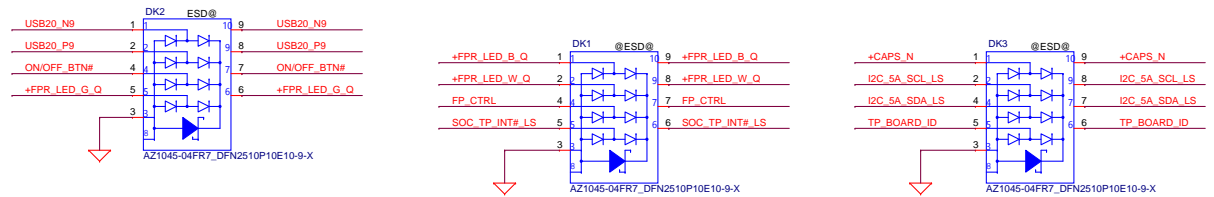
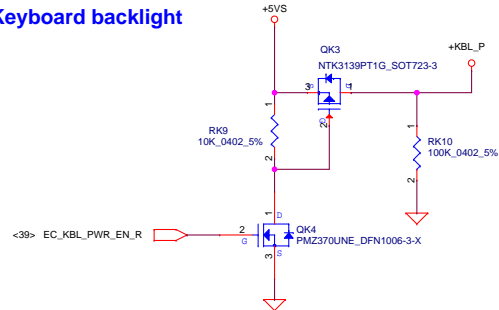
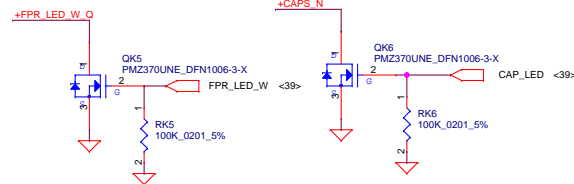


**Main Func = NGFF WLAN / BT (Key E)**

**[PCIe+USB/CNVi]**



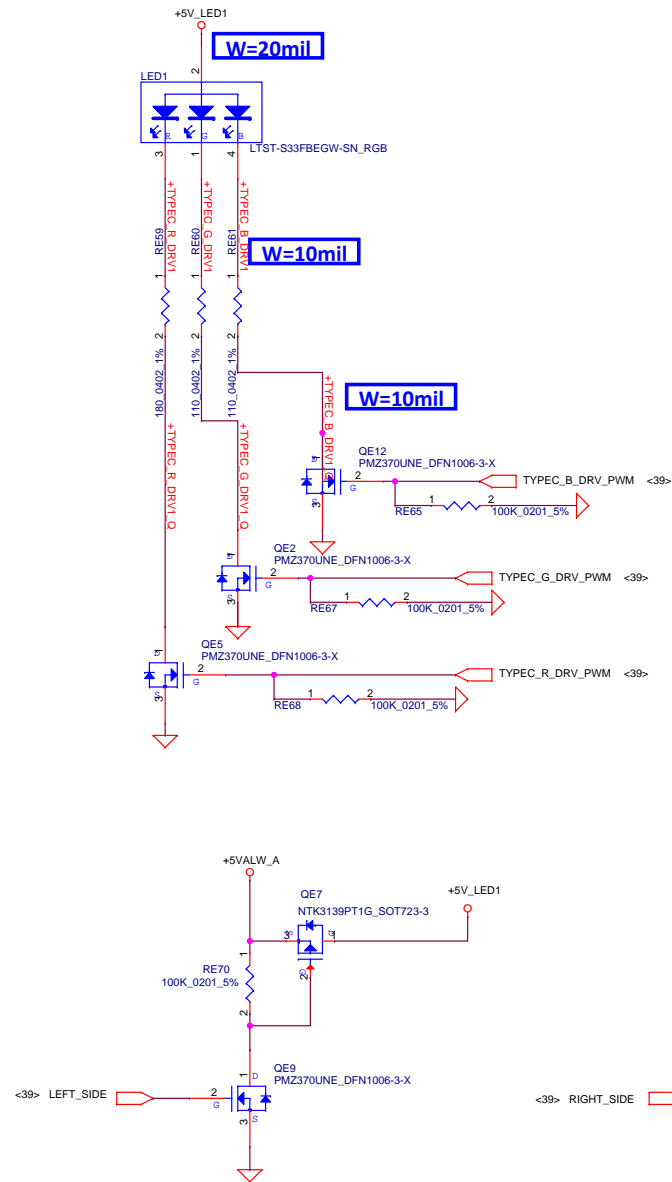
```
<39> KSI[0..7]    >>>
<39> KSO0[0..9]   <<<
<39> KSO[10..15] <<<
```



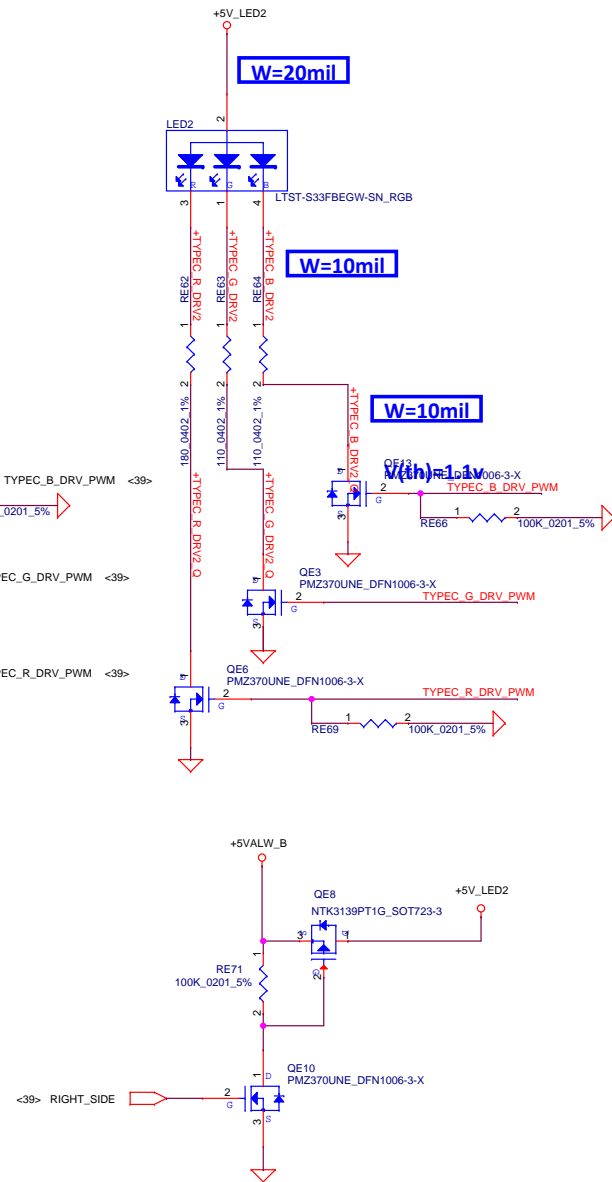
**SC300001Y00**

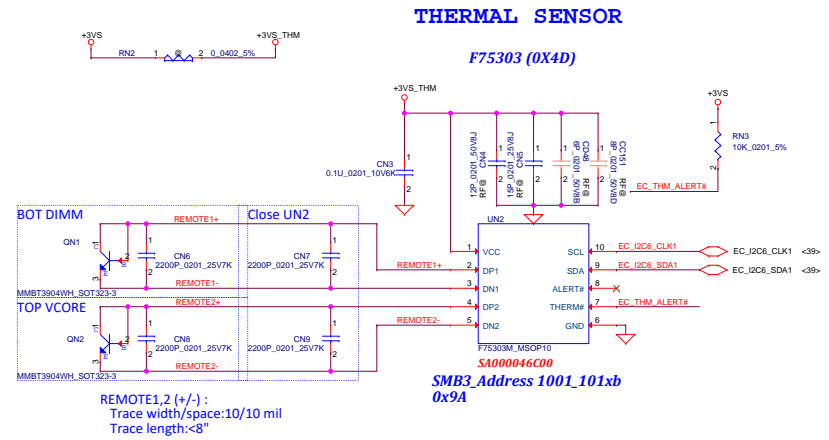
**Main Func = LED**

SC50000EI00  
Left TBT LED

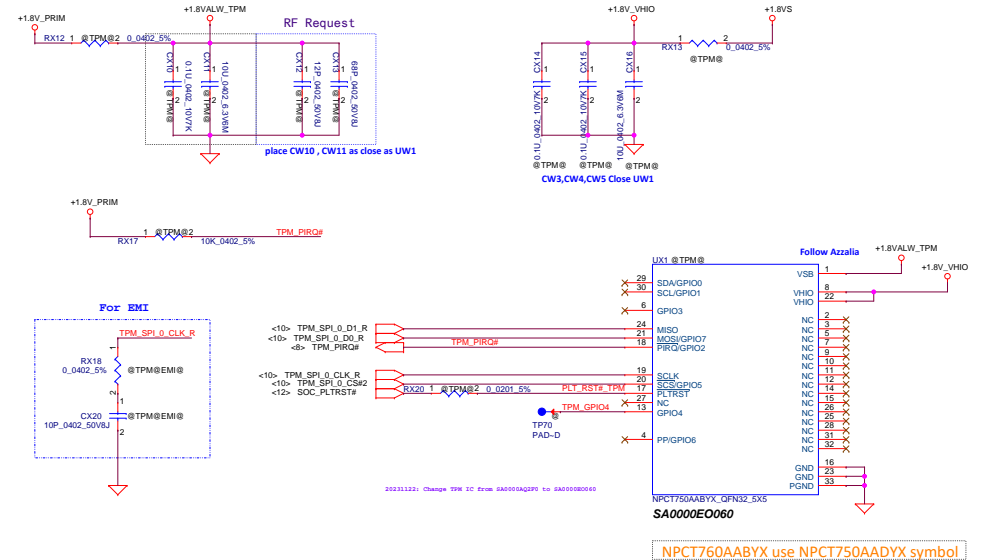


Right TBT LED  
SC50000EI00

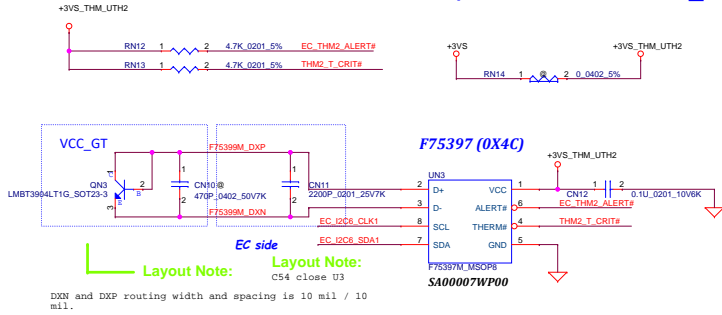




**TPM will change to NPCT750AADYX SA0000AQ2F0**



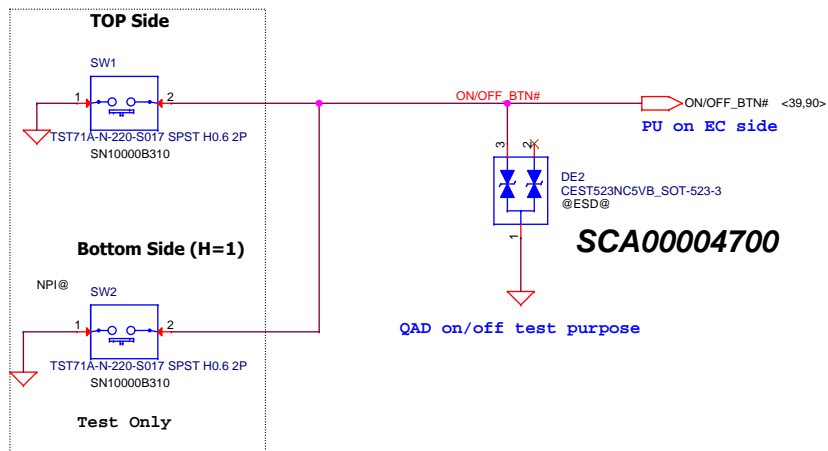
**THERMAL SENSOR (Reserve for VCC\_GT)**



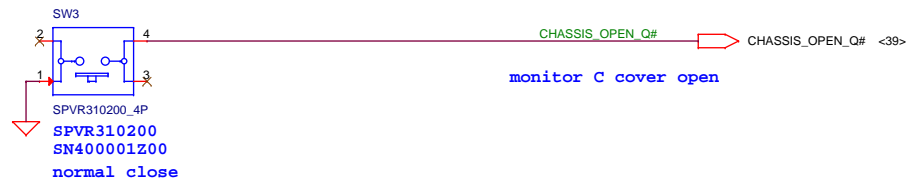
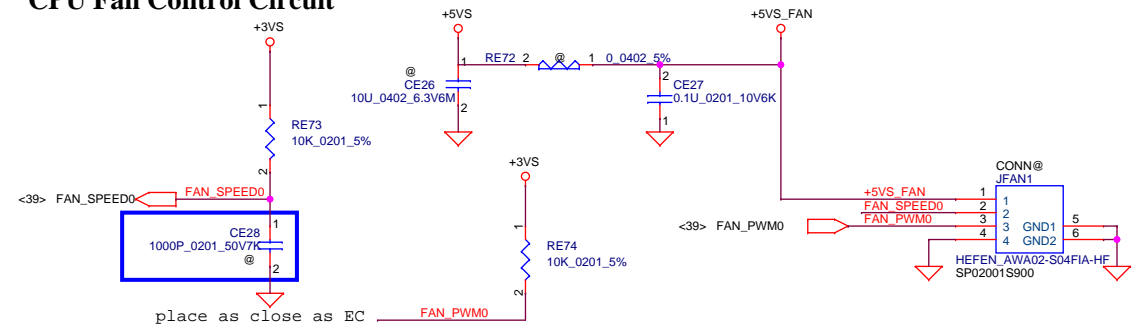


## Power Button

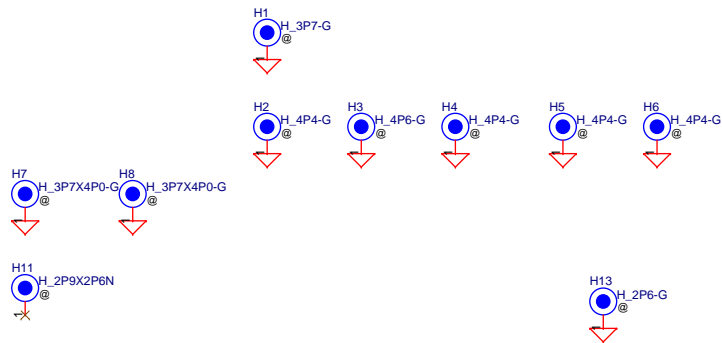
ON / OFF switch



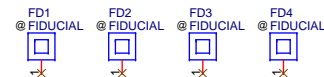
## CPU Fan Control Circuit



## SCREW



## Fiducial Mark



## Stand Off (Fan)

## Stand Off (SSD)

