Code Genation

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- MIPS has 32 registers:
 - t0, ..., t9
 - a0, a1, a3, a4
 - v0, v1
 - sp, fp
 - ra
 - •
- We will work with MIPS32
 - 32-bit registers

- Arithmetic instructions operate on registers and constants:
 - add, sub, and, mult, div, or, xor, nor, ...

```
li $t0, 3
li $t1, 4
add $t2, $t0, $t1
mul $t3, t1, 7
```

Read from memory:

```
lw $t0,$t1
lw $t0,2($t1)
lw $t0,label
lw $t0,label+4
lw $t0,label+8($t1)
```

• Write to memory:

```
sw $t0,$t1
sw $t0,2($t1)
sw $t0,label
sw $t0,label+4
sw $t0,label+8($t1)
```

Branches and Jumps:

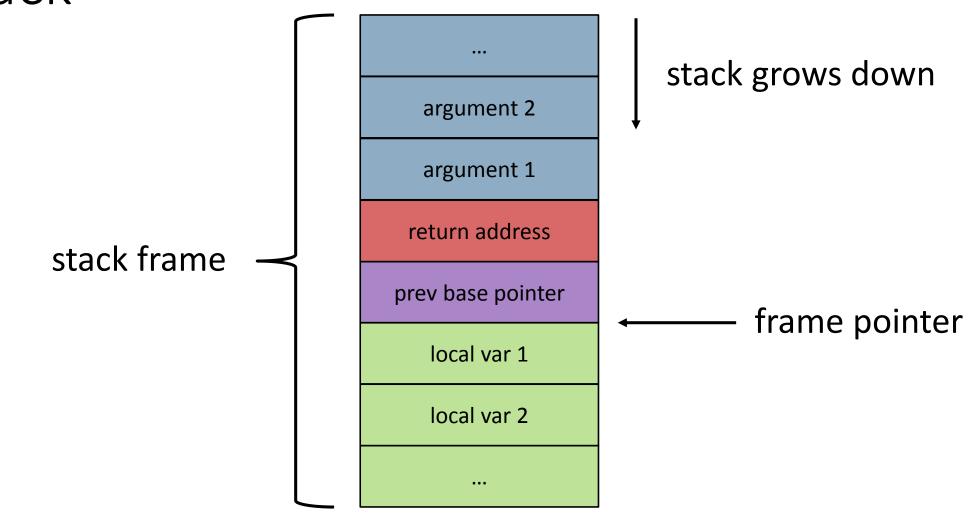
```
beq $t1, $t2, label
bne $t1, 7, label
j label
```

- System calls:
 - Syscall number passed via v0
 - Arguments are passed via a0, a1, a2, a3
- Calling PrintInt(17):

```
li $v0, 1
li $a0, 17
syscall
```

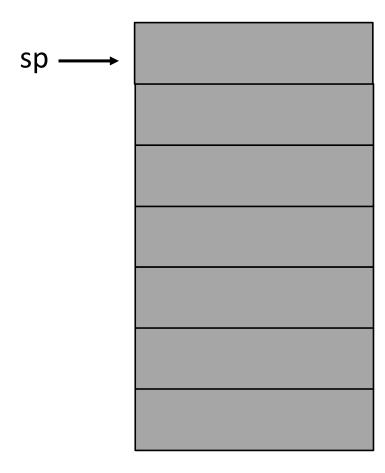
Stack Frames

- The stack consists of stack frame
- Each called function creates it's stack frame

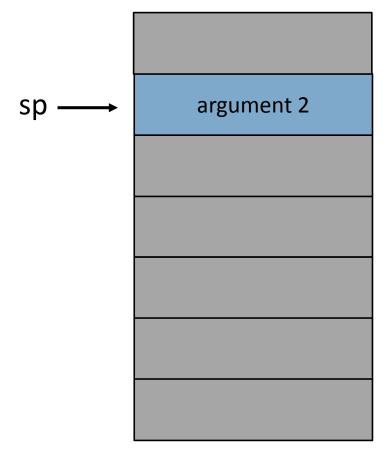


```
int f(int x, int y) {
   int z = x + y;
   return z;
}
int g() {
   int x = f(10, 20)
}
```

f: subu \$sp, \$sp, 4 sw \$ra, 0(\$sp) subu \$sp, \$sp, 4 sw \$fp, 0(\$sp) move \$fp, \$sp sub \$sp, \$sp, 16 lw \$t0, 8(\$fp) lw \$t1, 12(\$fp) add \$t2, \$t0, \$t1 sw \$t2, -4(\$fp)1w \$v0, -4 (\$fp)move \$sp, \$fp lw \$fp, 0(\$sp) lw \$ra, 4(\$sp) addu \$sp, \$sp, 8 jr \$ra



f: subu \$sp, \$sp, 4 sw \$ra, 0(\$sp) subu \$sp, \$sp, 4 sw \$fp, 0(\$sp) move \$fp, \$sp sub \$sp, \$sp, 16 lw \$t0, 8(\$fp) lw \$t1, 12(\$fp) sw \$t2, -4(\$fp)1w \$v0, -4 (\$fp)move \$sp, \$fp lw \$fp, 0(\$sp) lw \$ra, 4(\$sp) addu \$sp, \$sp, 8 jr \$ra



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argument 2 argument 1 return address sp

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argument 2 argument 1 return address sp previous fp

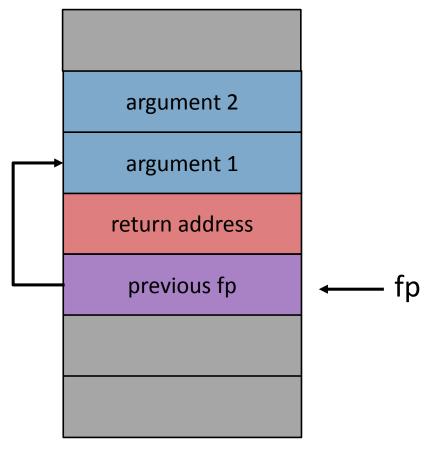
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argument 2 argument 1 return address sp previous fp fp

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argument 2 argument 1 return address previous fp

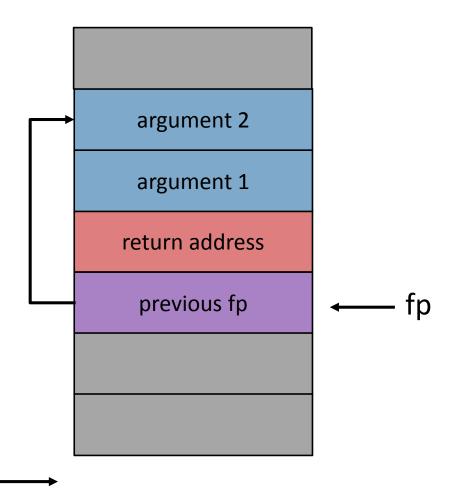
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```
f:
subu $sp, $sp, 4
sw $ra, 0($sp)
subu $sp, $sp, 4
sw $fp, 0($sp)
move $fp, $sp
sub $sp, $sp, 16
lw $t0, 8($fp)
lw $t1, 12($fp)
add $t2, $t0, $t1
sw $t2, -4($fp)
1w $v0, -4 ($fp)
move $sp, $fp
lw $fp, 0($sp)
lw $ra, 4($sp)
addu $sp, $sp, 8
jr $ra
```

```
g:
li $t0, 20
subu $sp, $sp, 4
sw $t0, 0($sp)
li $t0, 10
subu $sp, $sp, 4
sw $t0, 0($sp)
jal f
addu $sp, $sp, 8
move $t0, $v0
```

sp



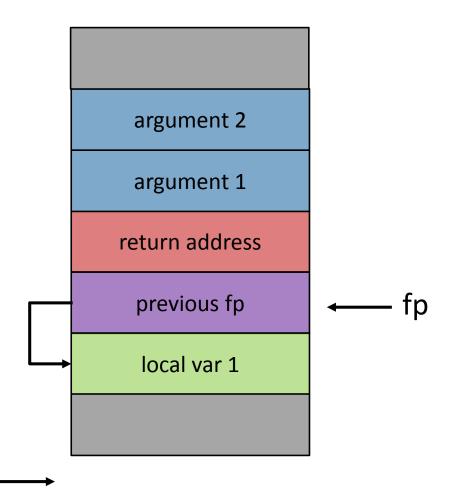
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argument 2 argument 1 return address previous fp fp local var 1

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Translating IR

Strings • TODO

Arrays • TODO

Runtime Errors

SPIM

Classes