


ESD_LAB

imt_2019015@iiitm.ac.in [Switch account](#)

 Draft saved

1. Answer the following:

1. In 8085 microprocessor, the value of the most significant bit of the result following the execution of any arithmetic or Boolean instruction is stored in the 1 point

- ☐ carry status flag
- ☐ auxiliary carry status flag
- ☒ sign status flag
- ☐ zero status flag

[Clear selection](#)

2. Programming language in which there is a very strong correspondence between the language and the architecture's machine code instructions is termed as 1 point

- ☐ machine language
- ☒ assembly language
- ☐ high level language
- ☐ medium language

[Clear selection](#)

3. Assembly language programs are written using 1 point

- ☐ Hex code
- ☒ Mnemonics
- ☐ ASCII code
- ☐ None of these

[Clear selection](#)

4.The instructions like MOV or ADD are called as _____ 1 point

- ☒ OP-Code
- ☐ Operators
- ☐ Commands
- ☐ None of the mentioned

[Clear selection](#)

5.. DEBUG is able to troubleshoot only 1 point

- ☒ .EXE files
- ☐ .OBJ files
- ☐ .EXE file and .OBJ file
- ☐ .EXE file and .LST file

[Clear selection](#)

6.The last statement of the source program should be _____ 1 point

- ☐ Stop
- ☐ Return
- ☐ OP
- ☒ End

Clear selection

7. A machine language instruction format consists of

1 point

- ☐ Operand field
- ☐ Operation code field
- ☒ Operation code field & operand field
- ☐ None of the mentioned

Clear selection

8. What is the content of Stack Pointer?

1 point

- ☐ Address of the current instruction
- ☐ Address of the next instruction
- ☒ Address of the top element of the stack
- ☐ None of these

Clear selection

9. The instruction format 'register to register' has a length of

1 point

- ☒ 2 bytes
- ☐ 1 byte
- ☐ 3 bytes
- ☐ 4 bytes

Clear selection

10. Translator for low level programming language were termed as

1 point

- ☒ Assembler
- ☐ Compiler
- ☐ Linker
- ☐ Loader

Clear selection

11. What is 01100101b XOR 11010110b?

1 point

- ☐ 01000100b
- ☐ 11110111b
- ☒ 10110011b
- ☐ 01001101b

Clear selection

12. In 8051 which is/are the 16 bit registers?

1 point

- ☐ Program Counter
- ☐ Stack Pointer
- ☒ Both [A] & [B]
- ☐ None of these

Clear selection

13. 8051 series has how many 16 bit registers?

1 point

- ☒ 2
- ☐ 3

- ☐ 1
- ☐ 0

Clear selection

14. When 8051 wakes up then 0x00 is loaded to which register?

1 point

- ☒ SP
- ☐ PSW
- ☐ DPTR
- ☐ PC

Clear selection

15. The instruction DEC N inform the assembler to

1 point

- ☒ The instruction DEC N inform the assembler to
- ☐ Decrement the data addressed by N
- ☐ Convert signed decimal number to binary
- ☐ none of these

Clear selection

16. In 8085 microprocessor, the value of the most significant bit of the result following the execution of any arithmetic or Boolean instruction is stored in the

1 point

- ☐ carry status flag
- ☐ auxiliary carry status flag
- ☒ sign status flag
- ☐ zero status flag

Clear selection

17. Instructions performing actions in assembly language are called

1 point

- ☒ imperative statements
- ☐ declarative statements
- ☐ Option 3
- ☐ none of the above

Clear selection

18. If we push data onto the stack then the stack pointer

1 point

- ☒ increases with every push
- ☐ decreases with every push
- ☐ increases & decreases with every push
- ☐ none of the mentioned

Clear selection

19. Number of machine cycles required for RET instruction in 8051

1 point

- ☐ 1
- ☐ 2
- ☒ 3
- ☐ 5

Clear selection

20. The operations performed by data transfer instructions are on

1 point

- ☐ bit data
- ☐ byte data
- ☐ 16-bit data
- ☒ all of the mentioned

Clear selection

Back

Submit

Clear form

Never submit passwords through Google Forms.

This form was created outside of your domain. [Report Abuse](#) - [Terms of Service](#) - [Privacy Policy](#)

Google Forms

