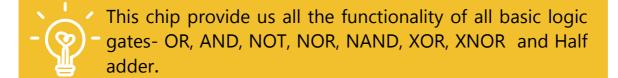
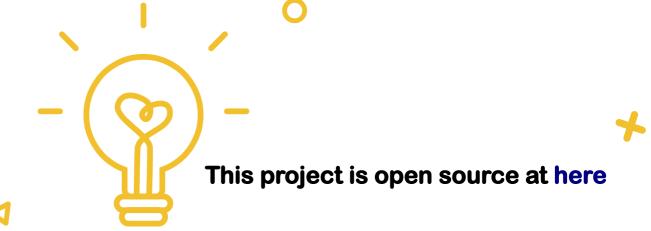


BAR2308TT

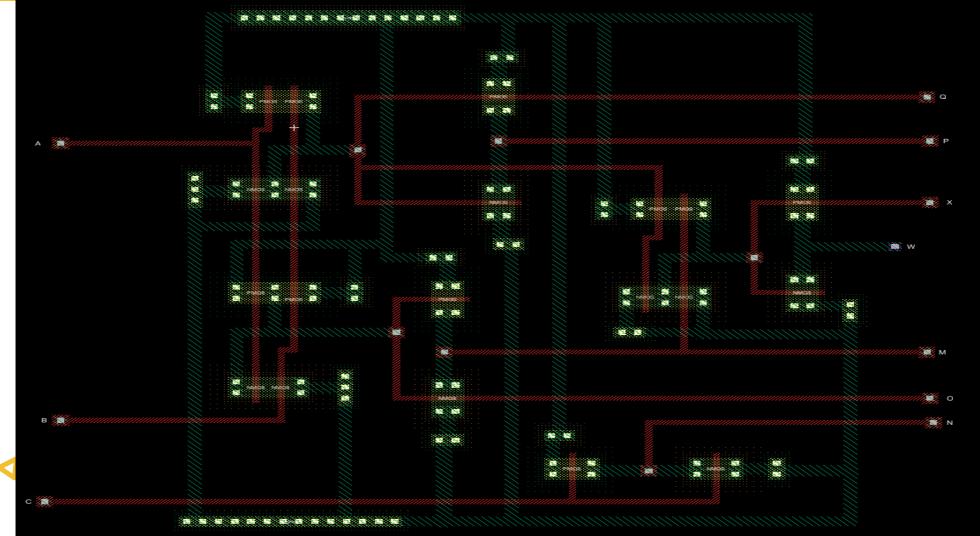
A SSI chip with all the functionality of all basic Logic gates



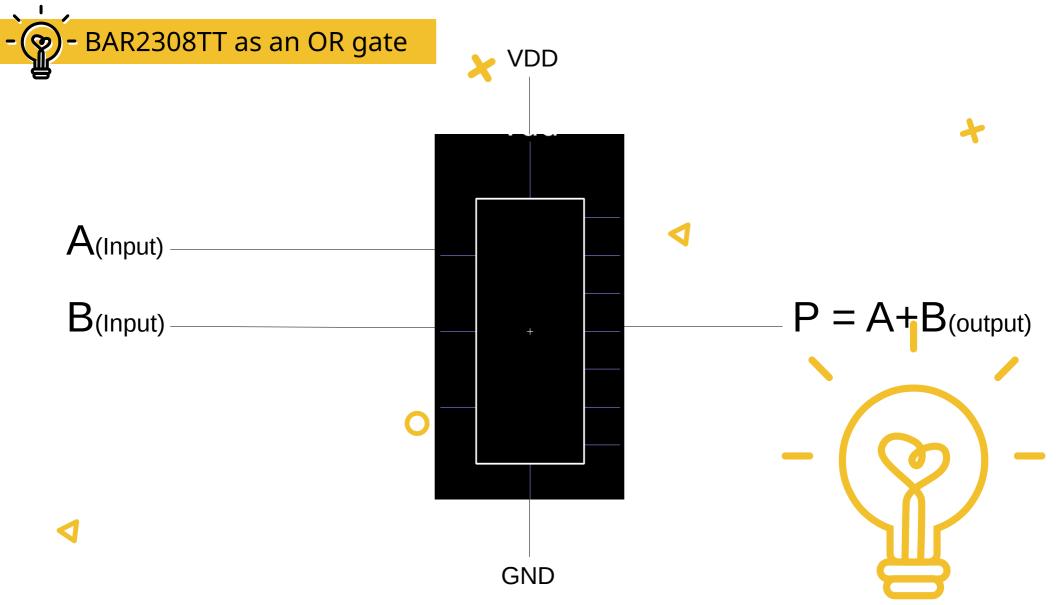




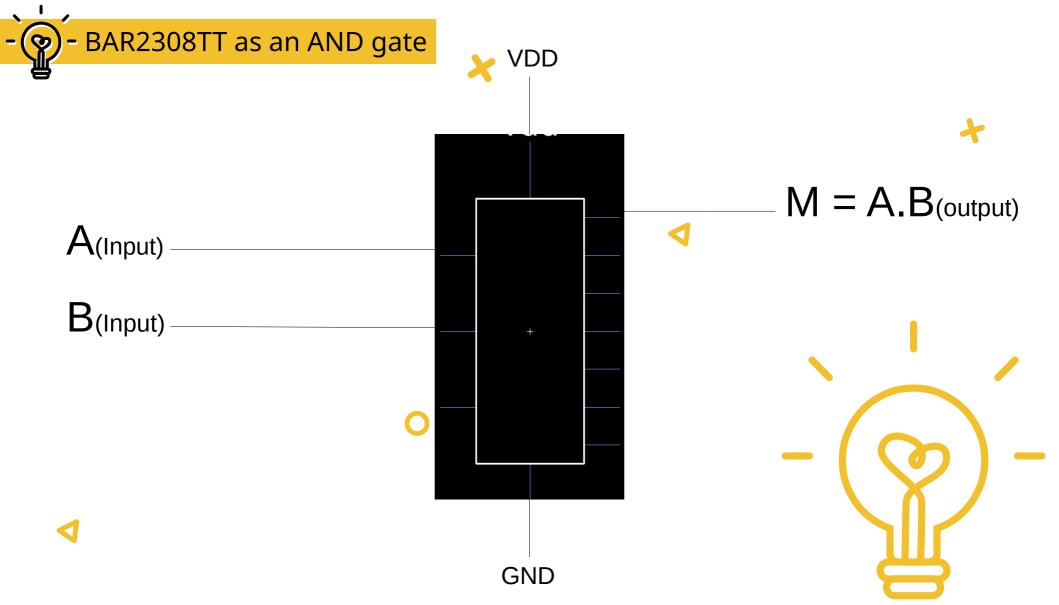
- Icon of BAR2308TT vdd M A В gnd **BAR2308TT**



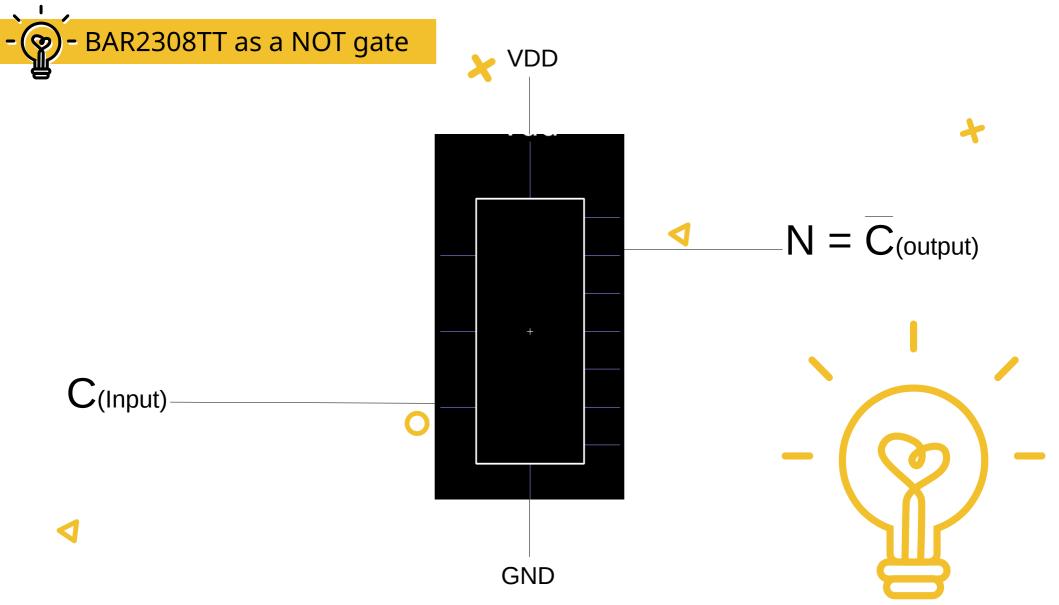




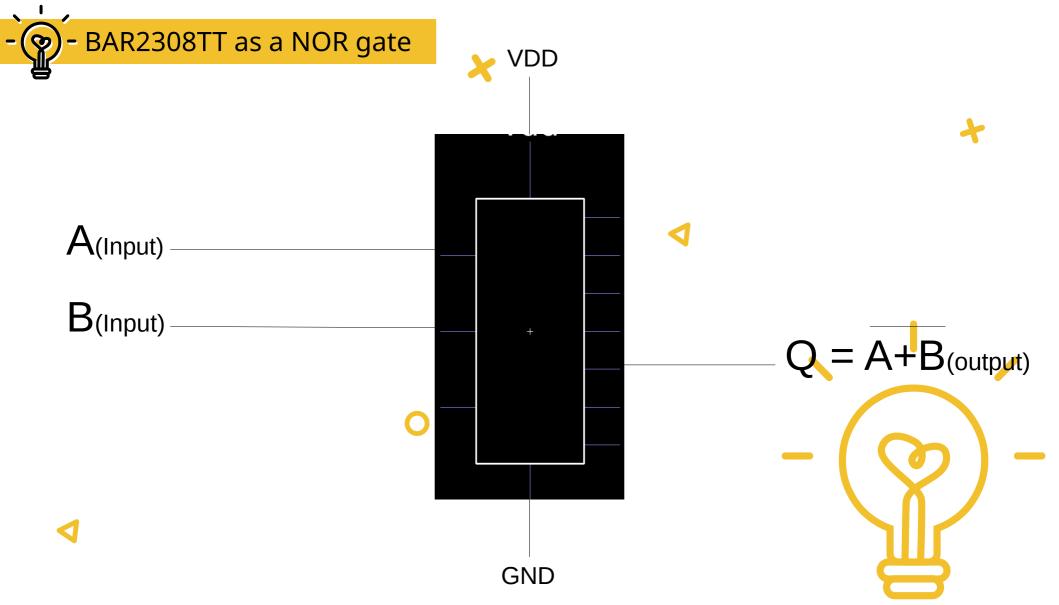




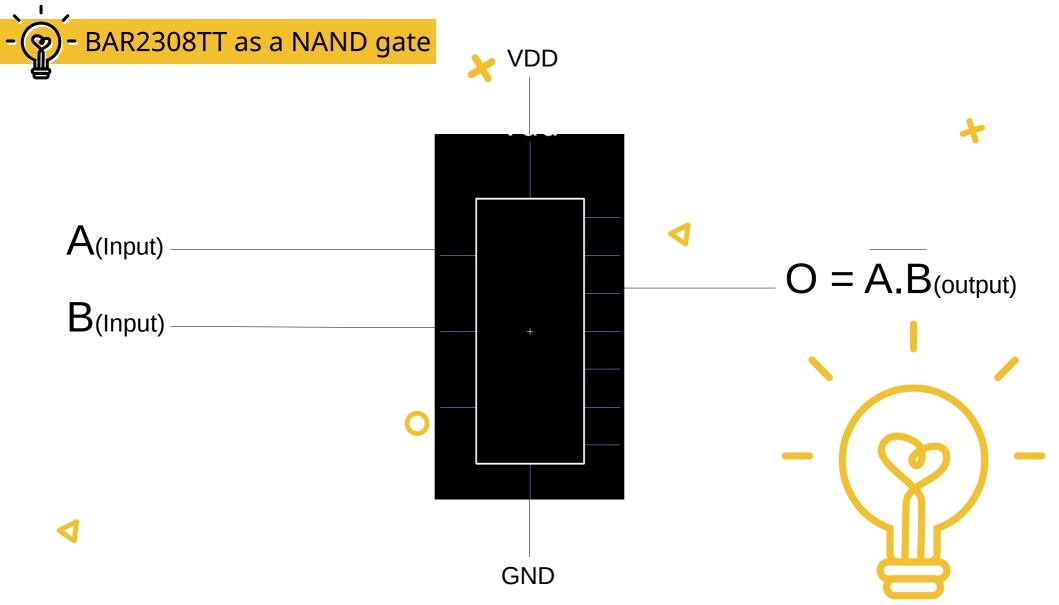


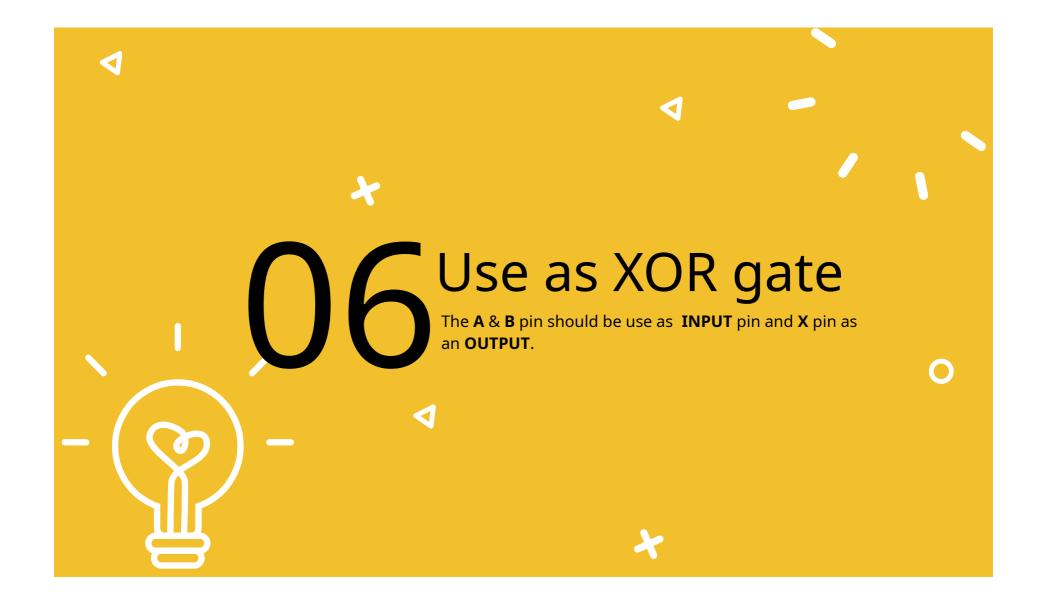


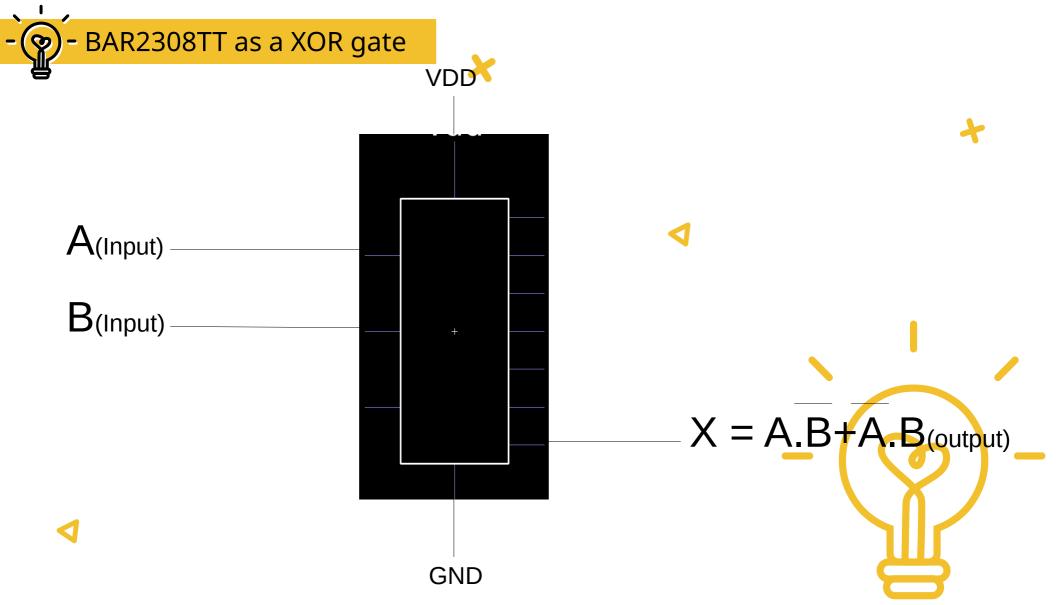




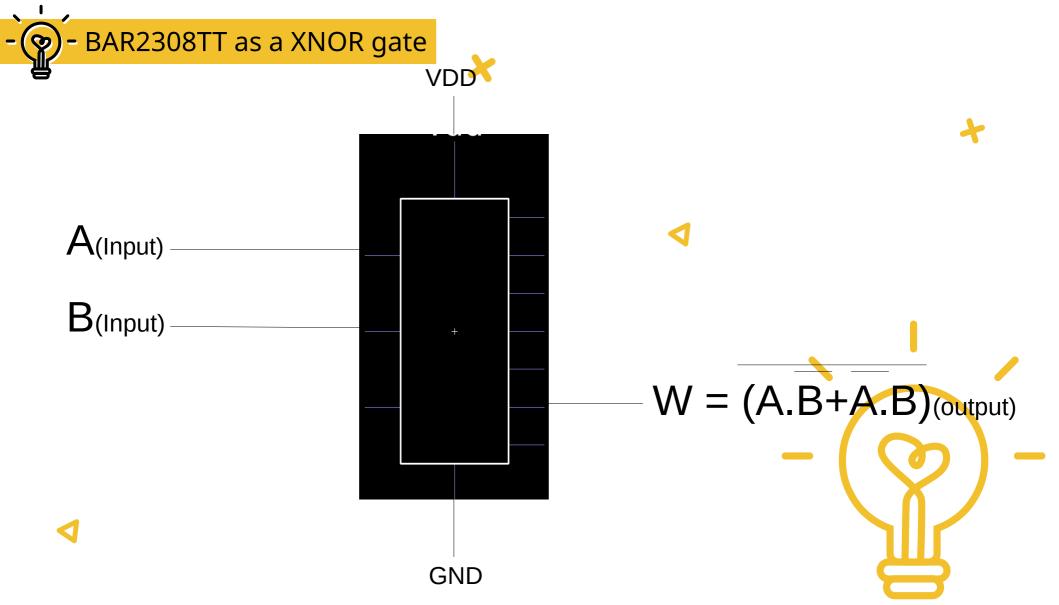


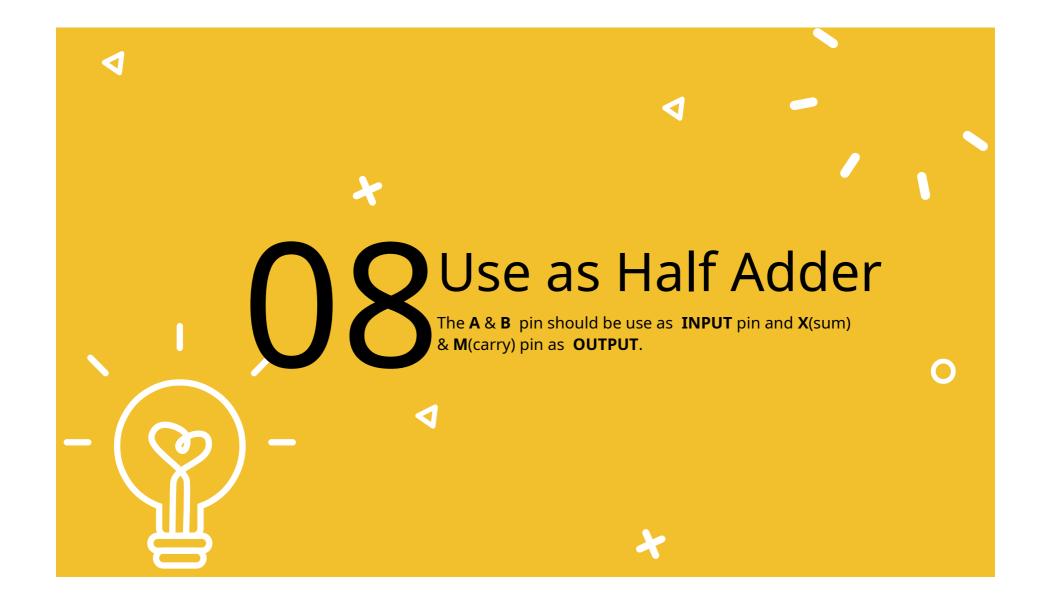


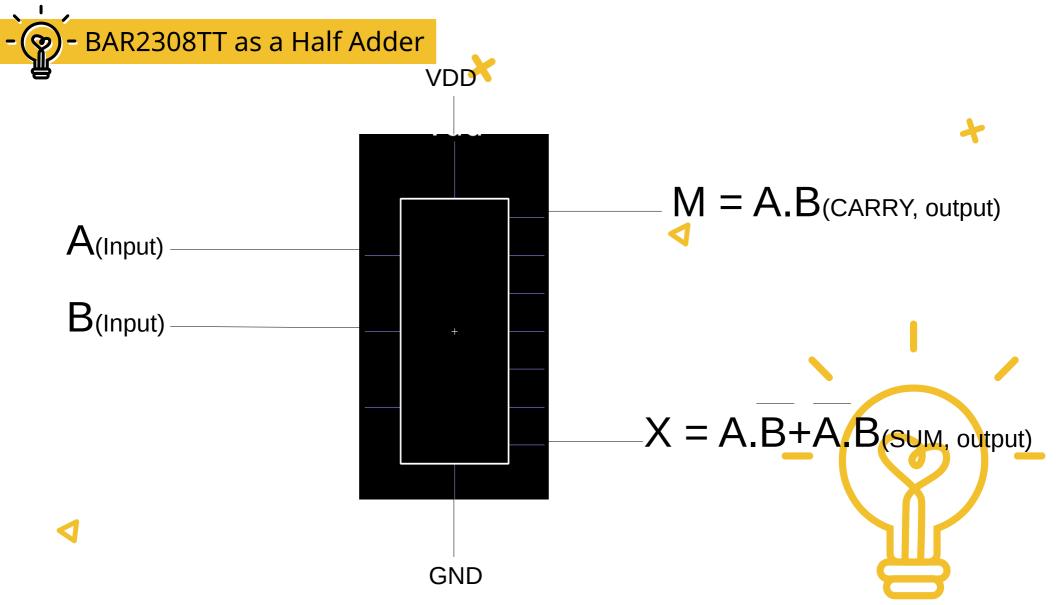
















SIMPLE DESIGN

This chip layout is Simple design of CMOS. I used here 20 CMOS and connected there as per equation. There is no resistors, inductors, capacitors etc.





02

FUNCTIONALITY

Functionality of this IC chip (**BAR2308TT**) is very much important. One can use it as similar as any of the Logic gates – OR, AND, NOT, NOR, NAND, XOR, XNOR. He/She can also use it as Half adder.



COMPUTING POWER

Using as a Half adder one can also be able to make a computer(4-bit,8-bit). For that reason, one must be needed multiples **BAR2308TT** IC chips and needed to add as per equation.



C



OPEN SOURCE

It is an Open Source project. Although, I am going to create a License for it, but it will always for public use. Anyone can use it in their projects.

Equations

- P = A + B= (A+B)"P= Q'
 - M = A.B = (A.B)''M = O'
- > N = C'
- P = (A+B)'
- > O = (A.B)'
- X = A.B' + A'.B
 - = (A.B' + A'.B)"= ((A.B')'.(A'.B)')'
 - = ((A'+B).(A+B'))'
 - = (A.A'+A.B+A'.B'+B.B')'= (A.B+A'.B')'
- X = (A.B+(A+B)')'









- Equations

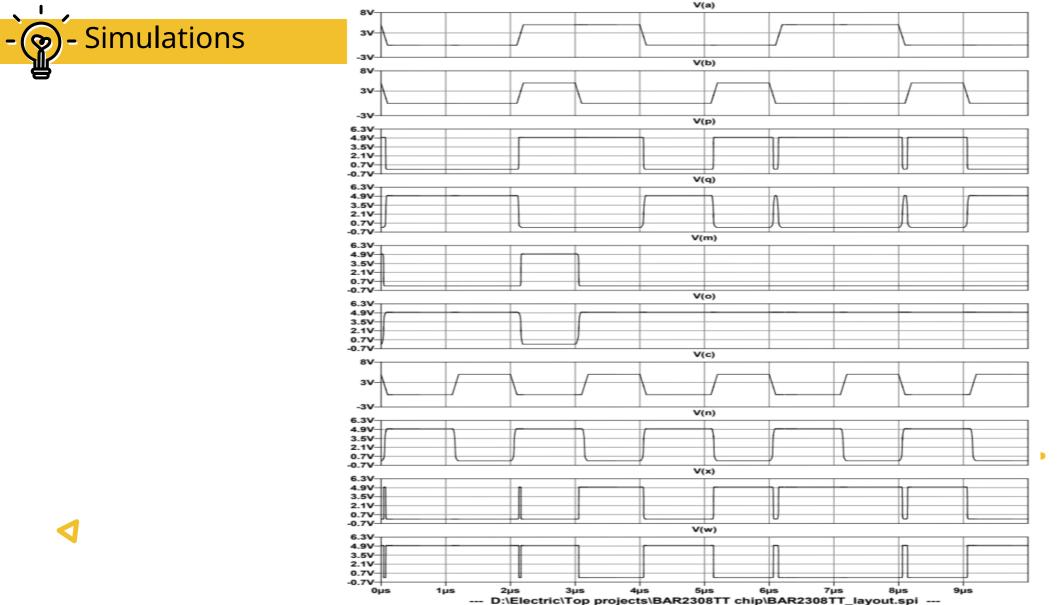












- Usability





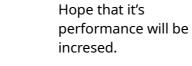








This will generate many ideas in future.One can use it "All logic gates in one IC chips".

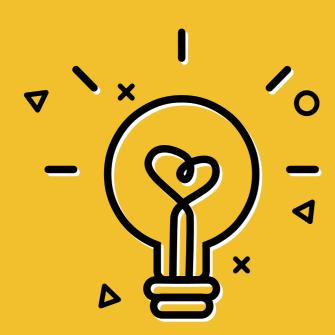


Yeah, one can definitely share it with anyone.

Due to open source project, It will open to all. Anyone can use it as base chip. It's layout should not be changed. I have create a license for it.







THANK YOU

AMIT BARMAN

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